

**DESCRIPTION**

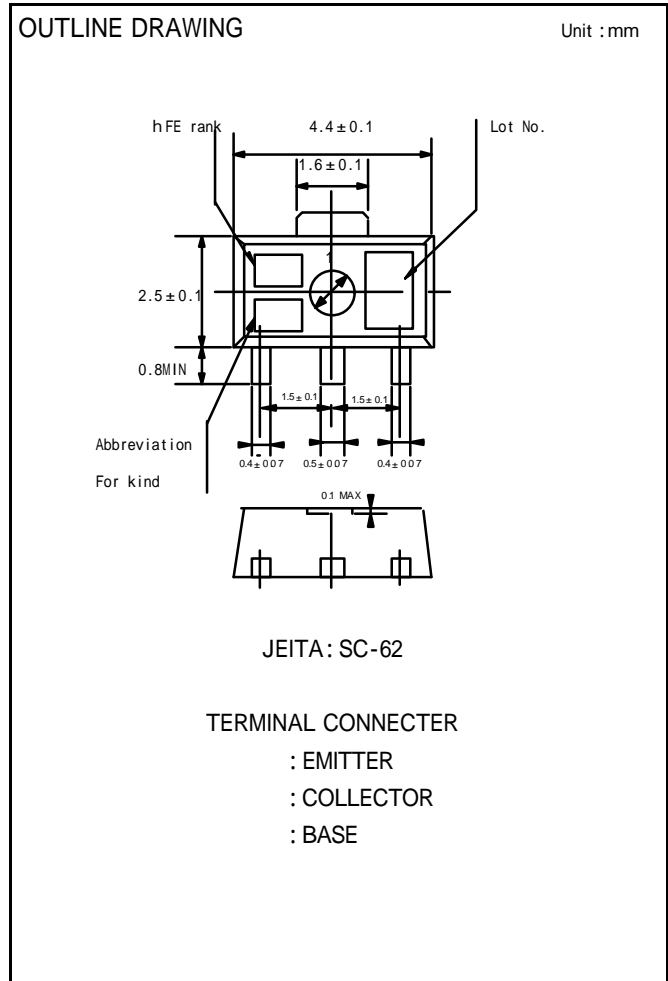
2SA2027 is a super mini package resin sealed silicon PNP epitaxial transistor, It is designed for high voltage application.

**FEATURE**

Small collector to emitter saturation voltage.  
VCE(sat)=-0.5V max

**APPLICATION**

For Hybrid IC, DC-DC converter



**MAXIMUM RATINGS**(Ta=25 )

Symbol	Parameter	Ratings	Unit
V <sub>CBO</sub>	Collector to Base voltage	-300	V
V <sub>CEO</sub>	Collector to Emitter voltage	-300	V
V <sub>EBO</sub>	Emitter to Base voltage	-7	V
I <sub>O</sub>	Collector current	-100	mA
P <sub>c</sub>	Collector dissipation	500	mW
T <sub>j</sub>	Junction temperature	+ 150	
T <sub>stg</sub>	Storage temperature	-55 ~ + 150	

**ELECTRICAL CHARACTERISTICS** ( Ta=25 )

Parameter	Symbol	Test conditions	Limits			Unit
			Min	Typ	Max	
C to B break down voltage	V(BR) <sub>CBO</sub>	I <sub>C</sub> =-50 μA, I <sub>E</sub> =0	-300	-	-	V
E to B break down voltage	V(BR) <sub>EBO</sub>	I <sub>E</sub> =-50 μA, I <sub>C</sub> =0	-7	-	-	V
C to E break down voltage	V(BR) <sub>CEO</sub>	I <sub>C</sub> =-1mA, R <sub>BE</sub> =	-300	-	-	V
Collector cut off current	I <sub>CBO</sub>	V <sub>CB</sub> =-300V, I <sub>E</sub> =0mA	-	-	-0.5	μA
Emitter cut off current	I <sub>EBO</sub>	V <sub>EB</sub> =-5V, I <sub>C</sub> =0mA	-	-	-0.5	μA
DC forward current gain	hFE	V <sub>CE</sub> =-10V, I <sub>C</sub> =-10mA	50	-	305	
C to E Saturation Voltage	VCE(sat)	I <sub>C</sub> =-100mA, I <sub>B</sub> =-10mA	-	-	-0.5	V
Gain bandwidth product	fT	V <sub>CE</sub> =-6V, I <sub>E</sub> =10mA	-	40	-	MHz
Collector output capacitance	C <sub>ob</sub>	V <sub>CE</sub> =-6V, I <sub>C</sub> =-10mA	-	2.0	-	pF



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