



# DIM250PLM33-TS000

# **IGBT Chopper Module**

DS6108-2 July 2013 (LN30764)

# **FEATURES**

Replaces DS6108-1

- 10µs Short Circuit Withstand
- High Thermal Cycling Capability
- High Current Density Enhanced DMOS SPT
- Isolated AISiC Base with AIN Substrates

#### **APPLICATIONS**

- Choppers
- Motor Controllers
- Power Supplies
- Traction Auxiliaries

The Powerline range of high power modules includes half bridge, chopper, dual, single and bi-directional switch configurations covering voltages from 1200V to 6500V and currents up to 2400A.

The DIM250PLM33-TS000 is a 3300V, n-channel enhancement mode, insulated gate bipolar transistor (IGBT) chopper module configured with the lower arm of the bridge controlled. The IGBT has a wide reverse bias safe operating area (RBSOA). This device is optimised for traction drives and other applications requiring high thermal cycling capability.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

## **ORDERING INFORMATION**

Order As:

## DIM250PLM33-TS000

Note: When ordering, please use the complete part number

### **KEY PARAMETERS**

$V_{CES}$		3300V
V <sub>CE(sat)</sub>	* (typ)	2.2V
I <sub>C</sub>	(max)	250A
I <sub>C(PK)</sub>	(max)	500A

<sup>\*</sup> Measured at the auxiliary terminals

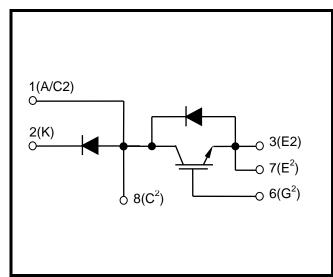


Fig. 1 Circuit configuration

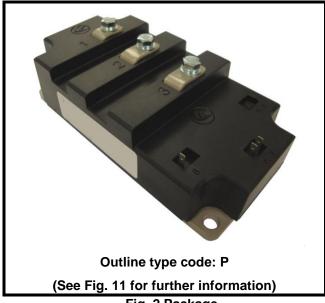


Fig. 2 Package



## **ABSOLUTE MAXIMUM RATINGS**

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

T<sub>case</sub> = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions		Units
V <sub>CES</sub>	Collector-emitter voltage	V <sub>GE</sub> = 0V	3300	V
V <sub>GES</sub>	Gate-emitter voltage		±20	V
I <sub>C</sub>	Continuous collector current	T <sub>case</sub> = 110°C	250	Α
I <sub>C(PK)</sub>	Peak collector current	1ms, T <sub>case</sub> = 140°C	500	Α
P <sub>max</sub>	Max. transistor power dissipation	$T_{case} = 25^{\circ}C, T_{j} = 150^{\circ}C$	2.6	kW
l <sup>2</sup> t	Diode I <sup>2</sup> t value – IGBT Arm	$V_R = 0$ , $t_0 = 10$ ms, $T_i = 150$ °C		kA <sup>2</sup> s
''	Diode I <sup>2</sup> t value – Diode Arm			kA <sup>2</sup> s
V <sub>isol</sub>	Isolation voltage – per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	6000	V
$Q_{PD}$	Partial discharge – per module	IEC1287, $V_1 = 3500V$ , $V_2 = 2600V$ , 50Hz RMS	10	рС

## THERMAL AND MECHANICAL RATINGS

Internal insulation material:

Baseplate material:

Creepage distance:

Clearance:

CTI (Comparative Tracking Index):

AIN

AISiC

33mm

20mm

>600

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
R <sub>th(j-c)</sub>	Thermal resistance – transistor	Continuous dissipation - junction to case	-	-	48	°C/kW
ь	Thermal resistance – diode (IGBT Arm)	Continuous dissipation - junction to case	-	-	96	°C/kW
R <sub>th(j-c)</sub>	Thermal resistance – diode (Diode Arm)		-	-	96	°C/kW
R <sub>th(c-h)</sub>	Thermal resistance – case to heatsink (per module)	Mounting torque 5Nm (with mounting grease)	-	-	16	°C/kW
_	Junction temperature	Transistor	-	-	150	°C
T <sub>j</sub>		Diode	-	-	150	°C
T <sub>stg</sub>	Storage temperature range	-	-40	-	125	°C
	Screw torque	Mounting – M6	-	-	5	Nm
		Electrical connections – M5	-	-	4	Nm



## **ELECTRICAL CHARACTERISTICS**

 $T_{case}$  = 25°C unless stated otherwise.

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
	Collector cut-off current	$V_{GE} = 0V$ , $V_{CE} = V_{CES}$			1	mA
I <sub>CES</sub>		$V_{GE} = 0V$ , $V_{CE} = V_{CES}$ , $T_{case} = 125$ °C			15	mA
		$V_{GE} = 0V$ , $V_{CE} = V_{CES}$ , $T_{case} = 150$ °C			25	mA
I <sub>GES</sub>	Gate leakage current	$V_{GE} = \pm 20V, V_{CE} = 0V$			1	μA
V <sub>GE(TH)</sub>	Gate threshold voltage	$I_C = 40$ mA, $V_{GE} = V_{CE}$		5.7		V
		V <sub>GE</sub> = 15V, I <sub>C</sub> = 250A		2.2		V
$V_{CE(sat)}^{\dagger}$	Collector-emitter saturation voltage	$V_{GE} = 15V, I_C = 250A, T_j = 125^{\circ}C$		2.8		V
	g-	$V_{GE} = 15V$ , $I_C = 250A$ , $T_j = 150$ °C		3.0		V
I <sub>F</sub>	Diode forward current	DC		250		Α
I <sub>FM</sub>	Diode maximum forward current	$t_p = 1 ms$		500		А
	Diode forward voltage † (IGBT arm) Diode forward voltage ‡ (Diode arm)	I <sub>F</sub> = 250A		2.4		V
				2.5		V
	Diode arm)  Diode forward voltage † (IGBT arm)  Diode forward voltage ‡ (Diode arm)			2.5		V
$V_{F}$		$I_F = 250A, T_j = 125^{\circ}C$		2.6		V
	Diode arm)  Diode forward voltage †  (IGBT arm)  Diode forward voltage ‡  (Diode arm)	I <sub>F</sub> = 250A, T <sub>j</sub> = 150°C		2.4		V
				2.5		V
C <sub>ies</sub>	Input capacitance	$V_{CE} = 25V, V_{GE} = 0V, f = 1MHz$		45		nF
$Q_g$	Gate charge	±15V Including external C <sub>ge</sub>		5		μC
C <sub>res</sub>	Reverse transfer capacitance	V <sub>CE</sub> = 25V, V <sub>GE</sub> = 0V, f = 1MHz		1		nF
L <sub>M</sub>	Module inductance			40		nΗ
R <sub>INT</sub>	Internal transistor resistance			500		μΩ
SC <sub>Data</sub>	Short circuit current, I <sub>SC</sub>	$T_{j} = 150^{\circ}\text{C}, \ V_{CC} = 2500\text{V}$ $t_{p} \le 10\mu\text{s}, \ V_{GE} \le 15\text{V}$ $V_{CE \ (max)} = V_{CES} - L^{*} x \ dI/dt$ IEC 60747-9		950		А

<sup>†</sup> Measured at the auxiliary terminals † Measured at the power busbars

<sup>\*</sup> L is the circuit inductance  $+ L_M$ 



## **ELECTRICAL CHARACTERISTICS**

T<sub>case</sub> = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
t <sub>d(off)</sub>	Turn-off delay time	I <sub>C</sub> = 250A		2700		ns
$t_f$	Fall time	$V_{GE} = \pm 15V$		520		ns
E <sub>OFF</sub>	Turn-off energy loss	$V_{CE} = 1800V$		480		mJ
t <sub>d(on)</sub>	Turn-on delay time	$\begin{aligned} R_{g(ON)} &= 10\Omega \\ R_{g(OFF)} &= 10\Omega \\ C_{GE} &= 56 nF \\ L_S &\sim 150 nH \end{aligned}$		1000		ns
t <sub>r</sub>	Rise time			400		ns
E <sub>ON</sub>	Turn-on energy loss			320		mJ
Q <sub>rr</sub>	Diode reverse recovery charge	I <sub>F</sub> = 250A		180		μC
I <sub>rr</sub>	Diode reverse recovery current	$V_{CE} = 1800V$		160		Α
E <sub>rec</sub>	Diode reverse recovery energy	dI <sub>F</sub> /dt = 700A/μs		165		mJ

## T<sub>case</sub> = 125°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
t <sub>d(off)</sub>	Turn-off delay time	I <sub>C</sub> = 250A		2750		ns
t <sub>f</sub>	Fall time	$V_{GE} = \pm 15V$		570		ns
E <sub>OFF</sub>	Turn-off energy loss	$V_{CE} = 1800V$		540		mJ
t <sub>d(on)</sub>	Turn-on delay time	$R_{g(ON)} = 10\Omega$ $R_{g(OFF)} = 10\Omega$		1020		ns
t <sub>r</sub>	Rise time	$C_{GE} = 56nF$		420		ns
E <sub>ON</sub>	Turn-on energy loss	L <sub>S</sub> ~ 150nH		420		mJ
$Q_{rr}$	Diode reverse recovery charge	I <sub>F</sub> = 250A		230		μC
I <sub>rr</sub>	Diode reverse recovery current	V <sub>CE</sub> = 1800V		200		Α
E <sub>rec</sub>	Diode reverse recovery energy	dI <sub>F</sub> /dt = 700A/μs		280		mJ

# $T_{case}$ = 150°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
t <sub>d(off)</sub>	Turn-off delay time	I <sub>C</sub> = 250A		2800		ns
t <sub>f</sub>	Fall time	$V_{GE} = \pm 15V$		550		ns
E <sub>OFF</sub>	Turn-off energy loss	$V_{CE} = 1800V$		580		mJ
t <sub>d(on)</sub>	Turn-on delay time	$R_{g(ON)} = 10\Omega$ $R_{g(OFF)} = 10\Omega$		1030		ns
t <sub>r</sub>	Rise time	$C_{GE} = 56nF$		430		ns
E <sub>ON</sub>	Turn-on energy loss	L <sub>S</sub> ~ 150nH		460		mJ
$Q_{rr}$	Diode reverse recovery charge	I <sub>F</sub> = 250A		270		μC
I <sub>rr</sub>	Diode reverse recovery current	V <sub>CE</sub> = 1800V		200		Α
E <sub>rec</sub>	Diode reverse recovery energy	dI <sub>F</sub> /dt = 700A/μs		330		mJ



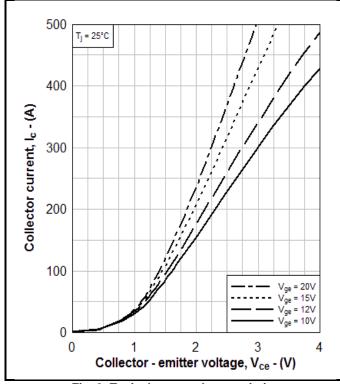


Fig. 3 Typical output characteristics

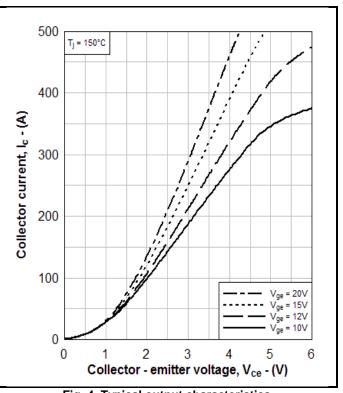


Fig. 4 Typical output characteristics

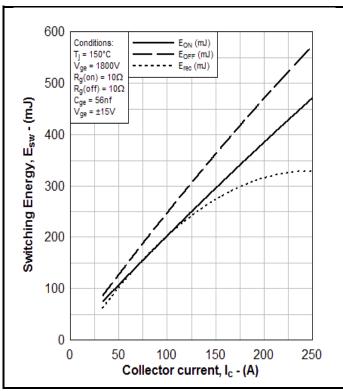


Fig. 5 Typical switching energy vs collector current

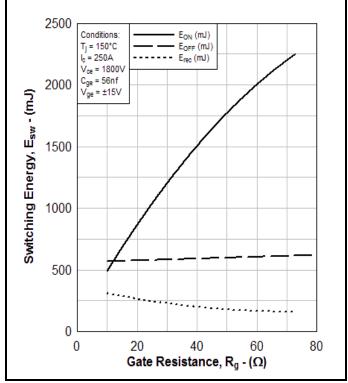


Fig. 6 Typical switching energy vs gate resistance



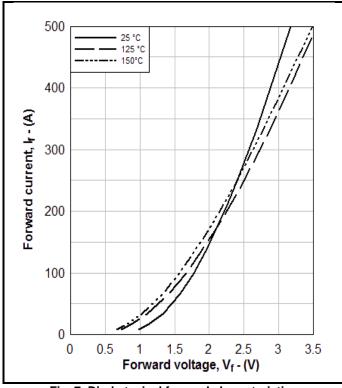


Fig. 7 Diode typical forward characteristics

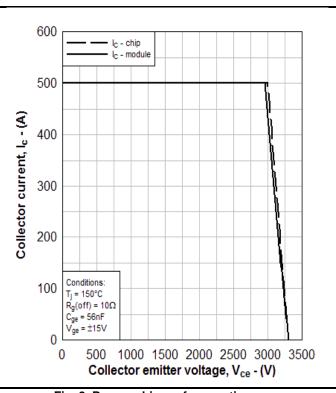


Fig. 8 Reverse bias safe operating area

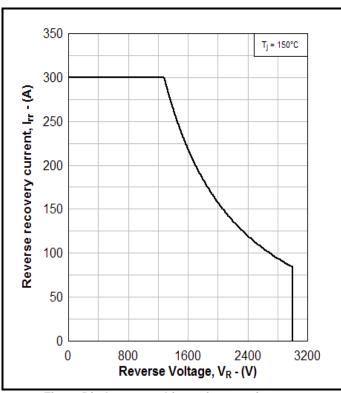


Fig. 9 Diode reverse bias safe operating area

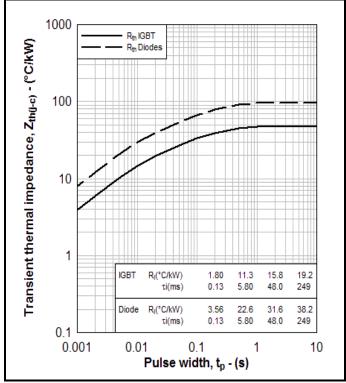


Fig. 10 Transient thermal impedance



## **PACKAGE DETAILS**

For further package information, please visit our website or contact Customer Services. All dimensions in mm, unless stated otherwise.

## DO NOT SCALE.

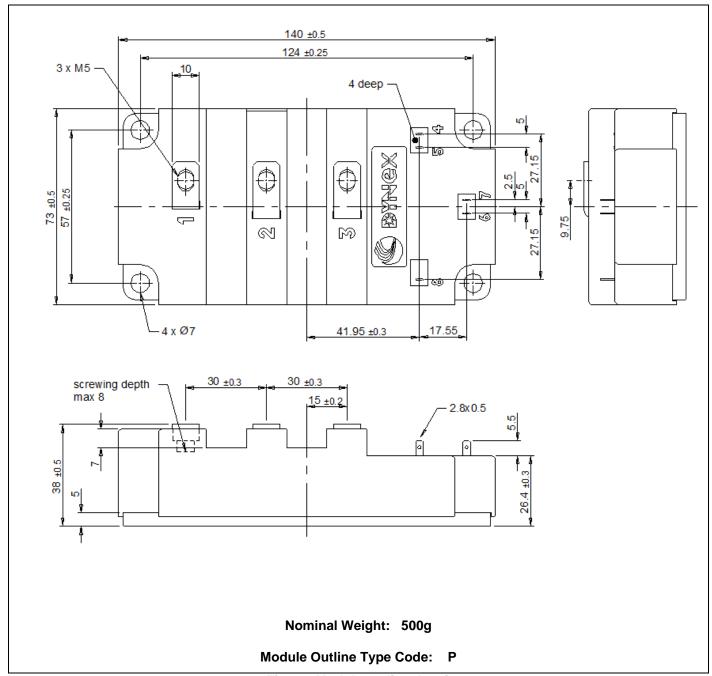


Fig. 11 Module outline drawing



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