TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type ( $\pi$ -MOSIV)

# 2SK3880

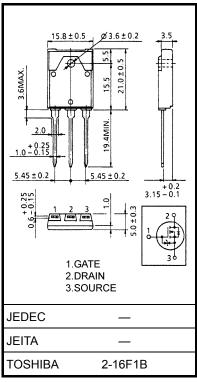
## **Switching Regulator Applications**

Unit: mm

- Low drain-source ON-resistance:  $R_{DS (ON)} = 1.35 \Omega (typ.)$
- High forward transfer admittance: |Y<sub>fS</sub>| = 5.2 S (typ.)
- Low leakage current:  $I_{DSS} = 100 \mu A \text{ (max) (V}_{DS} = 640 \text{ V)}$
- Enhancement model:  $V_{th}$  = 2.0 to 4.0 V ( $V_{DS}$  = 10 V,  $I_D$  = 1 mA)

### **Absolute Maximum Ratings (Ta = 25°C)**

Characteristics		Symbol	Rating	Unit		
Drain-source voltage		$V_{DSS}$	800	V		
Drain-gate voltage ( $R_{GS} = 20 \text{ k}\Omega$ )			$V_{DGR}$	800	V	
Gate-source voltage			V <sub>GSS</sub>	±30	V	
Drain current	DC (N	ote 1)	ΙD	6.5	Α	
	Pulse (N	ote 1)	$I_{DP}$	19.5		
Drain power dissipation (Tc = 25°C)			$P_{D}$	80	W	
Single pulse avalanche energy (Note 2)			E <sub>AS</sub>	375	mJ	
Avalanche current			I <sub>AR</sub>	6.5	Α	
Repetitive avalanche energy (Note 3)			E <sub>AR</sub>	8	mJ	
Channel temperature			T <sub>ch</sub>	150	°C	
Storage temperature range			T <sub>stg</sub>	-55 to 150	°C	

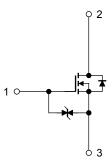


Weight: 5.8 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	1.56	°C/W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	41.6	°C/W



- Note 1: Ensure that the channel temperature does not exceed 150°C during use of the device.
- Note 2:  $V_{DD} = 90~V,~T_{ch} = 25^{\circ}C$  (initial), L = 16.1 mH, R<sub>G</sub> = 25  $\Omega,~I_{AR} = 6.5~A$
- Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.

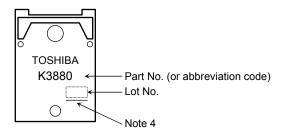
# **Electrical Characteristics (Ta = 25°C)**

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	Gate leakage current $I_{GSS}$ $V_{GS} = \pm 25 \text{ V}, V_{DS}$		$V_{GS}=\pm 25~V,~V_{DS}=0~V$	_	_	±10	μА
Gate-source breakdown voltage		V (BR) GSS	$I_G=\pm 10~\mu A,~V_{DS}=0~V$	±30	_	_	V
Drain cutoff current		I <sub>DSS</sub>	V <sub>DS</sub> = 640 V, V <sub>GS</sub> = 0 V	_	_	100	μА
Drain-source breakdown voltage		V (BR) DSS	$I_D = 10$ mA, $V_{GS} = 0$ V	800	_	_	V
Gate threshold voltage		V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.0	_	4.0	V
Drain-source ON resistance		R <sub>DS</sub> (ON)	$V_{GS} = 10 \text{ V}, I_D = 3.5 \text{ A}$	_	1.35	1.7	Ω
Forward transfer	ord transfer admittance $ Y_{fS} $ $V_{DS} = 20 \text{ V}, I_D = 3.5 \text{ A}$		V <sub>DS</sub> = 20 V, I <sub>D</sub> = 3.5 A	2.5	5.2	_	S
Input capacitance	e	C <sub>iss</sub>		_	1500	_	
Reverse transfer capacitance		C <sub>rss</sub>	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	25	_	pF
Output capacitance		Coss		_	140	_	
Switching time	Rise time	t <sub>r</sub>	$V_{GS}^{10 \text{ V}} V_{GS}^{10 \text{ V}} = 3.5 \text{ A Vout}$ $0 \text{ V} \downarrow $	_	35	_	- ns
	Turn-on time	t <sub>on</sub>		_	80	_	
	Fall time	t <sub>f</sub>		_	50		
	Turn-off time	t <sub>off</sub>		_	220		
Total gate charge (gate-source plus gate-drain)		Qg	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 6.5 \text{ A}$	_	35	_	
Gate-source charge		Q <sub>gs</sub>		_	22	_	nC
Gate-drain ("Miller") charge		Q <sub>gd</sub>		_	13	_	

# **Source-Drain Ratings and Characteristics (Ta = 25°C)**

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	$I_{DR}$	_	_	_	6.5	Α
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_	_	19.5	Α
Forward voltage (diode)	$V_{DSF}$	I <sub>DR</sub> = 6.5 A, V <sub>GS</sub> = 0 V	_	_	-1.7	V
Reverse recovery time	t <sub>rr</sub>	$I_{DR} = 6.5 \text{ A}, V_{GS} = 0 \text{ V},$	_	1200		ns
Reverse recovery charge	Q <sub>rr</sub>	dl <sub>DR</sub> /dt = 100 A/μs	_	11.5		μС

## Marking

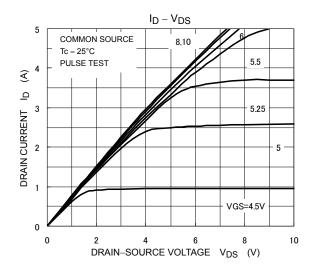


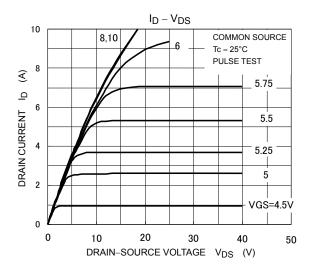
Note 4: A line under a Lot No. identifies the indication of product Labels.

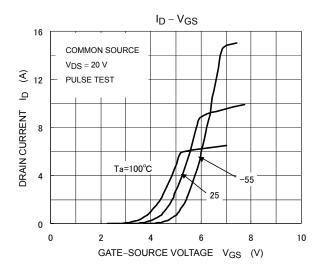
Not underlined: [[Pb]]/INCLUDES > MCV

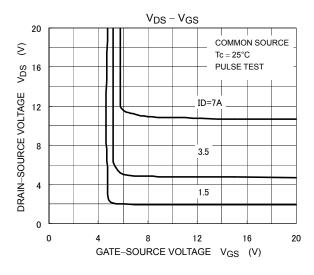
Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

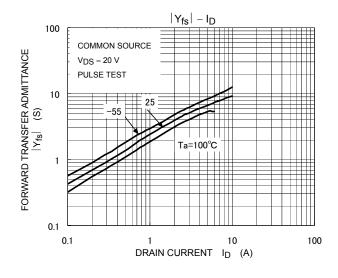
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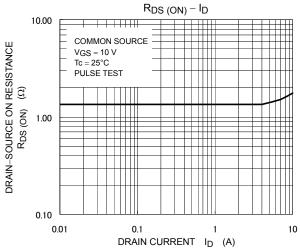


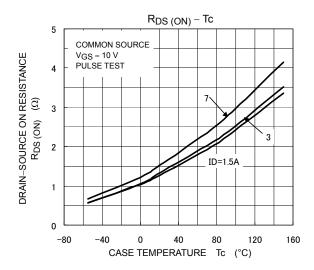


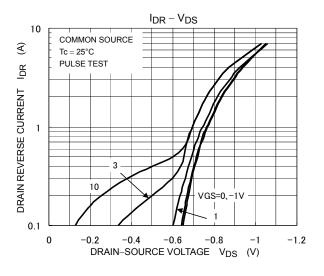


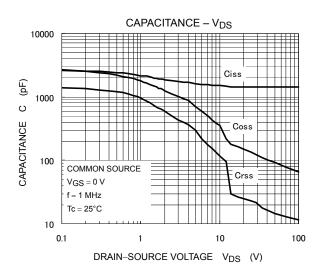


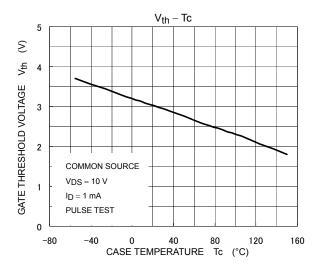


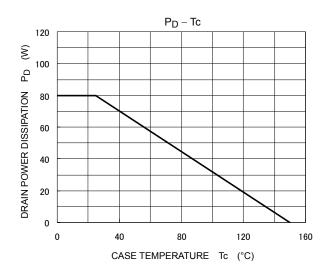


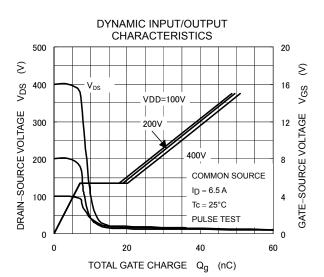


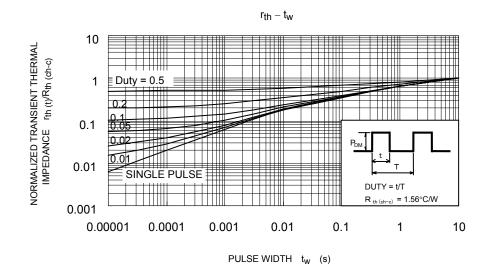


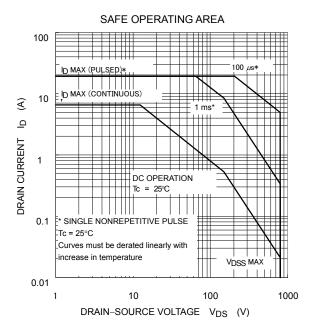


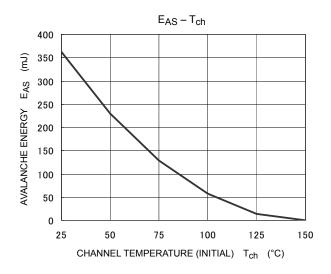


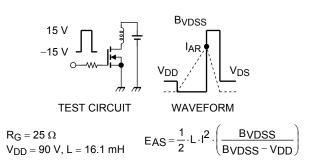












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