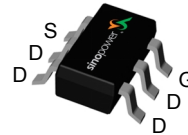


N-Channel Enhancement Mode MOSFET

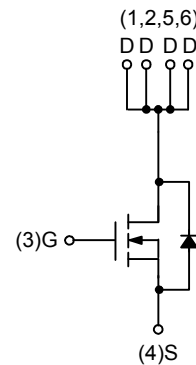
Features

- 30V/7.4A,
 $R_{DS(ON)} = 24m\Omega(max.) @ V_{GS}=10V$
 $R_{DS(ON)} = 32.5m\Omega(max.) @ V_{GS}=4.5V$
- Reliable and Rugged
- Lead Free and Green Devices Available (RoHS Compliant)

Pin Description



Top View of SOT-23-6



N-Channel MOSFET

Applications

- Power Management in Notebook Computer, Portable Equipment and Battery Powered Systems.
- Load Switch.

Ordering and Marking Information

<p>SM2604NS □□-□□□</p> <ul style="list-style-type: none"> └─ Assembly Material └─ Handling Code └─ Temperature Range └─ Package Code 	<p>Package Code C : SOT-23-6</p> <p>Operating Junction Temperature Range C : -55 to 150 °C</p> <p>Handling Code TR : Tape & Reel (3000ea/reel)</p> <p>Assembly Material G : Halogen and Lead Free Device</p>
<p>SM2604NS C : ● C04XX</p>	<p>XX - Lot Code</p>

Note : SINOPOWER lead-free products contain molding compounds/die attach materials and 100% matte tin plate termination finish; which are fully compliant with RoHS. SINOPOWER lead-free products meet or exceed the lead-free requirements of IPC/JEDEC J-STD-020D for MSL classification at lead-free peak reflow temperature. SINOPOWER defines "Green" to mean lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

SINOPOWER reserves the right to make changes to improve reliability or manufacturability without notice, and advise customers to obtain the latest version of relevant information to verify before placing orders.

Absolute Maximum Ratings ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Rating	Unit
Common Ratings			
V_{DSS}	Drain-Source Voltage	30	V
V_{GSS}	Gate-Source Voltage	± 20	
T_J	Maximum Junction Temperature	150	$^\circ\text{C}$
T_{STG}	Storage Temperature Range	-55 to 150	
I_S	Diode Continuous Forward Current	$T_A=25^\circ\text{C}$ 1	A
I_D	Continuous Drain Current	$T_A=25^\circ\text{C}$ 7.4	
		$T_A=70^\circ\text{C}$ 5.9	
I_{DM}^a	Pulsed Drain Current	$T_A=25^\circ\text{C}$ 29	
P_D	Maximum Power Dissipation	$T_A=25^\circ\text{C}$ 2	W
		$T_A=70^\circ\text{C}$ 1.28	
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient	$t \leq 10\text{s}$ 62.5	$^\circ\text{C/W}$
		Steady State 110	
$R_{\theta JL}$	Thermal Resistance-Junction to Lead	Steady State 68	

Note a : Pulse width limited by max. junction temperature.

Electrical Characteristics ($T_A = 25^\circ\text{C}$ unless otherwise noted)

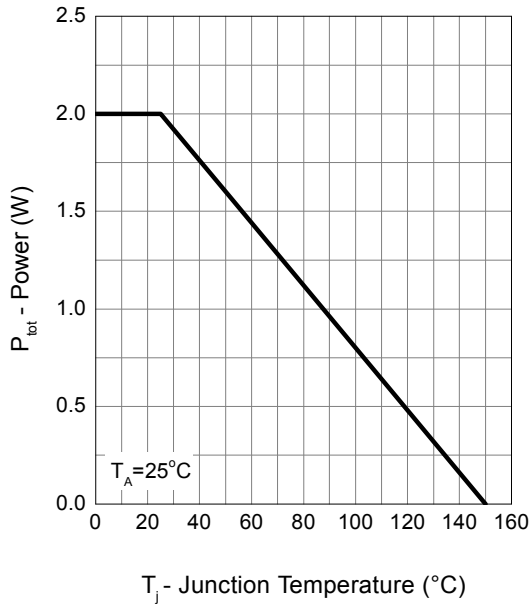
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_{DS}=250\mu A$	30	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=24V, V_{GS}=0V$	-	-	1	μA
		$T_J=85^\circ\text{C}$	-	-	30	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_{DS}=250\mu A$	1.3	1.8	2.5	V
I_{GSS}	Gate Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	± 100	nA
$R_{DS(ON)}^b$	Drain-Source On-state Resistance	$V_{GS}=10V, I_{DS}=8A$	-	20	24	m Ω
		$V_{GS}=4.5V, I_{DS}=5A$	-	25	32.5	
Diode Characteristics						
V_{SD}^b	Diode Forward Voltage	$I_{SD}=1A, V_{GS}=0V$	-	0.75	1.1	V
t_{rr}^c	Reverse Recovery Time	$I_{SD}=8A, di_{SD}/dt=100A/\mu s$	-	14	-	ns
Q_{rr}^c	Reverse Recovery Charge		-	4.5	-	nC
Dynamic Characteristics ^c						
R_G	Gate Resistance	$V_{GS}=0V, V_{DS}=0V, F=1\text{MHz}$	1	1.5	2.5	Ω
C_{iss}	Input Capacitance	$V_{GS}=0V, V_{DS}=15V, \text{Frequency}=1.0\text{MHz}$	320	410	500	pF
C_{oss}	Output Capacitance		48	73	98	
C_{riss}	Reverse Transfer Capacitance		25	42	60	
$t_{d(ON)}$	Turn-on Delay Time	$V_{DD}=15V, R_L=15\Omega, I_{DS}=1A, V_{GEN}=10V, R_G=6\Omega$	-	6.8	-	ns
t_r	Turn-on Rise Time		-	10	-	
$t_{d(OFF)}$	Turn-off Delay Time		-	15	-	
t_f	Turn-off Fall Time		-	3.6	-	
Gate Charge Characteristics ^c						
Q_g	Total Gate Charge	$V_{DS}=15V, V_{GS}=4.5V, I_{DS}=8A$	-	4	7	nC
Q_g	Total Gate Charge	$V_{DS}=15V, V_{GS}=10V, I_{DS}=8A$	-	8.2	11.5	
Q_{gth}	Threshold Gate Charge		-	0.8	1.2	
Q_{gs}	Gate-Source Charge		-	1.65	2.3	
Q_{gd}	Gate-Drain Charge		-	1.55	2.1	

Note b : Pulse test ; pulse width $\leq 300 \mu s$, duty cycle $\leq 2\%$.

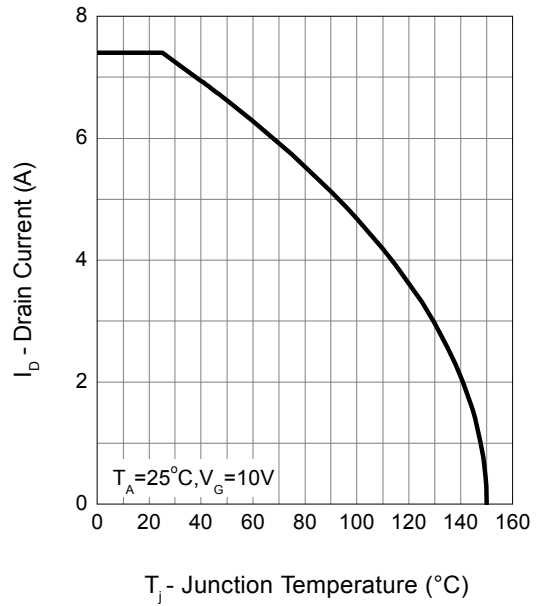
Note c : Guaranteed by design, not subject to production testing.

Typical Operating Characteristics

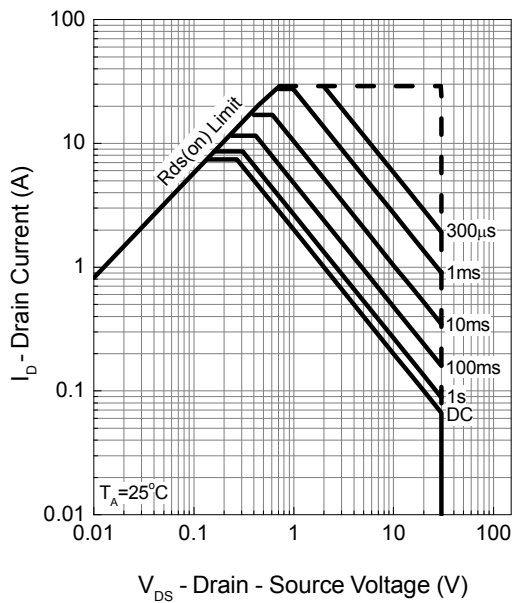
Power Dissipation



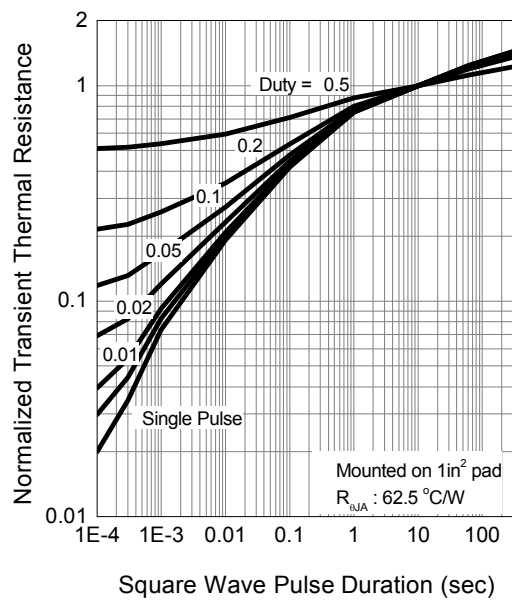
Drain Current



Safe Operation Area

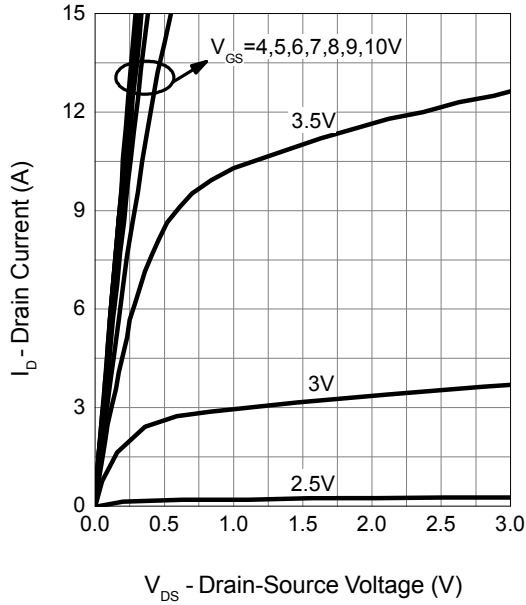


Thermal Transient Impedance

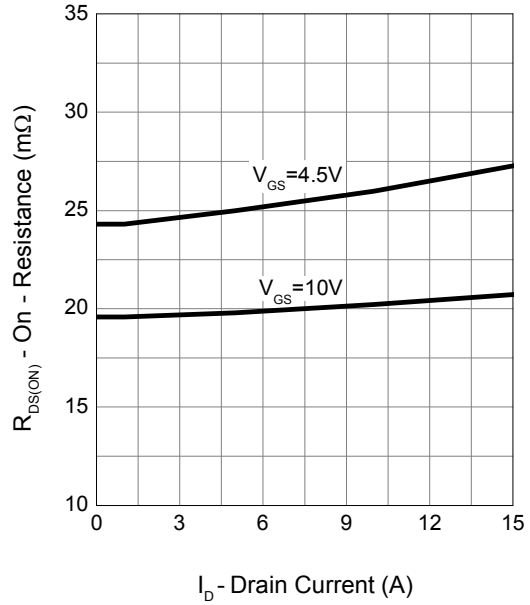


Typical Operating Characteristics (Cont.)

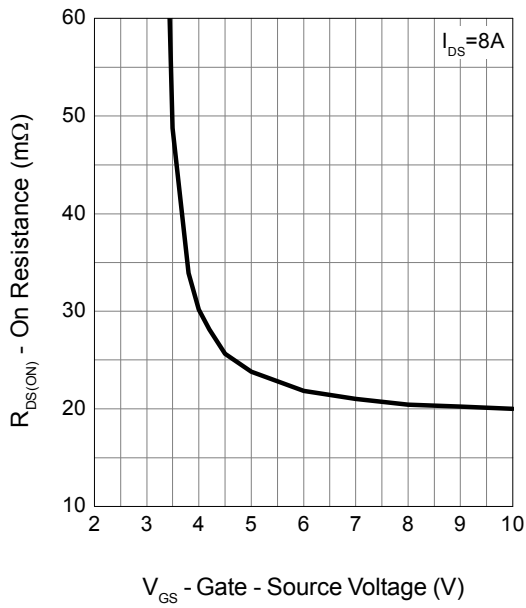
Output Characteristics



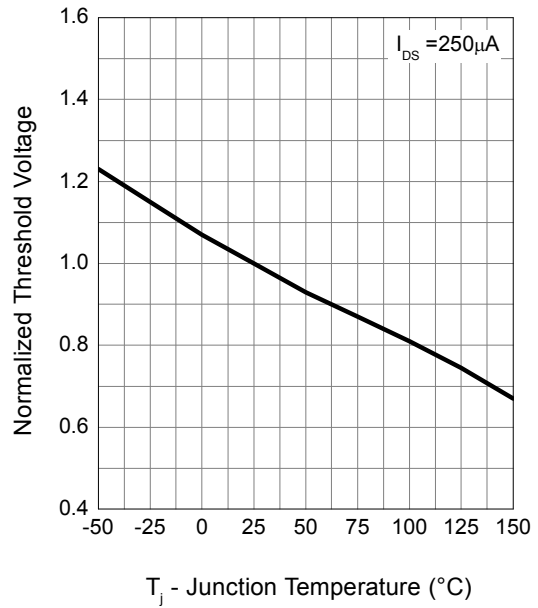
Drain-Source On Resistance



Gate-Source On Resistance

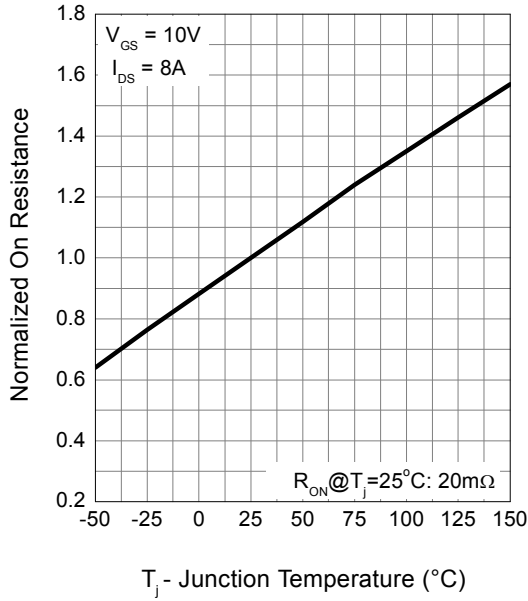


Gate Threshold Voltage

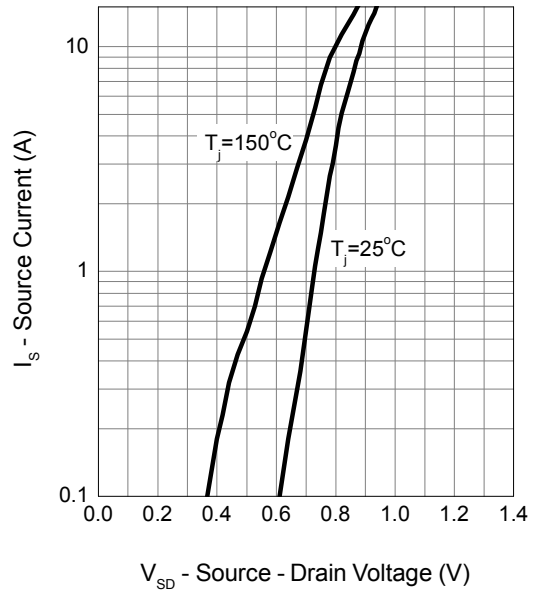


Typical Operating Characteristics (Cont.)

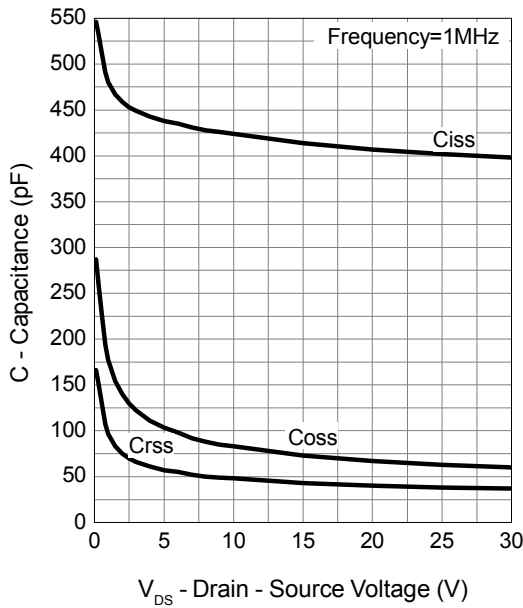
Drain-Source On Resistance



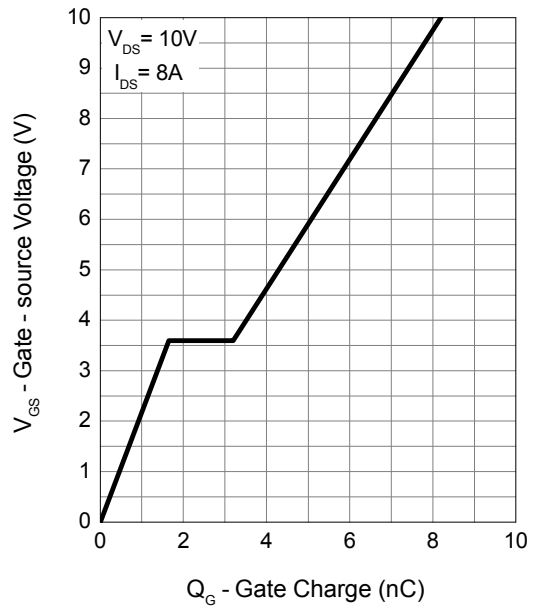
Source-Drain Diode Forward



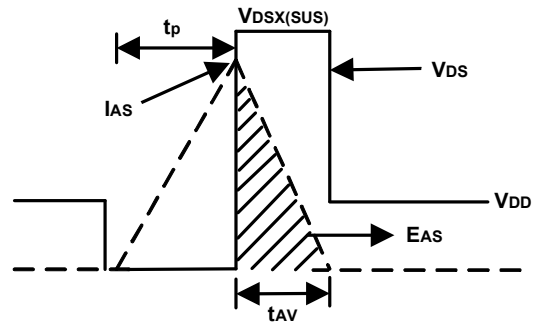
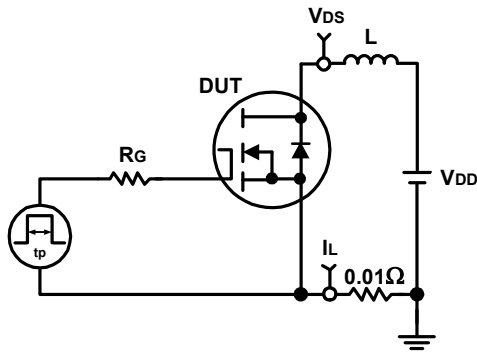
Capacitance



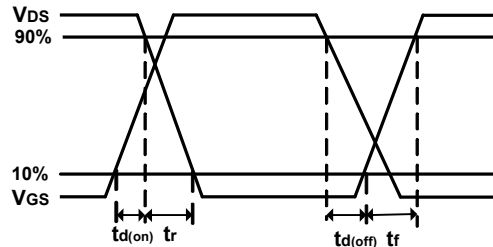
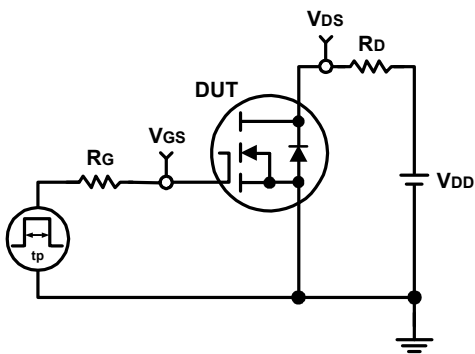
Gate Charge



Avalanche Test Circuit and Waveforms

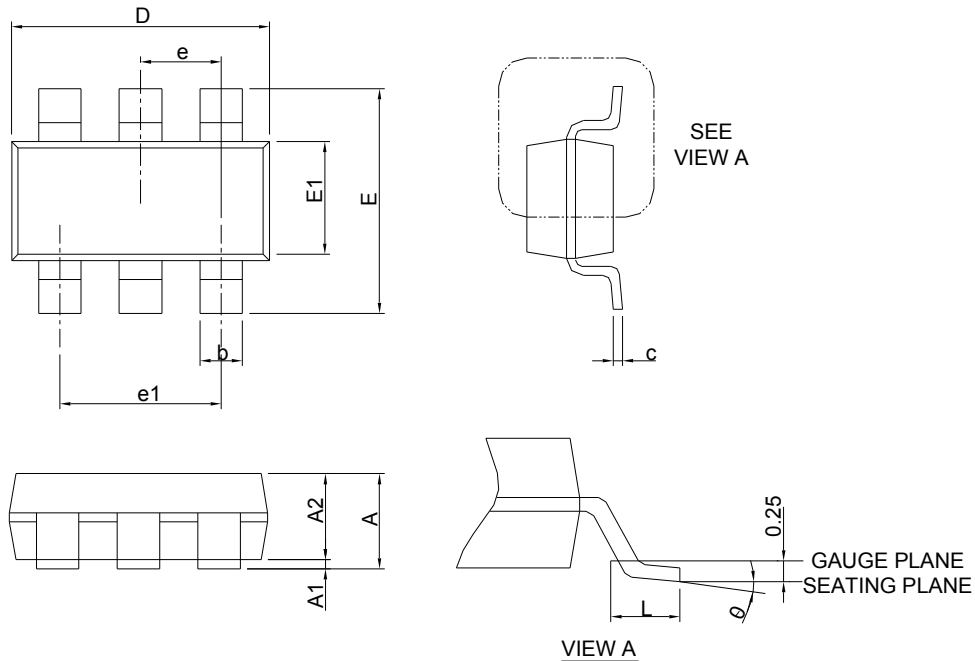


Switching Time Test Circuit and Waveforms



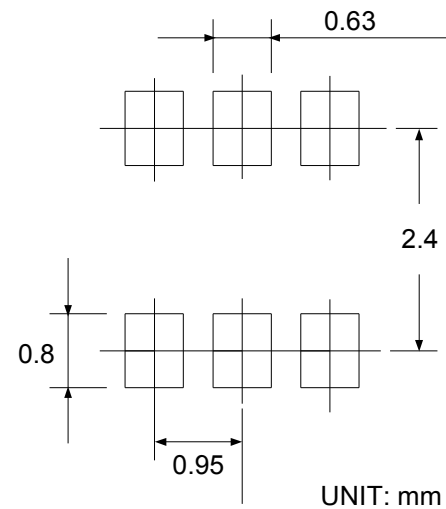
Package Information

SOT-23-6



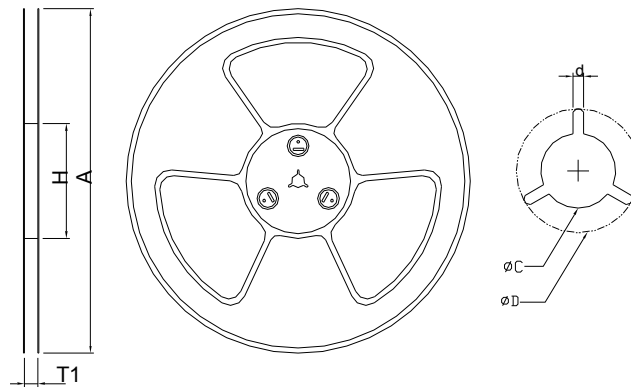
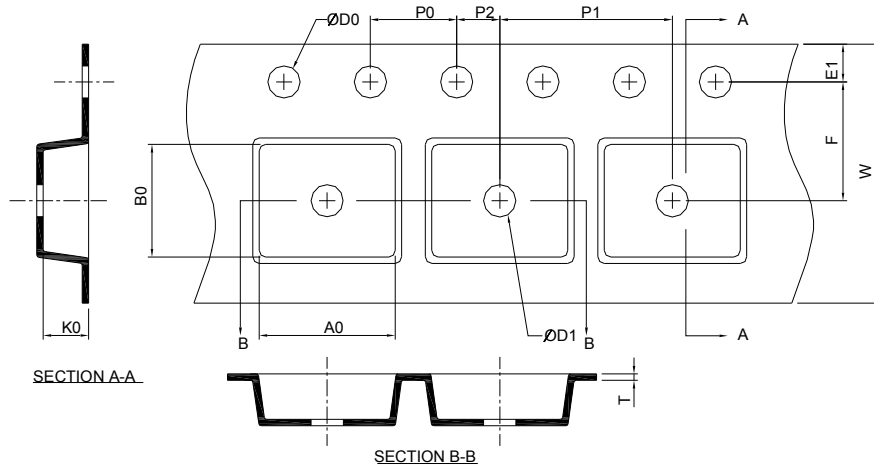
SYMBOL	SOT-23-6			
	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	-	1.25	-	0.049
A1	0.00	0.15	0.000	0.006
A2	0.90	1.30	0.035	0.051
b	0.30	0.50	0.012	0.020
c	0.08	0.22	0.003	0.009
D	2.70	3.10	0.106	0.122
E	2.60	3.00	0.102	0.118
E1	1.40	1.80	0.055	0.071
e	0.95 BSC		0.037 BSC	
e1	1.90 BSC		0.075 BSC	
L	0.30	0.60	0.012	0.024
θ	0°	8°	0°	8°

RECOMMENDED LAND PATTERN



- Note : 1. Follow JEDEC TO-178 AB.
 2. Dimension D and E1 do not include mold flash, protrusions or gate burrs. Mold flash, protrusion or gate burrs shall not exceed 10 mil per side.

Carrier Tape & Reel Dimensions

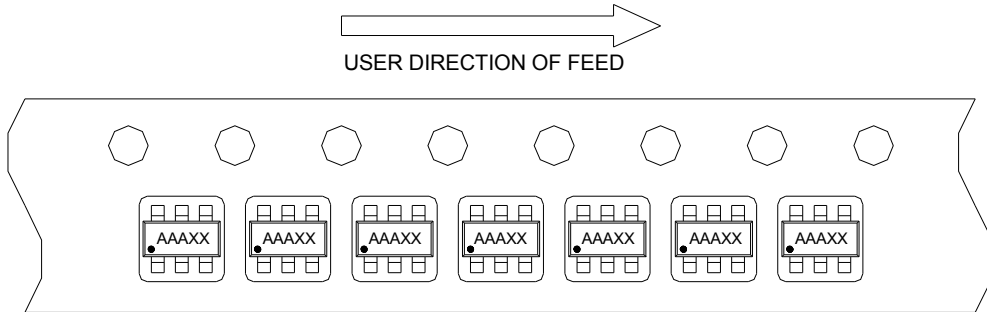


Application	A	H	T1	C	d	D	W	E1	F
SOT-23-6	178.0±2.00	50 MIN.	8.4+2.00 -0.00	13.0+0.50 -0.20	1.5 MIN.	20.2 MIN.	8.0±0.30	1.75±0.10	3.5±0.05
	P0	P1	P2	D0	D1	T	A0	B0	K0
	4.0±0.10	4.0±0.10	2.0±0.05	1.5+0.10 -0.00	1.0 MIN.	0.6+0.00 -0.40	3.20±0.20	3.10±0.20	1.50±0.20

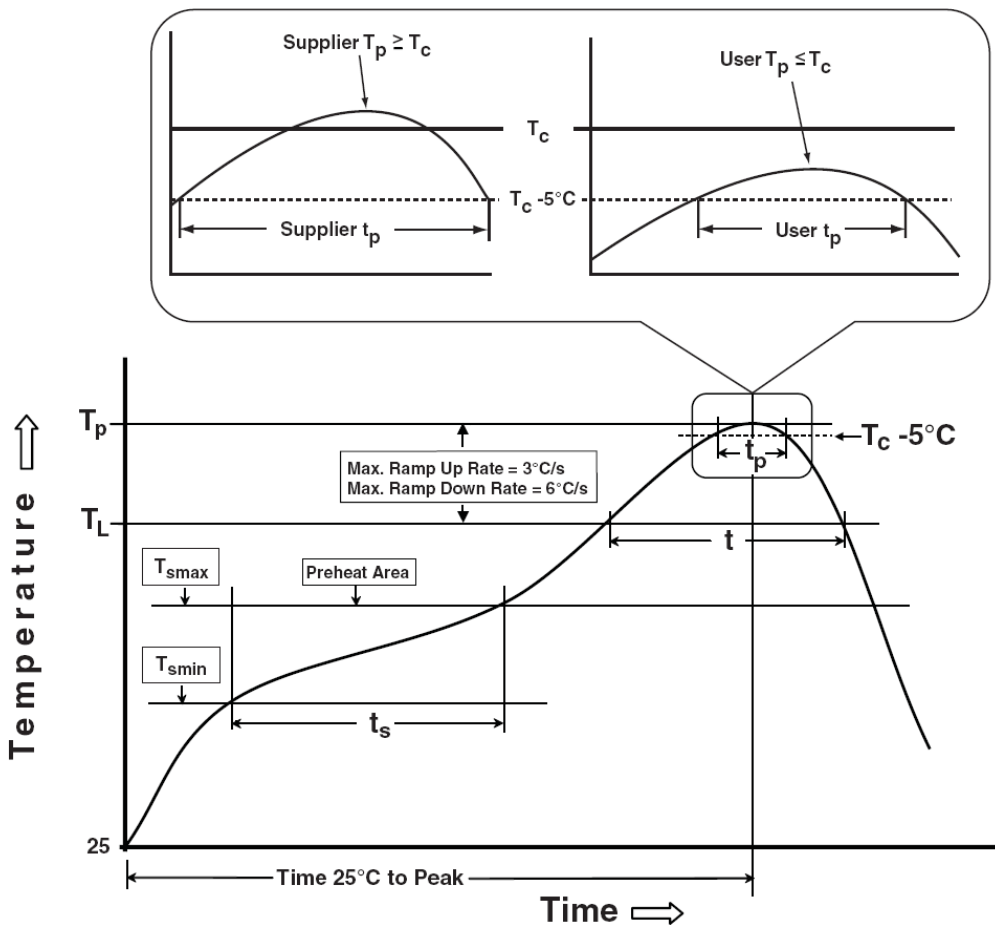
(mm)

Taping Direction Information

SOT-23-6



Classification Profile



Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Preheat & Soak		
Temperature min (T_{smin})	100 °C	150 °C
Temperature max (T_{smax})	150 °C	200 °C
Time (T_{smin} to T_{smax}) (t_s)	60-120 seconds	60-120 seconds
Average ramp-up rate (T_{smax} to T_p)	3 °C/second max.	3°C/second max.
Liquidous temperature (T_L)	183 °C	217 °C
Time at liquidous (t_L)	60-150 seconds	60-150 seconds
Peak package body Temperature (T_p)*	See Classification Temp in table 1	See Classification Temp in table 2
Time (t_p)** within 5°C of the specified classification temperature (T_c)	20** seconds	30** seconds
Average ramp-down rate (T_p to T_{smax})	6 °C/second max.	6 °C/second max.
Time 25°C to peak temperature	6 minutes max.	8 minutes max.
* Tolerance for peak profile Temperature (T_p) is defined as a supplier minimum and a user maximum.		
** Tolerance for time at peak profile temperature (t_p) is defined as a supplier minimum and a user maximum.		

Table 1. SnPb Eutectic Process – Classification Temperatures (T_c)

Package Thickness	Volume mm ³ <350	Volume mm ³ ≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2. Pb-free Process – Classification Temperatures (T_c)

Package Thickness	Volume mm ³ <350	Volume mm ³ 350-2000	Volume mm ³ >2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HTRB	JESD-22, A108	1000 Hrs, 80% of VDS max @ T_{jmax}
HTGB	JESD-22, A108	1000 Hrs, 100% of VGS max @ T_{jmax}
PCT	JESD-22, A102	168 Hrs, 100%RH, 2atm, 121°C
TCT	JESD-22, A104	500 Cycles, -65°C~150°C

Customer Service

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