

Dual Enhancement Mode MOSFET (N-and P-Channel)

Features

- N Channel**
 60V/3.6A,
 $R_{DS(ON)} = 65m\Omega$ (max.) @ $V_{GS} = 10V$
 $R_{DS(ON)} = 78m\Omega$ (max.) @ $V_{GS} = 4.5V$
- P Channel**
 -60V/-2.5A,
 $R_{DS(ON)} = 137m\Omega$ (max.) @ $V_{GS} = -10V$
 $R_{DS(ON)} = 180m\Omega$ (max.) @ $V_{GS} = -4.5V$
- 100% UIS Tested
- Reliable and Rugged
- Lead Free Available (RoHS Compliant)
- ESD Protection

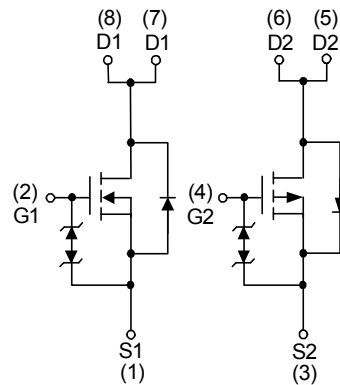
Applications

- Synchronous Rectification.
- Motor Control.
- High Current, High Speed Switching.
- Portable equipment application.

Pin Description



Top View of DFN3x3-8(NP)



N-Channel MOSFET

P-Channel MOSFET

Ordering and Marking Information

<p>SM6043CS □□-□□□</p> <p>□□□□□</p> <p>Assembly Material</p> <p>Handling Code</p> <p>Temperature Range</p> <p>Package Code</p>	<p>Package Code Q : DFN3x3-8(NP)</p> <p>Operating Junction Temperature Range C : -55 to 150 °C</p> <p>Handling Code TR : Tape & Reel (3000ea/reel)</p> <p>Assembly Material G : Halogen and Lead Free Device</p>
<p>SM6043CS Q :</p> <div style="border: 1px solid black; padding: 2px; display: inline-block;"> SM 6043C XXXXX </div>	<p>XXXXXX - Lot Code</p>

Note : SINOPOWER lead-free products contain molding compounds/die attach materials and 100% matte tin plate termination finish; which are fully compliant with RoHS. SINOPOWER lead-free products meet or exceed the lead-free requirements of IPC/JEDEC J-STD-020D for MSL classification at lead-free peak reflow temperature. SINOPOWER defines "Green" to mean lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

SINOPOWER reserves the right to make changes to improve reliability or manufacturability without notice, and advise customers to obtain the latest version of relevant information to verify before placing orders.

Absolute Maximum Ratings ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	N Channel	P Channel	Unit	
Common Ratings					
V_{DSS}	Drain-Source Voltage	60	-60	V	
V_{GSS}	Gate-Source Voltage	±20			
T_J	Maximum Junction Temperature	150		°C	
T_{STG}	Storage Temperature Range	-55 to 150			
I_S	Diode Continuous Forward Current	$T_A=25^\circ\text{C}$	1.8	-1	A
I_D	Continuous Drain Current	$T_A=25^\circ\text{C}$	3.6	-2.5	A
		$T_A=70^\circ\text{C}$	2.8	-2	
I_{DM}^a	Pulsed Drain Current	$T_A=25^\circ\text{C}$	14	-10	A
P_D	Maximum Power Dissipation	$T_A=25^\circ\text{C}$	1.79	1.79	W
		$T_A=70^\circ\text{C}$	1.1	1.1	
$R_{\theta JA}^b$	Thermal Resistance-Junction to Ambient	$t \leq 10\text{s}$	70		°C/W
		Steady State	120		°C/W
I_{AS}^c	Avalanche Current, Single pulse	$L=0.5\text{mH}$	7	-7	A
E_{AS}^c	Avalanche Energy, Single pulse	$L=0.5\text{mH}$	12	12	mJ

Note a : Pulse width limited by max. junction temperature.

Note b : Surface mounted on 1in² pad area.

Note c : UIS tested and pulse width limited by maximum junction temperature 150°C (initial temperature $T_J=25^\circ\text{C}$).

N Channel Electrical Characteristics (T_A = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	N Channel			Unit
			Min.	Typ.	Max.	
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =250μA	60	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =48V, V _{GS} =0V	-	-	1	μA
		T _J =85°C	-	-	30	
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250μA	1	2	3	V
I _{GSS}	Gate Leakage Current	V _{GS} =±20V, V _{DS} =0V	-	-	±10	uA
R _{DS(ON)} ^d	Drain-Source On-state Resistance	V _{GS} =10V, I _{DS} =3.6A	-	54	65	mΩ
		V _{GS} =4.5V, I _{DS} =3A	-	60	78	mΩ
Diode Characteristics						
V _{SD} ^d	Diode Forward Voltage	I _{SD} =1.8A, V _{GS} =0V	-	0.8	1.3	V
t _{rr}	Reverse Recovery Time	I _{SD} =3.6A, dI _{SD} /dt=100A/μs	-	18	-	ns
Q _{rr}	Reverse Recovery Charge		-	16	-	nC
Dynamic Characteristics^e						
R _G	Gate Resistance	V _{GS} =0V, V _{DS} =0V, f=1MHz	-	3.5	-	Ω
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =30V, Frequency=1.0MHz	-	400	520	pF
C _{oss}	Output Capacitance		-	40	-	
C _{rss}	Reverse Transfer Capacitance		-	18	-	
t _{d(ON)}	Turn-on Delay Time		V _{DD} =30V, R _L =30Ω, I _{DS} =1A, V _{GEN} =10V, R _G =6Ω	-	7	13
t _r	Turn-on Rise Time	-		7	13	
t _{d(OFF)}	Turn-off Delay Time	-		18	33	
t _f	Turn-off Fall Time	-		5	9	
Gate Charge Characteristics^e						
Q _g	Total Gate Charge	V _{DS} =30V, V _{GS} =4.5V, I _{DS} =3.6A	-	4	-	nC
Q _g	Total Gate Charge	V _{DS} =30V, V _{GS} =10V, I _{DS} =3.6A	-	9	13	
Q _{gs}	Gate-Source Charge		-	2	-	
Q _{gd}	Gate-Drain Charge		-	2	-	

Note d : Pulse test ; pulse width≤300μs, duty cycle≤2%.

Note e : Guaranteed by design, not subject to production testing.

P Channel Electrical Characteristics (T_A = 25°C unless otherwise noted)

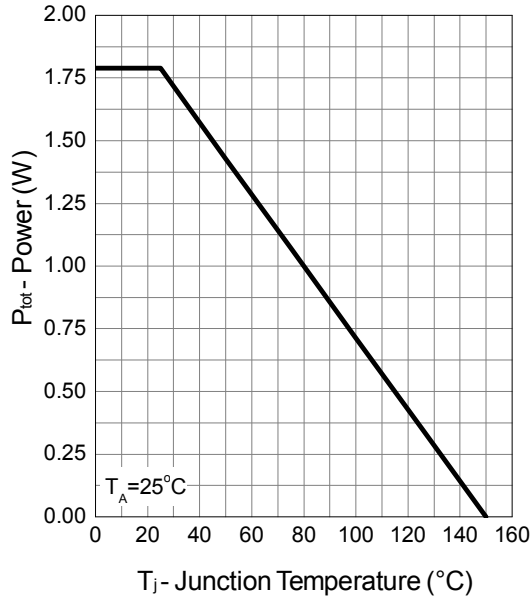
Symbol	Parameter	Test Conditions	P Channel			Unit
			Min.	Typ.	Max.	
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =-250μA	-60	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =-48V, V _{GS} =0V T _J =85°C	-	-	-1	μA
			-	-	-30	
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =-250μA	-1	-2	-3	V
I _{GSS}	Gate Leakage Current	V _{GS} =±20V, V _{DS} =0V	-	-	±10	uA
R _{DS(ON)} ^d	Drain-Source On-state Resistance	V _{GS} =-10V, I _{DS} =-2.5A	-	110	137	mΩ
		V _{GS} =-4.5V, I _{DS} =2A	-	130	180	mΩ
Diode Characteristics						
V _{SD} ^d	Diode Forward Voltage	I _{SD} =-1.8A, V _{GS} =0V	-	-0.8	-1	V
t _{rr}	Reverse Recovery Time	I _{SD} =-2.5A,	-	20	-	ns
Q _{rr}	Reverse Recovery Charge	di _{SD} /dt=100A/μs	-	19	-	nC
Dynamic Characteristics^e						
R _G	Gate Resistance	V _{GS} =0V, V _{DS} =0V, f=1MHz	-	10	-	Ω
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =-30V, Frequency=1.0MHz	-	430	560	pF
C _{oss}	Output Capacitance		-	41	-	
C _{rss}	Reverse Transfer Capacitance		-	25	-	
t _{d(ON)}	Turn-on Delay Time		V _{DD} =-30V, R _L =30Ω, I _{DS} =-1A, V _{GEN} =-10V, R _G =6Ω	-	8.5	-
t _r	Turn-on Rise Time	-		5.8	-	
t _{d(OFF)}	Turn-off Delay Time	-		36	-	
t _f	Turn-off Fall Time	-		24	-	
Gate Charge Characteristics^e						
Q _g	Total Gate Charge	V _{DS} =-30V, V _{GS} =-4.5V, I _{DS} =-2.5A	-	5.4	-	nC
Q _g	Total Gate Charge	V _{DS} =-30V, V _{GS} =-10V, I _{DS} =-2.5A	-	11	16	
Q _{gs}	Gate-Source Charge		-	1.4	-	
Q _{gd}	Gate-Drain Charge		-	2.4	-	

Note d : Pulse test ; pulse width≤300μs, duty cycle≤2%.

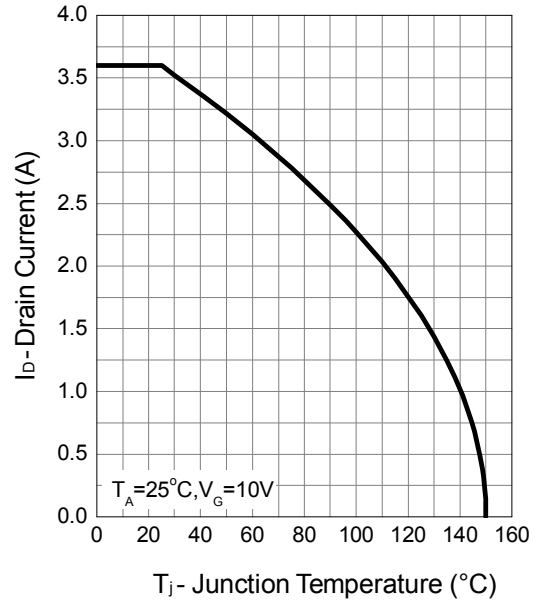
Note e : Guaranteed by design, not subject to production testing.

N Channel Typical Operating Characteristics

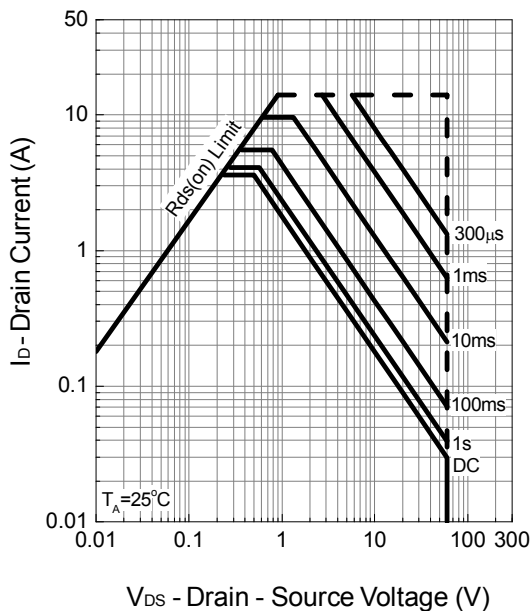
Power Dissipation



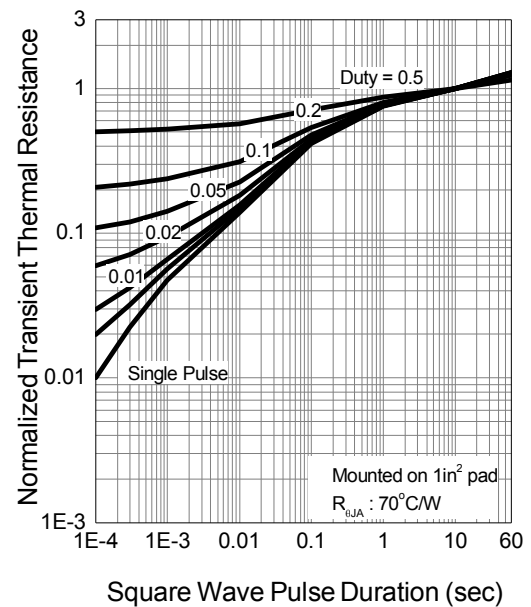
Drain Current



Safe Operation Area

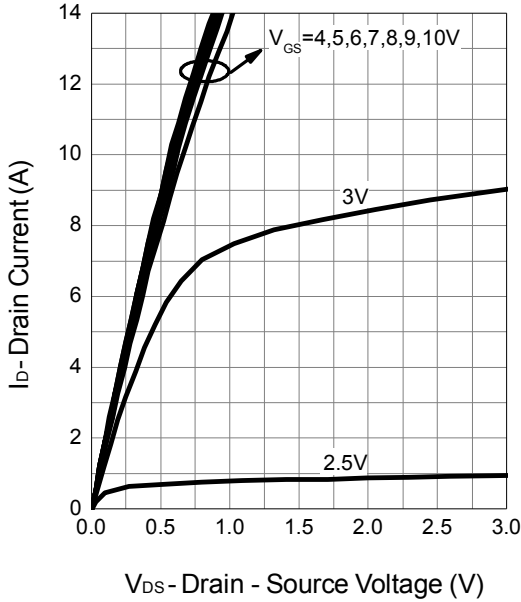


Thermal Transient Impedance

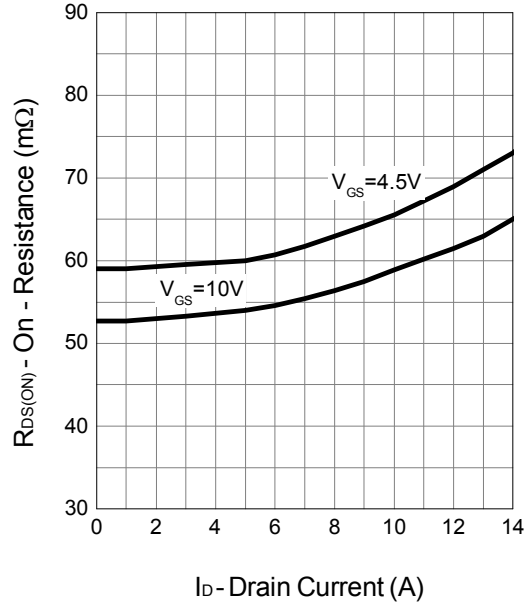


N Channel Typical Operating Characteristics (Cont.)

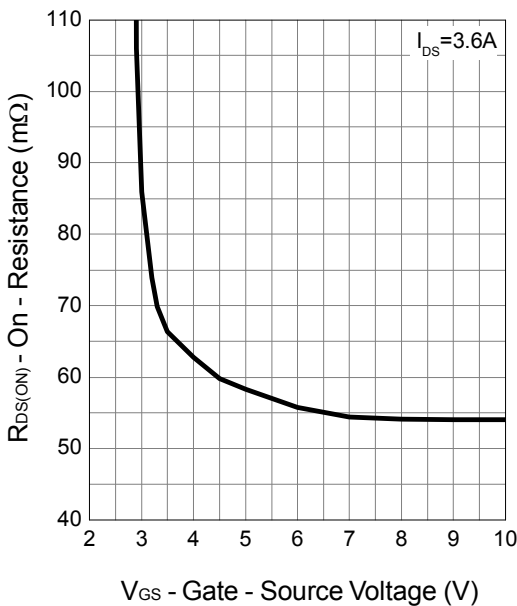
Output Characteristics



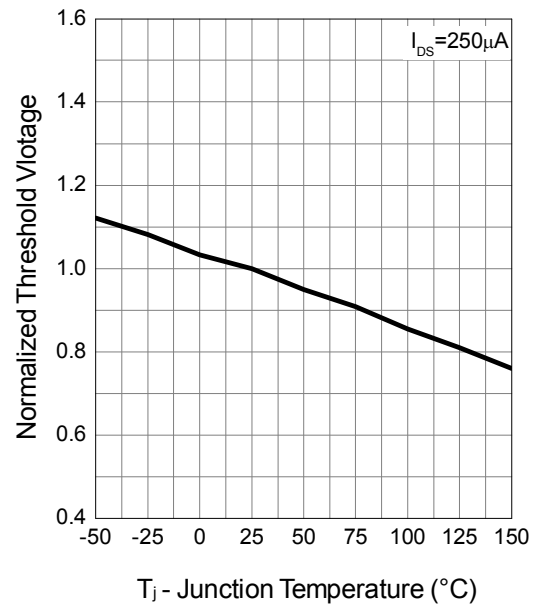
Drain-Source On Resistance



Gate-Source On Resistance

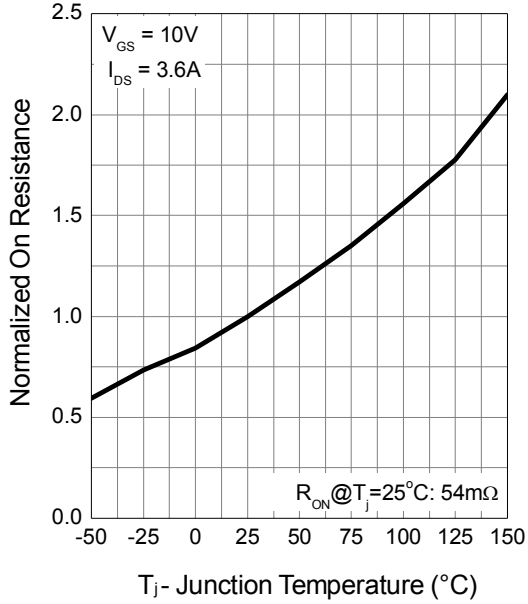


Gate Threshold Voltage

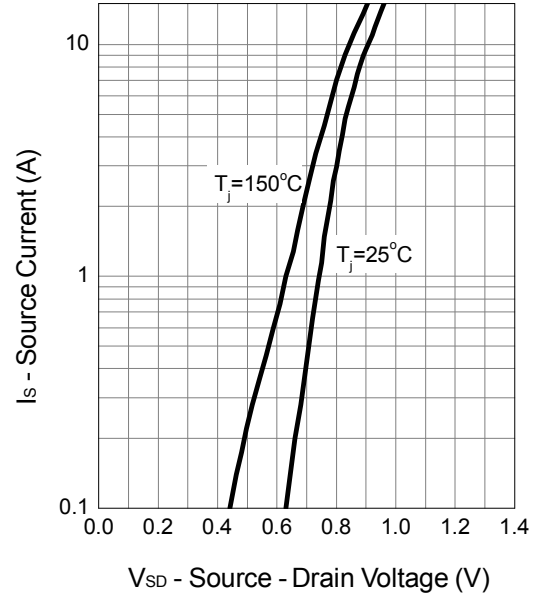


N Channel Typical Operating Characteristics (Cont.)

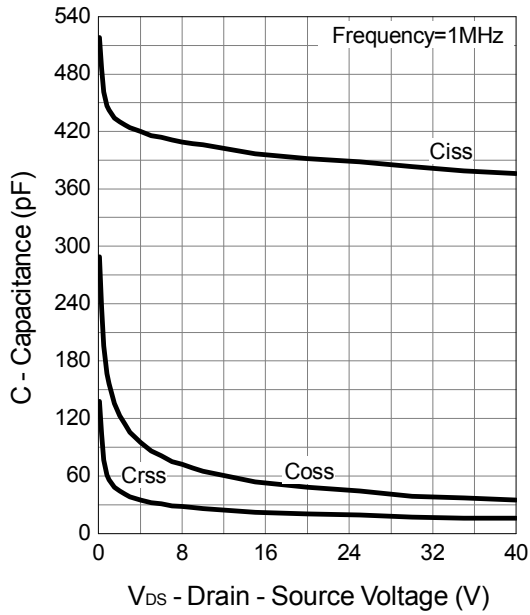
Drain-Source On Resistance



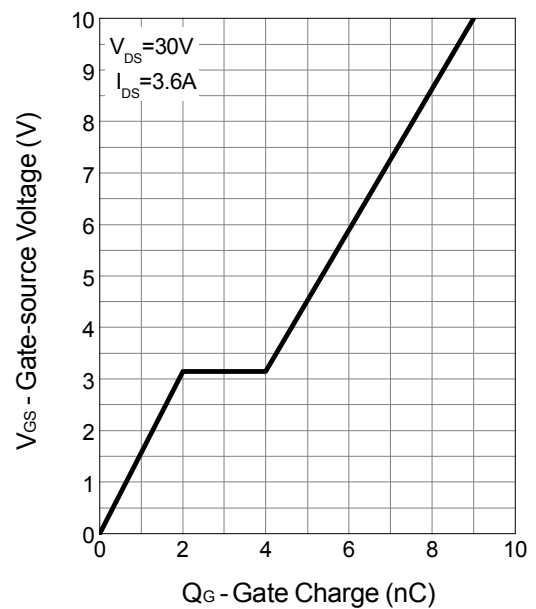
Source-Drain Diode Forward



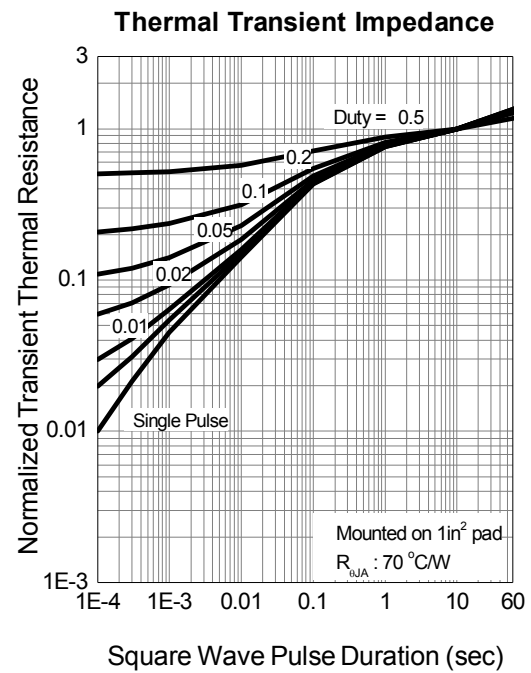
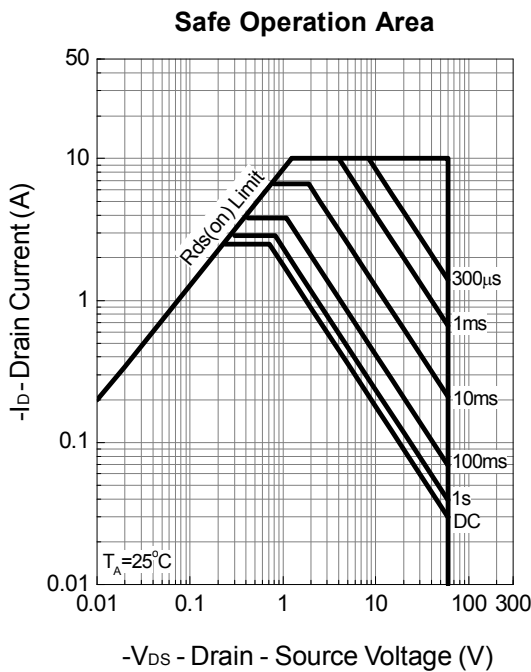
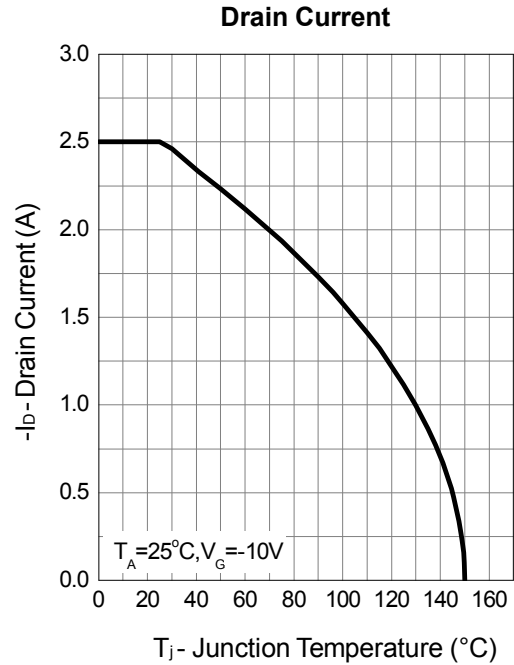
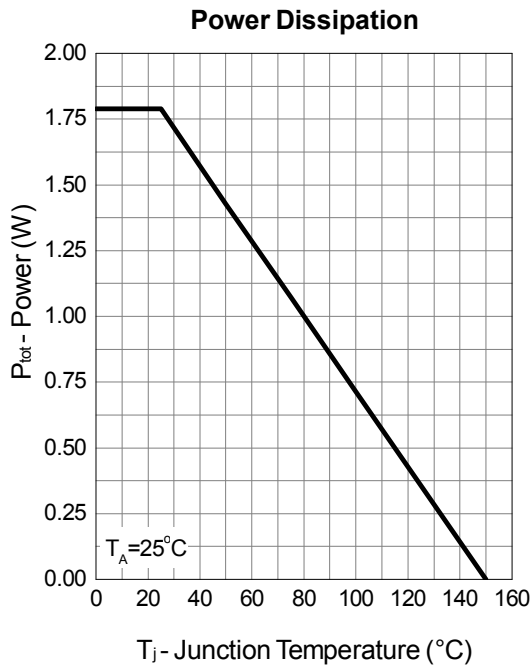
Capacitance



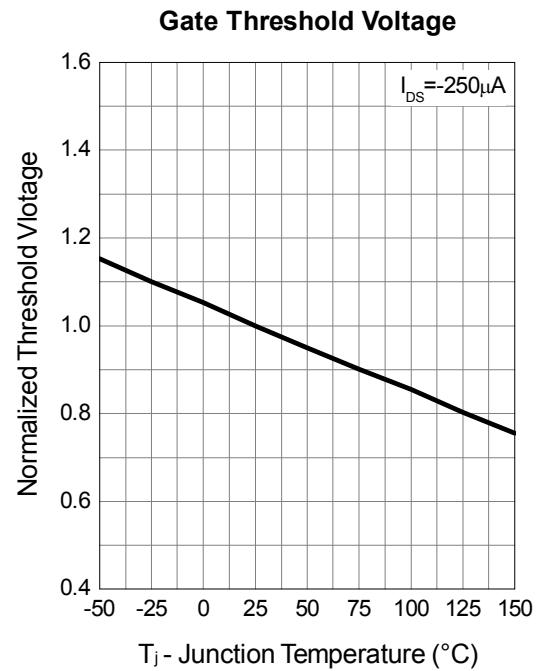
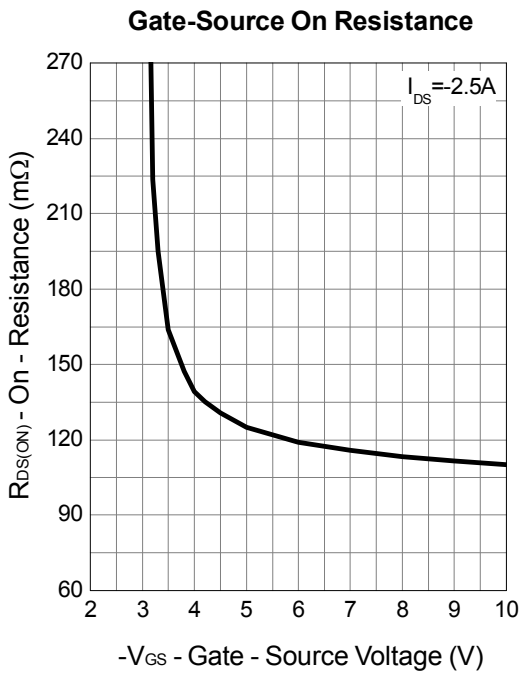
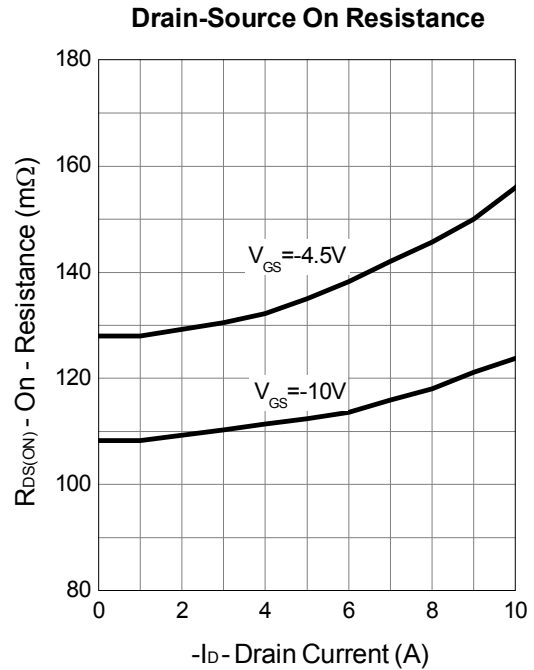
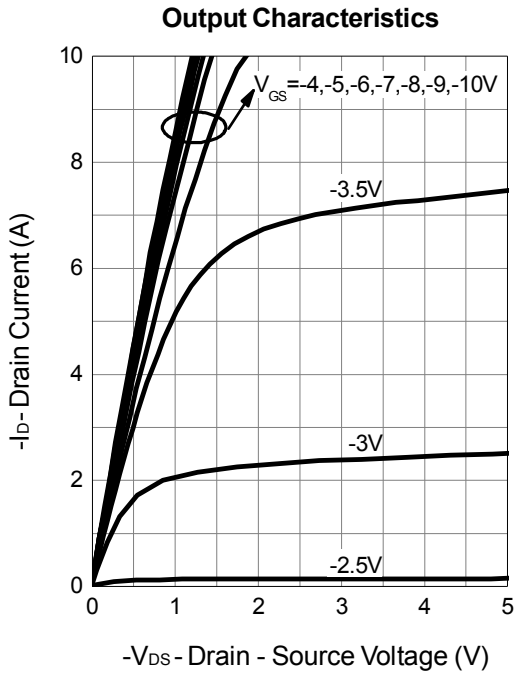
Gate Charge



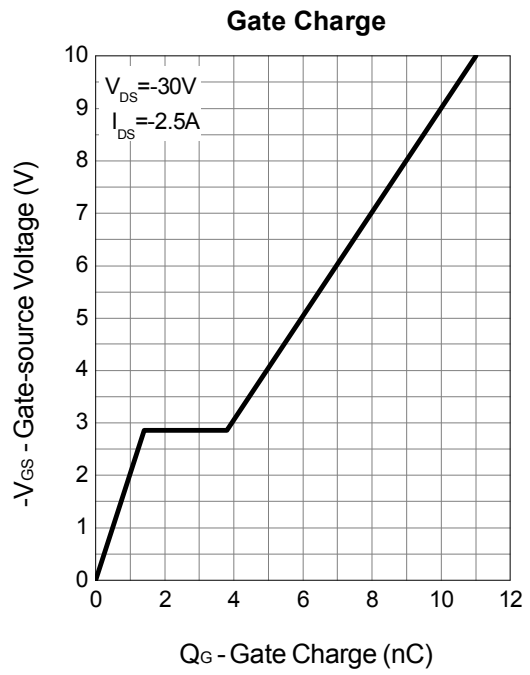
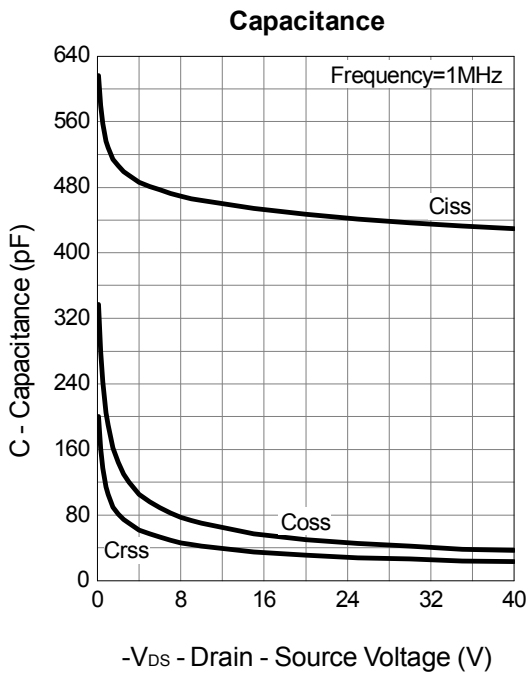
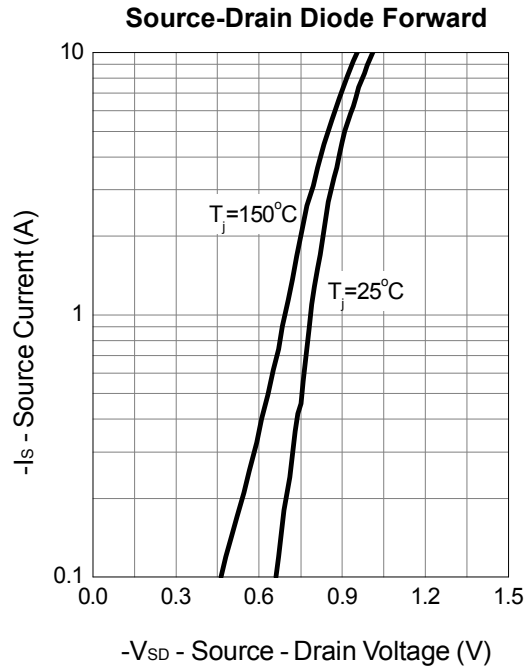
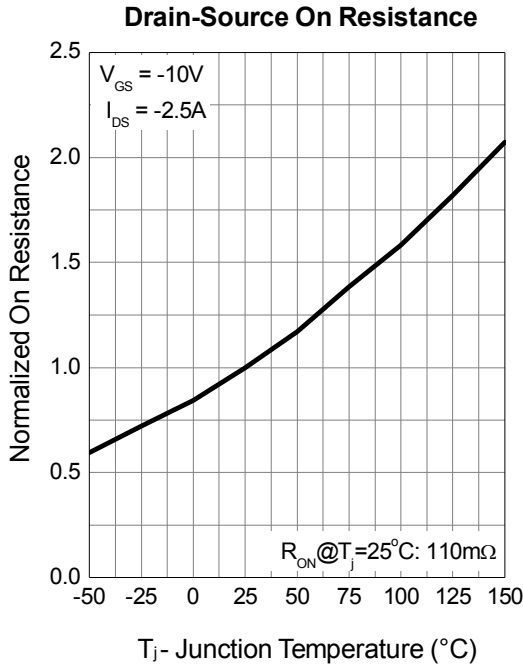
P Channel Typical Operating Characteristics



P Channel Typical Operating Characteristics (Cont.)

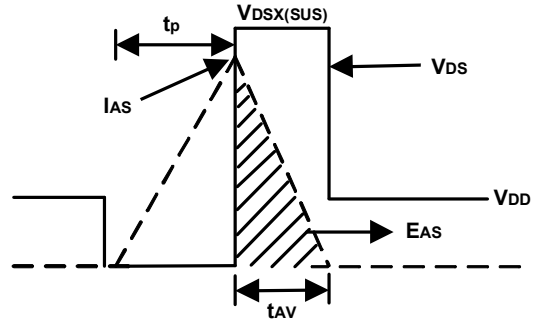
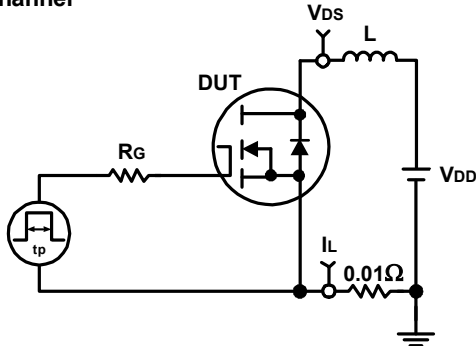


P Channel Typical Operating Characteristics (Cont.)

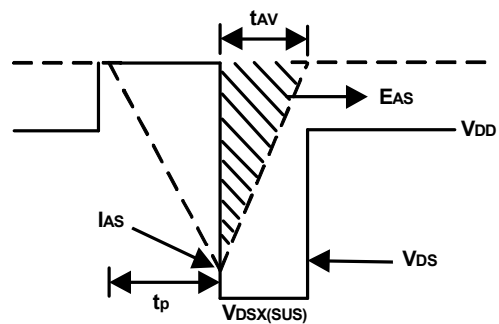
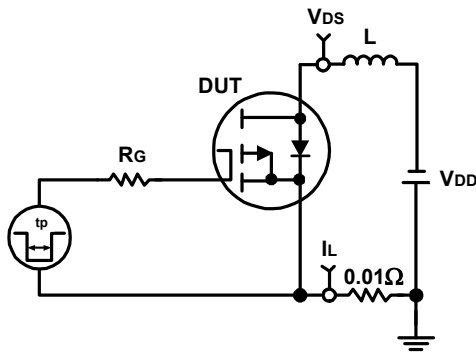


Avalanche Test Circuit and Waveforms

N Channel

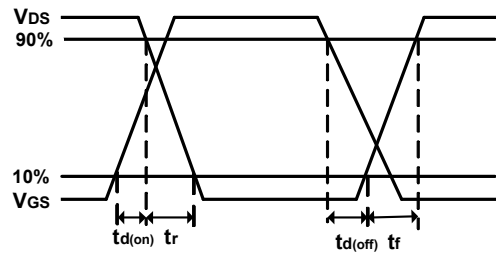
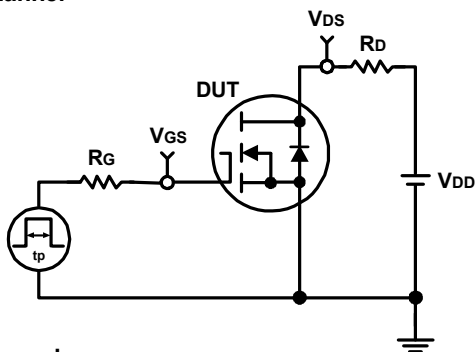


P Channel

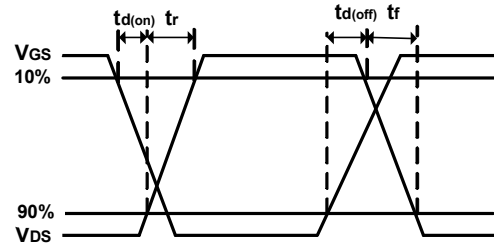
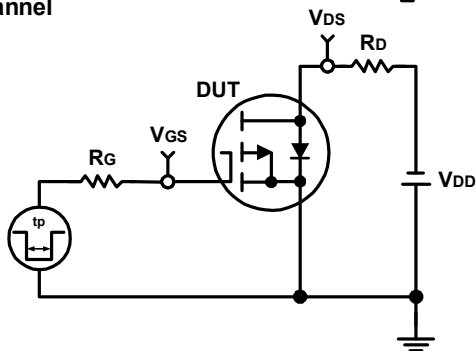


Switching Time Test Circuit and Waveforms

N Channel

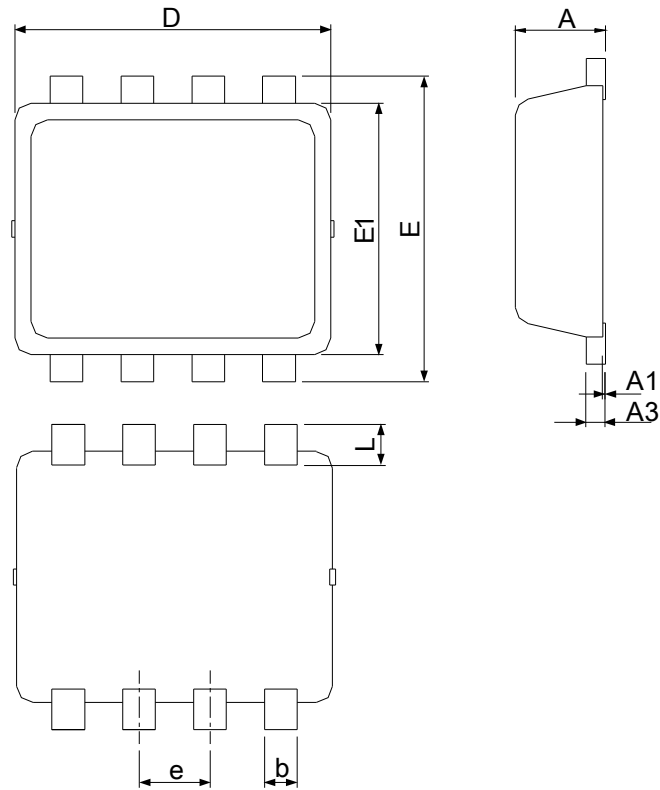


P Channel



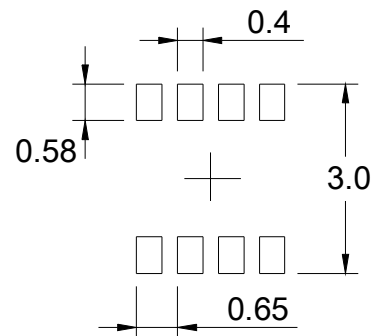
Package Information

DUAL DFN3x3-8(NP)



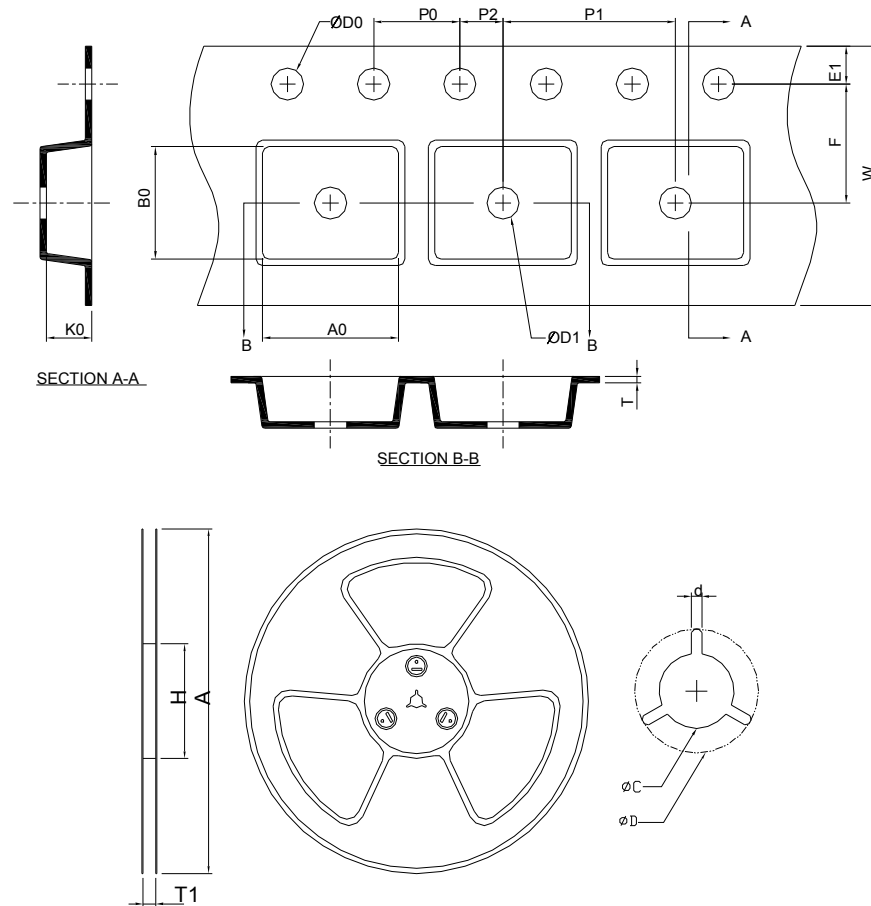
SYMBOL	DUAL DFN3x3-8(NP)			
	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	0.80	1.00	0.032	0.039
A1	0.00	0.05	0.000	0.002
A3	0.08	0.25	0.003	0.010
b	0.24	0.35	0.009	0.014
D	2.80	3.00	0.110	0.118
E	2.60	3.00	0.102	0.118
E1	2.20	2.40	0.087	0.095
e	0.65 BSC		0.026 BSC	
L	0.20	0.45	0.008	0.018

RECOMMENDED LAND PATTERN



UNIT: mm

Carrier Tape & Reel Dimensions

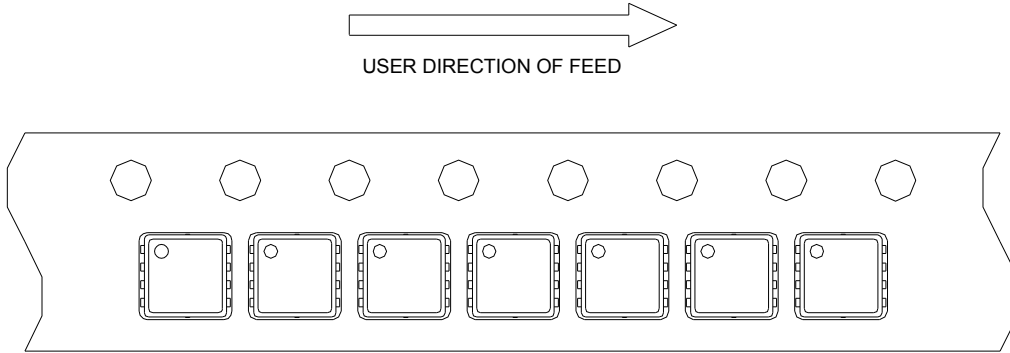


Application	A	H	T1	C	d	D	W	E1	F
DUAL DFN3x3-8 (NP)	330.0±2.00	50 MIN.	12.4+2.00 -0.00	13.0+0.50 -0.20	1.5 MIN.	20.2 MIN.	12.0±0.30	1.75±0.10	5.5±0.05
	P0	P1	P2	D0	D1	T	A0	B0	K0
	4.0±0.10	8.0±0.10	2.0±0.05	1.5+0.10 -0.00	1.5 MIN.	0.6+0.00 -0.40	3.35±0.20	3.35±0.20	1.30±0.20

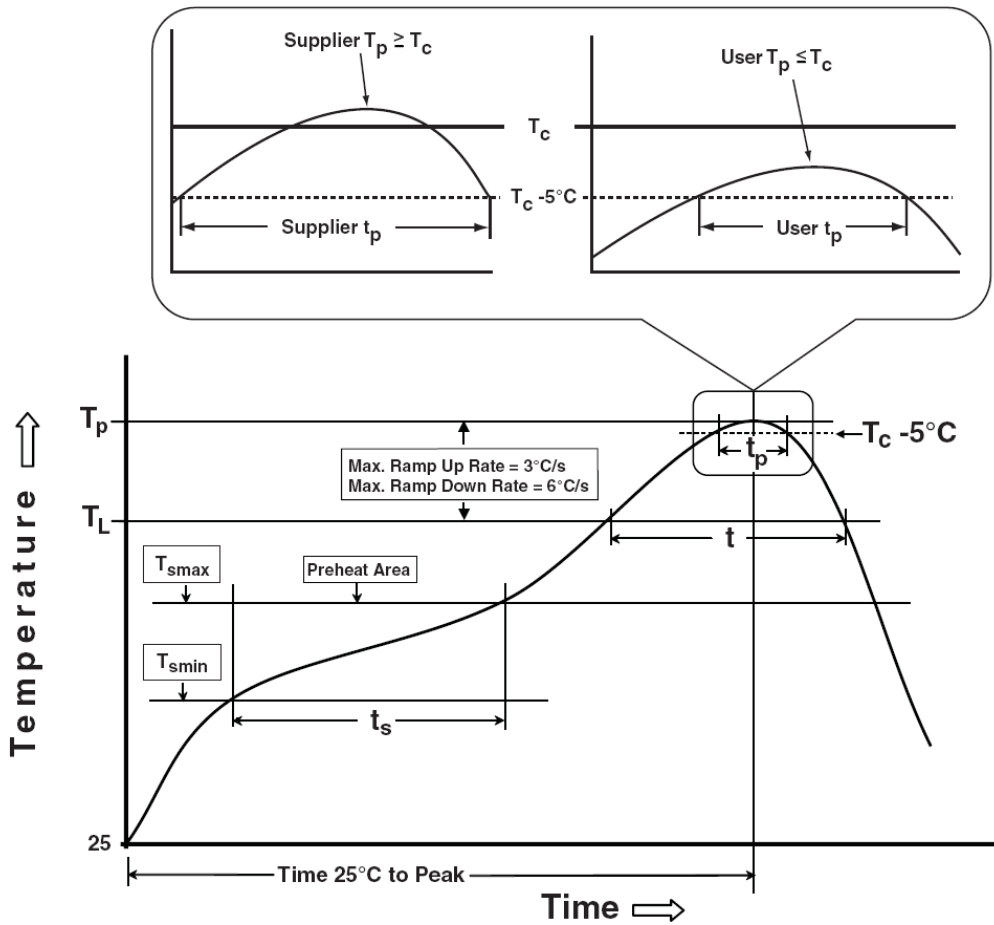
(mm)

Taping Direction Information

DUALDFN3x3-8(NP)



Classification Profile



Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Preheat & Soak		
Temperature min (T_{smin})	100 °C	150 °C
Temperature max (T_{smax})	150 °C	200 °C
Time (T_{smin} to T_{smax}) (t_s)	60-120 seconds	60-120 seconds
Average ramp-up rate (T_{smax} to T_p)	3 °C/second max.	3°C/second max.
Liquidous temperature (T_L)	183 °C	217 °C
Time at liquidous (t_L)	60-150 seconds	60-150 seconds
Peak package body Temperature (T_p)*	See Classification Temp in table 1	See Classification Temp in table 2
Time (t_p)** within 5°C of the specified classification temperature (T_c)	20** seconds	30** seconds
Average ramp-down rate (T_p to T_{smax})	6 °C/second max.	6 °C/second max.
Time 25°C to peak temperature	6 minutes max.	8 minutes max.
* Tolerance for peak profile Temperature (T_p) is defined as a supplier minimum and a user maximum.		
** Tolerance for time at peak profile temperature (t_p) is defined as a supplier minimum and a user maximum.		

Table 1. SnPb Eutectic Process – Classification Temperatures (T_c)

Package Thickness	Volume mm ³ <350	Volume mm ³ ≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2. Pb-free Process – Classification Temperatures (T_c)

Package Thickness	Volume mm ³ <350	Volume mm ³ 350-2000	Volume mm ³ >2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HTRB	JESD-22, A108	1000 Hrs, 80% of VDS max @ T_{jmax}
HTGB	JESD-22, A108	1000 Hrs, 100% of VGS max @ T_{jmax}
PCT	JESD-22, A102	168 Hrs, 100%RH, 2atm, 121°C
TCT	JESD-22, A104	500 Cycles, -65°C~150°C

Customer Service

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