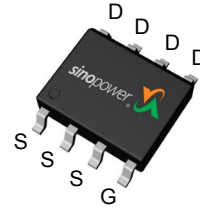


P-Channel Enhancement Mode MOSFET

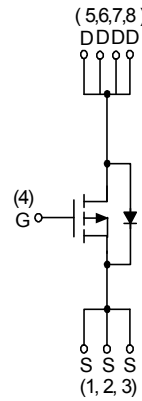
Features

- 30V/-5.4A ,
 $R_{DS(ON)} = 58m\Omega(max.) @ V_{GS} = -10V$
 $R_{DS(ON)} = 86m\Omega(max.) @ V_{GS} = -4.5V$
- Reliable and Rugged
- Lead Free and Green Devices Available (RoHS Compliant)

Pin Description



Top View of SOP – 8



P-Channel MOSFET

Applications

- Power Management in Notebook Computer, Portable Equipment and Battery Powered Systems

Ordering and Marking Information

| | |
|--|--|
| <p>SM9435PS □□-□□□</p> <div style="margin-left: 20px;"> <p>└─ Assembly Material</p> <p>└─ Handling Code</p> <p>└─ Temp. Range</p> <p>└─ Package Code</p> </div> | <p>Package Code K : SOP-8</p> <p>Operating Junction Temp. Range C : -55 to 150 °C</p> <p>Handling Code TR : Tape & Reel (2500ea/reel)</p> <p>Assembly Material G: Halogen and Lead Free Device</p> |
| <p>SM9435PS K :</p> | <div style="border: 1px solid black; display: inline-block; padding: 2px;"> 9435 XXXXX </div> <p>XXXXX - Lot Code</p> |

Note : SINOPOWER lead-free products contain molding compounds/die attach materials and 100% matte tin plate termination finish; which are fully compliant with RoHS. SINOPOWER lead-free products meet or exceed the lead-free requirements of IPC/JEDEC J-STD-020C for MSL classification at lead-free peak reflow temperature. SINOPOWER defines “Green” to mean lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

SINOPOWER reserves the right to make changes to improve reliability or manufacturability without notice, and advise customers to obtain the latest version of relevant information to verify before placing orders.

Absolute Maximum Ratings ($T_A = 25^\circ\text{C}$ unless otherwise noted)

| Symbol | Parameter | Rating | Unit | |
|-------------------|--|---------------------------|------------------|--------------------|
| V_{DSS} | Drain-Source Voltage | -30 | V | |
| V_{GSS} | Gate-Source Voltage | ± 20 | | |
| I_D^* | Continuous Drain Current | $V_{GS} = -10V$ -5.4 | A | |
| I_{DM}^* | Pulsed Drain Current | | | -21 |
| I_S^* | Diode Continuous Forward Current | -2 | A | |
| T_J | Maximum Junction Temperature | 150 | $^\circ\text{C}$ | |
| T_{STG} | Storage Temperature Range | -55 to 150 | | |
| P_D^* | Maximum Power Dissipation | $T_A = 25^\circ\text{C}$ | 2.5 | W |
| | | $T_A = 100^\circ\text{C}$ | 1 | |
| $R_{\theta JA}^*$ | Thermal Resistance-Junction to Ambient | $t \leq 10\text{sec}$ | 50 | $^\circ\text{C/W}$ |
| | | Steady state | 80 | |

Note * : Surface Mounted on 1in^2 pad area, $t \leq 10\text{sec}$.

Electrical Characteristics (T_A = 25°C unless otherwise noted)

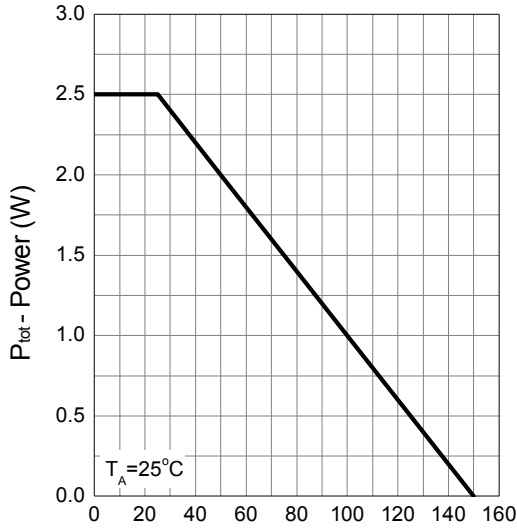
| Symbol | Parameter | Test Condition | Min. | Typ. | Max. | Unit |
|--|----------------------------------|--|------|------|------|------|
| Static Characteristics | | | | | | |
| BV _{DSS} | Drain-Source Breakdown Voltage | V _{GS} =0V, I _{DS} =-250μA | -30 | - | - | V |
| I _{DSS} | Zero Gate Voltage Drain Current | V _{DS} =-24V, V _{GS} =0V T _A =25°C | - | - | -1 | μA |
| | | | - | - | -30 | |
| V _{GS(th)} | Gate Threshold Voltage | V _{DS} =V _{GS} , I _{DS} =-250μA | -1 | -1.5 | -2 | V |
| I _{GSS} | Gate Leakage Current | V _{GS} =±20V, V _{DS} =0V | - | - | ±100 | nA |
| R _{DS(ON)} ^a | Drain-Source On-state Resistance | V _{GS} =-10V, I _{DS} =-5.4A | - | 46 | 58 | mΩ |
| | | V _{GS} =-4.5V, I _{DS} =-2A | - | 64 | 86 | |
| V _{SD} ^a | Diode Forward Voltage | I _{SD} =-1A, V _{GS} =0V | - | - | -1.3 | V |
| Gate Charge Characteristics^b | | | | | | |
| Q _g | Total Gate Charge | V _{DS} =-15V, V _{GS} =-10V, I _{DS} =-5.4A | - | 13 | - | nC |
| Q _{gs} | Gate-Source Charge | | - | 1.3 | - | |
| Q _{gd} | Gate-Drain Charge | | - | 3.1 | - | |
| Dynamic Characteristics^b | | | | | | |
| C _{iss} | Input Capacitance | V _{GS} =0V, V _{DS} =-15V, Frequency=1.0MHz | - | 642 | - | pF |
| C _{oss} | Output Capacitance | | - | 76 | - | |
| C _{rss} | Reverse Transfer Capacitance | | - | 66 | - | |
| t _{d(ON)} | Turn-on Delay Time | V _{DD} =-15V, R _L =15Ω, I _{DS} =-1A, V _{GEN} =-10V, R _G =6Ω | - | 8 | - | ns |
| T _r | Turn-on Rise Time | | - | 13 | - | |
| t _{d(OFF)} | Turn-off Delay Time | | - | 26 | - | |
| T _f | Turn-off Fall Time | | - | 7 | - | |
| t _{rr} | Reverse Recovery Time | I _{DS} =-5.4A, dI _{SD} /dt=100A/μs | - | 13 | - | ns |
| Q _{rr} | Reverse Recovery Charge | | - | 7 | - | nC |

Note a : Pulse test; pulse width≤300μs, duty cycle≤2%.

Note b : Guaranteed by design, not subject to production testing.

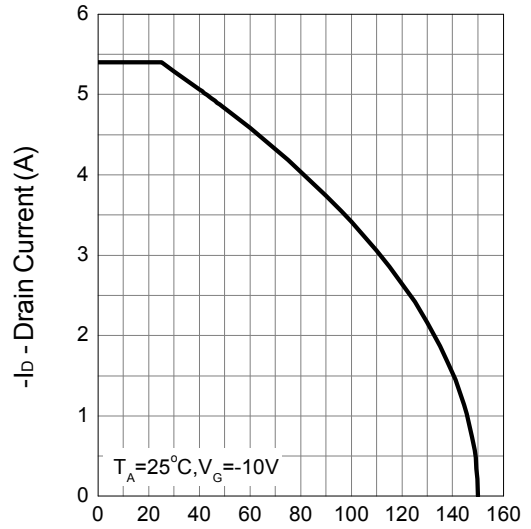
Typical Characteristics

Power Dissipation



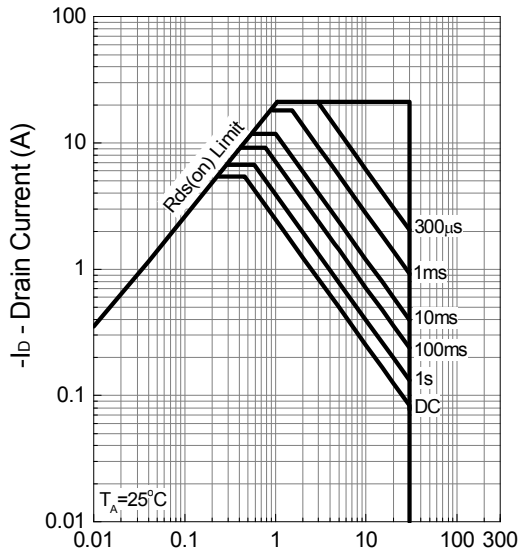
T_j - Junction Temperature (°C)

Drain Current



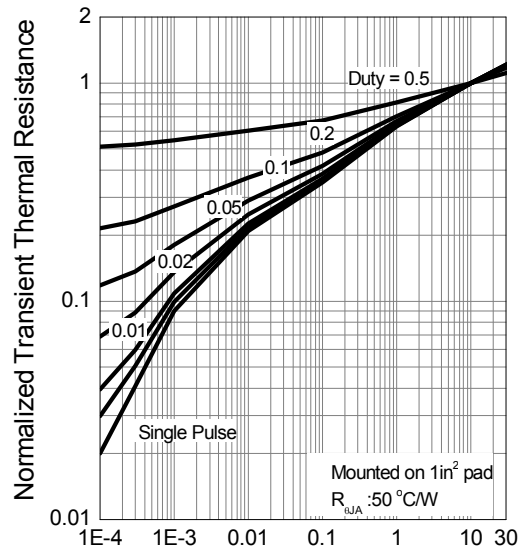
T_j - Junction Temperature (°C)

Safe Operation Area



-V_{DS} - Drain - Source Voltage (V)

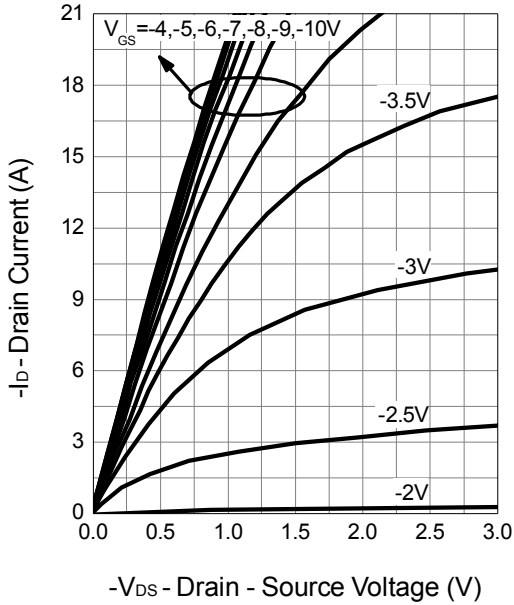
Thermal Transient Impedance



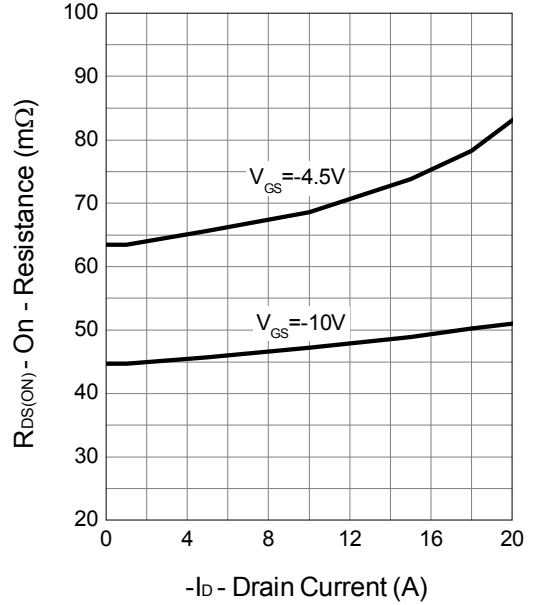
Square Wave Pulse Duration (sec)

Typical Characteristics (Cont.)

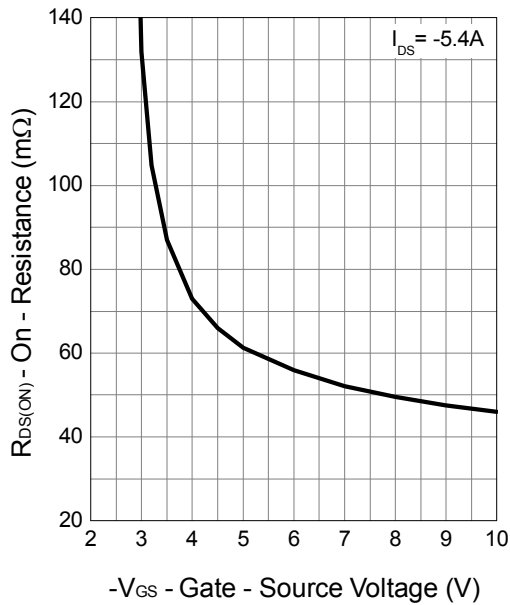
Output Characteristics



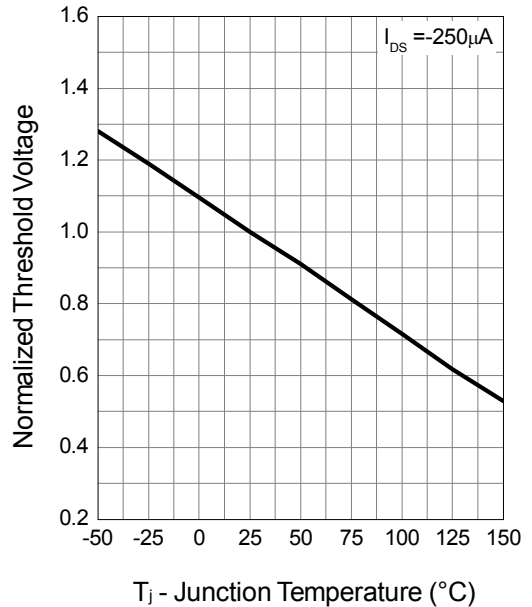
Drain-Source On Resistance



Gate-Source On Resistance

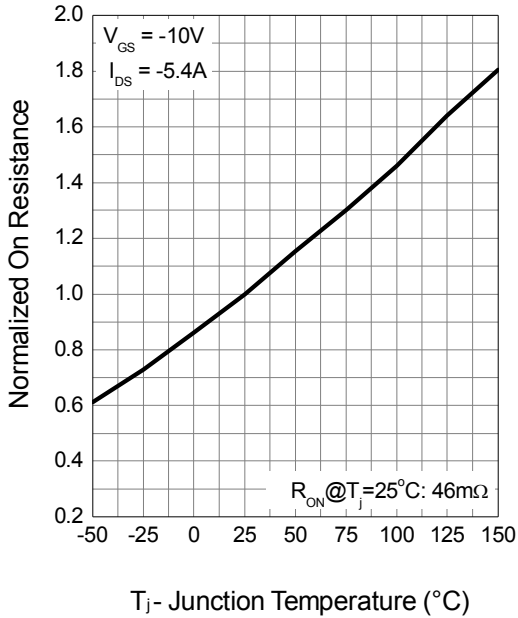


Gate Threshold Voltage

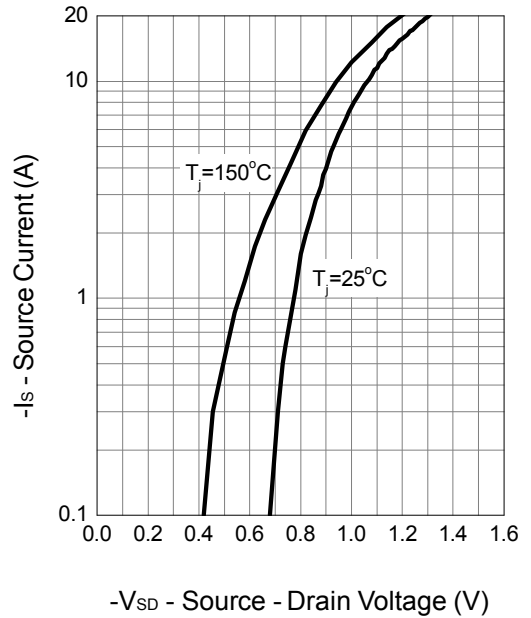


Typical Characteristics (Cont.)

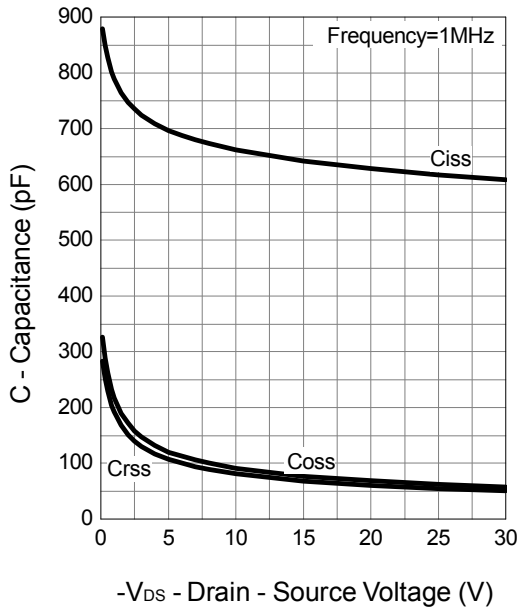
Drain-Source On Resistance



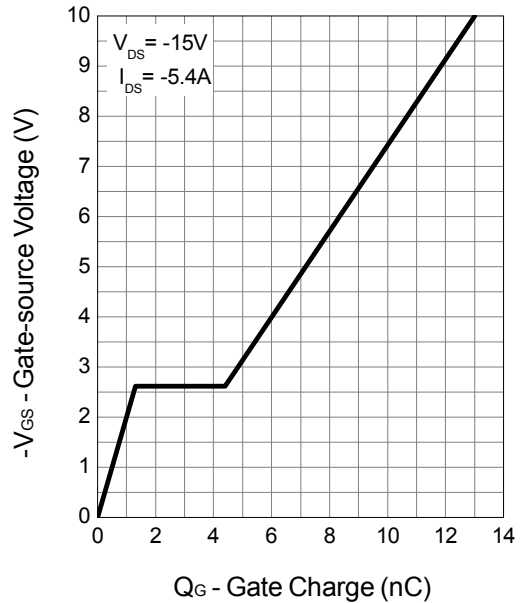
Source-Drain Diode Forward



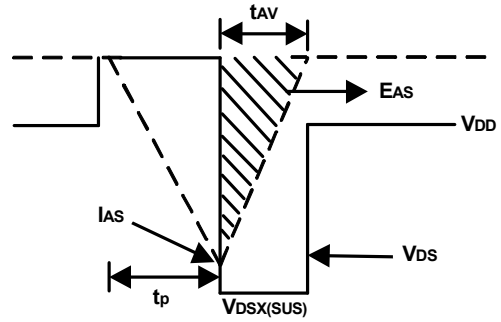
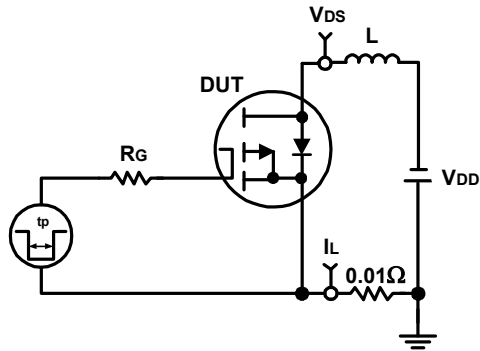
Capacitance



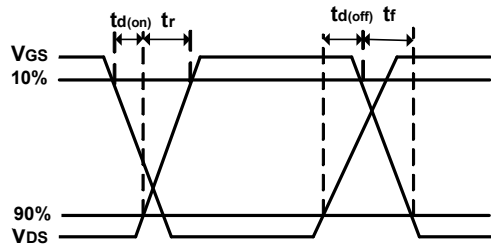
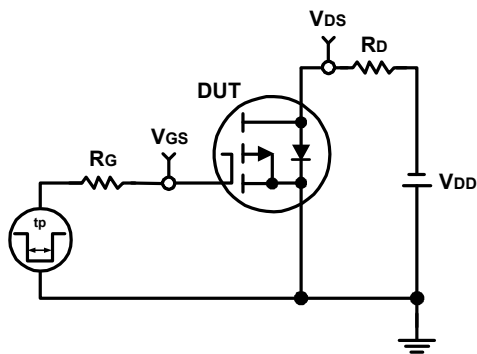
Gate Charge



Avalanche Test Circuit and Waveforms

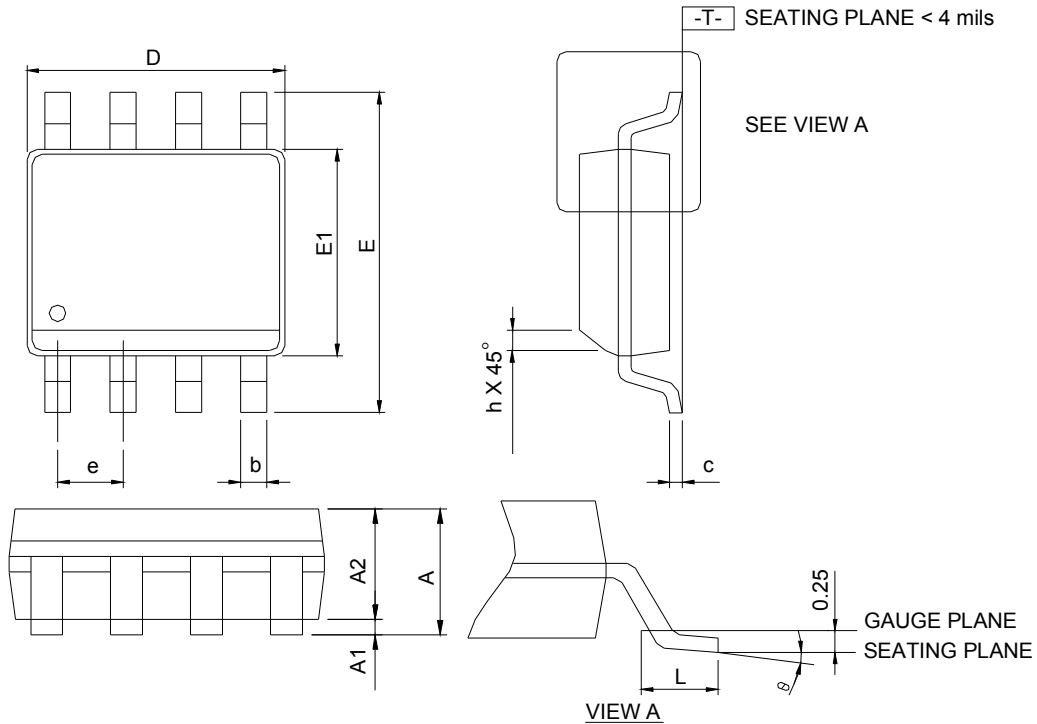


Switching Time Test Circuit and Waveforms



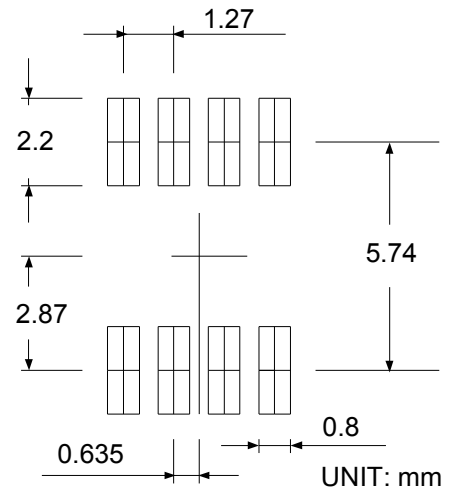
Package Information

SOP-8



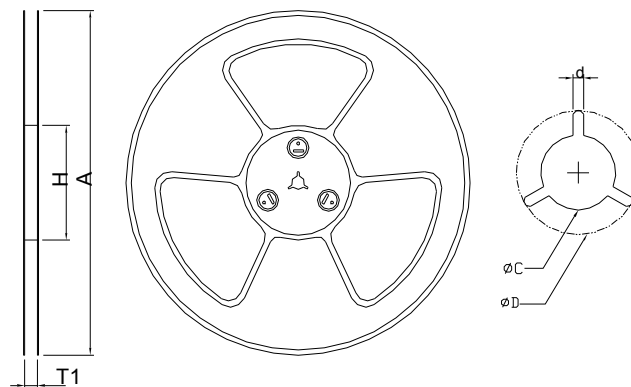
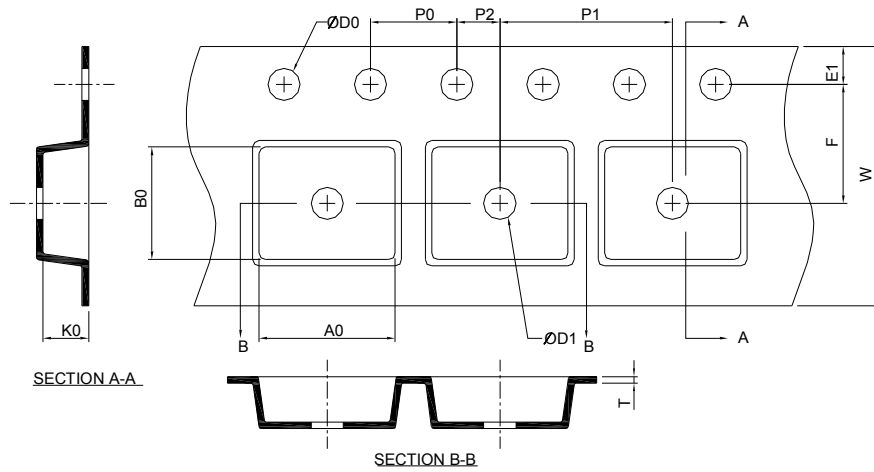
| SYMBOL | SOP-8 | | | |
|--------|-------------|------|-----------|-------|
| | MILLIMETERS | | INCHES | |
| | MIN. | MAX. | MIN. | MAX. |
| A | - | 1.75 | - | 0.069 |
| A1 | 0.10 | 0.25 | 0.004 | 0.010 |
| A2 | 1.25 | - | 0.049 | - |
| b | 0.31 | 0.51 | 0.012 | 0.020 |
| c | 0.17 | 0.25 | 0.007 | 0.010 |
| D | 4.80 | 5.00 | 0.189 | 0.197 |
| E | 5.80 | 6.20 | 0.228 | 0.244 |
| E1 | 3.80 | 4.00 | 0.150 | 0.157 |
| e | 1.27 BSC | | 0.050 BSC | |
| h | 0.25 | 0.50 | 0.010 | 0.020 |
| L | 0.40 | 1.27 | 0.016 | 0.050 |
| θ | 0° | 8° | 0° | 8° |

RECOMMENDED LAND PATTERN



- Note: 1. Follow JEDEC MS-012 AA.
 2. Dimension "D" does not include mold flash, protrusions or gate burrs. Mold flash, protrusion or gate burrs shall not exceed 6 mil per side.
 3. Dimension "E" does not include inter-lead flash or protrusions. Inter-lead flash and protrusions shall not exceed 10 mil per side.

Carrier Tape & Reel Dimensions

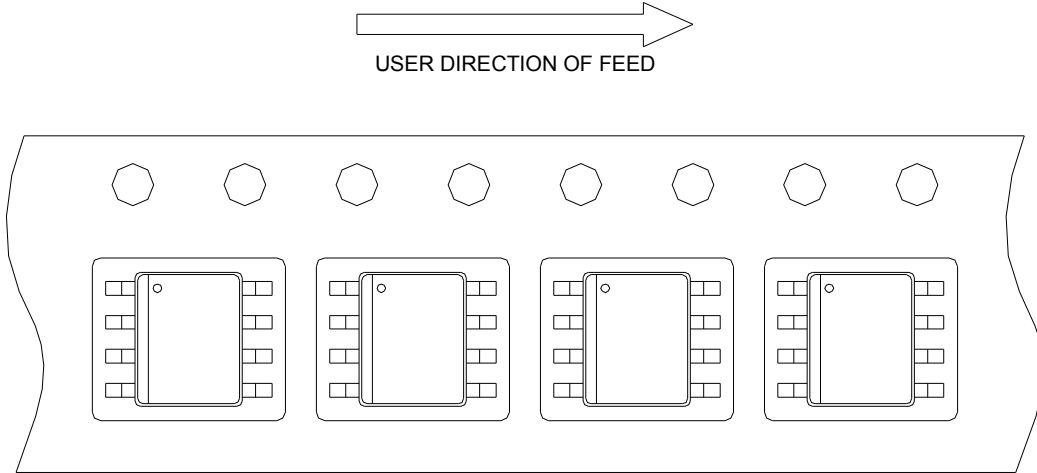


| Application | A | H | T1 | C | d | D | W | E1 | F |
|-------------|------------------|----------------|--------------------------|--------------------------|-----------|-------------------------|-----------------|-----------------|-----------------|
| SOP-8 | 330.0 ± 2.00 | 50 MIN. | $12.4 + 2.00$ -0.00 | $13.0 + 0.50$ -0.20 | 1.5 MIN. | 20.2 MIN. | 12.0 ± 0.30 | 1.75 ± 0.10 | 5.5 ± 0.05 |
| | P0 | P1 | P2 | D0 | D1 | T | A0 | B0 | K0 |
| | 4.0 ± 0.10 | 8.0 ± 0.10 | 2.0 ± 0.05 | $1.5 + 0.10$ -0.00 | 1.5 MIN. | $0.6 + 0.00$ -0.40 | 6.40 ± 0.20 | 5.20 ± 0.20 | 2.10 ± 0.20 |

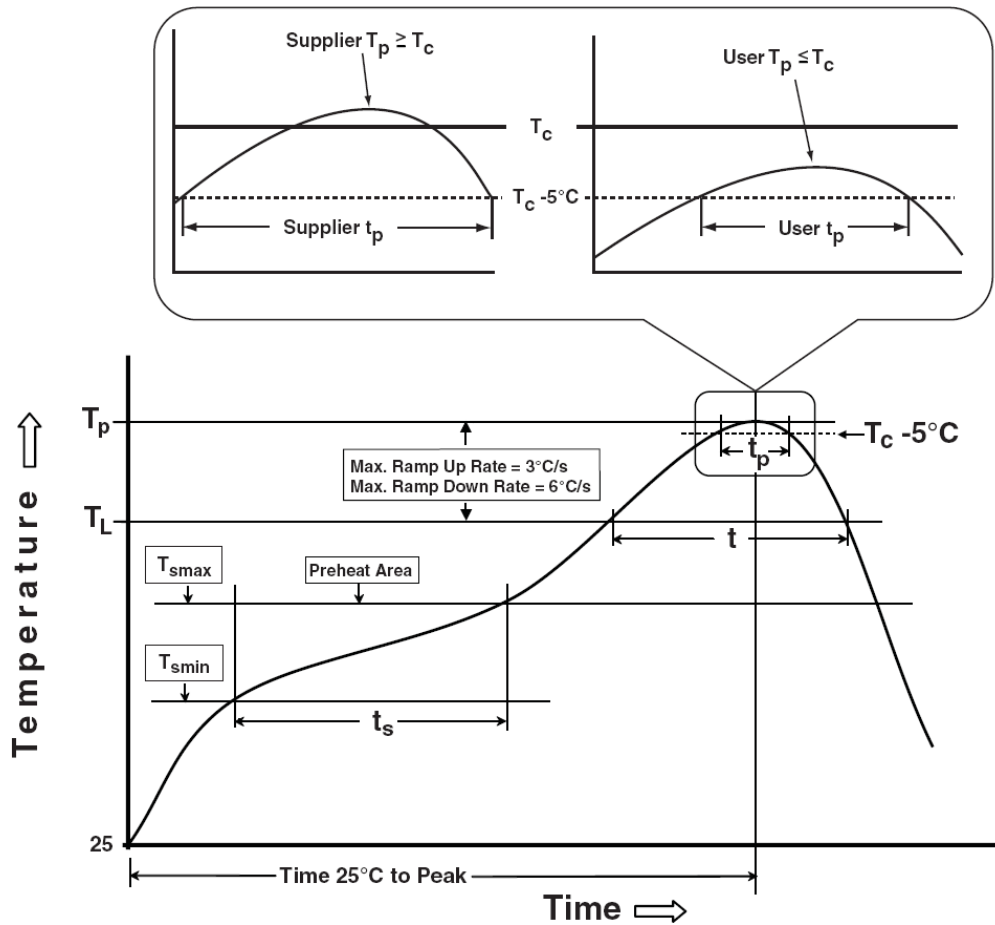
(mm)

Taping Direction Information

SOP-8



Classification Profile



Classification Reflow Profiles

| Profile Feature | Sn-Pb Eutectic Assembly | Pb-Free Assembly |
|---|------------------------------------|------------------------------------|
| Preheat & Soak | | |
| Temperature min (T_{smin}) | 100 °C | 150 °C |
| Temperature max (T_{smax}) | 150 °C | 200 °C |
| Time (T_{smin} to T_{smax}) (t_s) | 60-120 seconds | 60-120 seconds |
| Average ramp-up rate (T_{smax} to T_p) | 3 °C/second max. | 3°C/second max. |
| Liquidous temperature (T_L) | 183 °C | 217 °C |
| Time at liquidous (t_L) | 60-150 seconds | 60-150 seconds |
| Peak package body Temperature (T_p)* | See Classification Temp in table 1 | See Classification Temp in table 2 |
| Time (t_p)** within 5°C of the specified classification temperature (T_c) | 20** seconds | 30** seconds |
| Average ramp-down rate (T_p to T_{smax}) | 6 °C/second max. | 6 °C/second max. |
| Time 25°C to peak temperature | 6 minutes max. | 8 minutes max. |
| * Tolerance for peak profile Temperature (T_p) is defined as a supplier minimum and a user maximum. ** Tolerance for time at peak profile temperature (t_p) is defined as a supplier minimum and a user maximum. | | |

Table 1. SnPb Eutectic Process – Classification Temperatures (T_c)

| Package Thickness | Volume mm ³ <350 | Volume mm ³ ≥350 |
|-------------------|--------------------------------|--------------------------------|
| <2.5 mm | 235 °C | 220 °C |
| ≥2.5 mm | 220 °C | 220 °C |

Table 2. Pb-free Process – Classification Temperatures (T_c)

| Package Thickness | Volume mm ³ <350 | Volume mm ³ 350-2000 | Volume mm ³ >2000 |
|-------------------|--------------------------------|------------------------------------|---------------------------------|
| <1.6 mm | 260 °C | 260 °C | 260 °C |
| 1.6 mm – 2.5 mm | 260 °C | 250 °C | 245 °C |
| ≥2.5 mm | 250 °C | 245 °C | 245 °C |

Reliability Test Program

| Test item | Method | Description |
|---------------|---------------|--|
| SOLDERABILITY | JESD-22, B102 | 5 Sec, 245°C |
| HTRB | JESD-22, A108 | 1000 Hrs, 80% of VDS max @ T_{jmax} |
| HTGB | JESD-22, A108 | 1000 Hrs, 100% of VGS max @ T_{jmax} |
| PCT | JESD-22, A102 | 168 Hrs, 100%RH, 2atm, 121°C |
| TCT | JESD-22, A104 | 500 Cycles, -65°C~150°C |

Customer Service

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