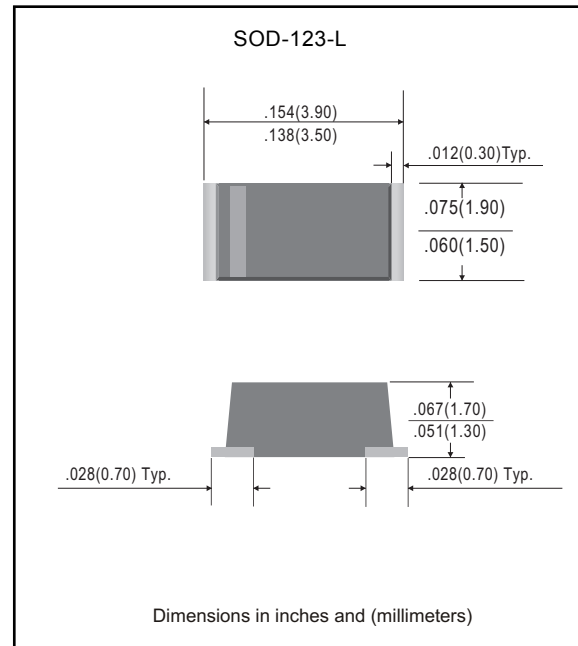


Features

- Plastic package has Underwriters Laboratory Flammability Classification 94V-O Utilizing Flame Retardant Epoxy Molding Compound.
- For surface mounted applications.
- Exceeds environmental standards of ML-S-19500 / 228
- Low leakage current
- RoHS product for packing code suffix "G"
Halogen free product for packing code suffix "H"

Mechanical data

- Epoxy : UL94-V0 rated flame retardant
- Case : Molded plastic, SOD-123-L / MINI SMA
- Terminals :Plated terminals, solderable per MIL-STD-750, Method 2026
- Polarity : Indicated by cathode band
- Mounting Position : Any
- Weight : Approximated 0.018 gram



MAXIMUM RATINGS (AT $T_A=25^{\circ}\text{C}$ unless otherwise noted)

PARAMETER	CONDITIONS	Symbol	MIN.	TYP.	MAX.	UNIT
Forward rectified current	Ambient temperature = 55°C	I_O			1.0	A
Forward surge current	8.3ms single half sine-wave superimposed on rate load (JEDEC methode)	I_{FSM}			30	A
Reverse current	$V_R = V_{RRM}$ $T_A = 25^{\circ}\text{C}$	I_R			5.0	μA
	$V_R = V_{RRM}$ $T_A = 100^{\circ}\text{C}$				100	μA
Thermal resistance	Junction to ambient	R_{JA}		42		$^{\circ}\text{C} / \text{w}$
Thermal resistance	Junction to case	R_{JC}		40		$^{\circ}\text{C} / \text{w}$
Diode junction capacitance	f=1MHz and applied 4vDC reverse voltage	C_J		15		pF
Storage temperature		T_{STG}	-55		+150	$^{\circ}\text{C}$

SYMBOLS	MARKING CODE	V_{RRM}^{*1} (V)	V_{RMS}^{*2} (V)	V_R^{*3} (V)	V_F^{*4} (V)	T_{RR}^{*5} (nS)	Operating temperature ($^{\circ}\text{C}$)
FFM101-M	F1	50	35	50	1.3	150	-55 to +150
FFM102-M	F2	100	70	100			
FFM103-M	F3	200	140	200			
FFM104-M	F4	400	280	400		250	
FFM105-M	F5	600	420	600			
FFM106-M	F6	800	560	800		500	
FFM107-M	F7	1000	700	1000			

*1 Repetitive peak reverse voltage

*2 RMS voltage

*3 Continuous reverse voltage

*4 Maximum forward voltage@ $I_F=1.0\text{A}$

*5 Maximum Reverse recovery time, note 1

Note 1. Reverse recovery time test condition, $I_F=0.5\text{A}$, $I_R=1.0\text{A}$, $I_{RR}=0.25\text{A}$

RATING AND CHARACTERISTIC CURVES

FIG.1-TYPICAL FORWARD CHARACTERISTICS

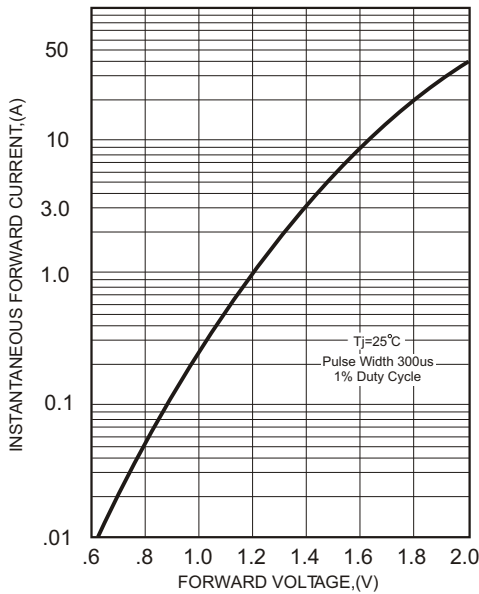


FIG.2-TYPICAL FORWARD CURRENT DERATING CURVE

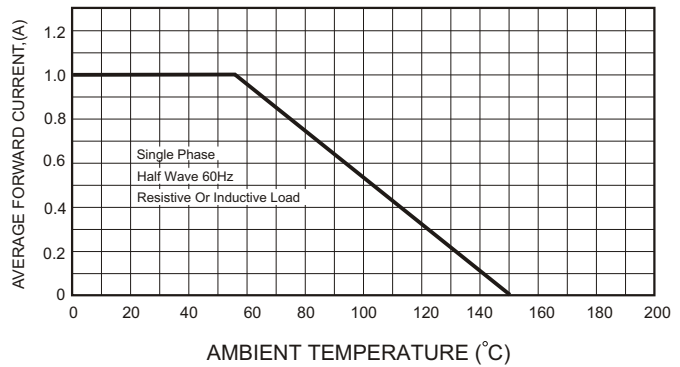
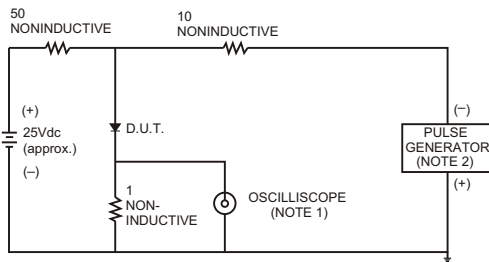


FIG.3- TEST CIRCUIT DIAGRAM AND REVERSE RECOVERY TIME CHARACTERISTICS



NOTES: 1. Rise Time= 7ns max., Input Impedance= 1 megohm, 22pF.
2. Rise Time= 10ns max., Source Impedance= 50 ohms.

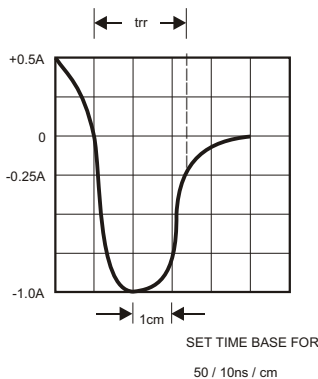


FIG.4-MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

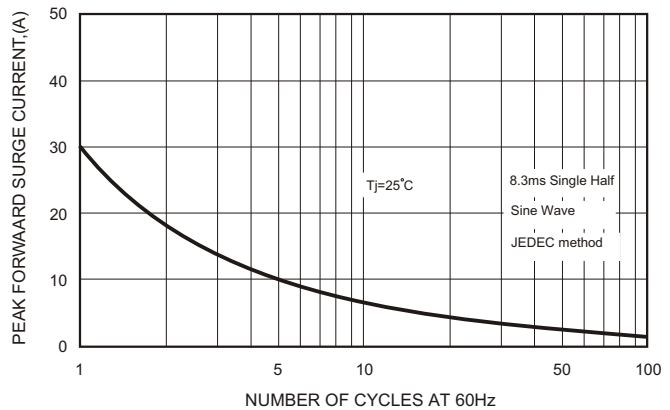
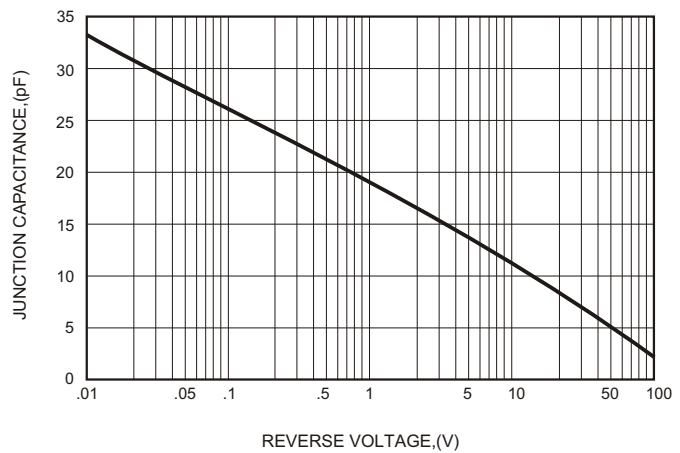




FIG.5-TYPICAL JUNCTION CAPACITANCE



**1.0A Surface Mount : UghF YWcj YfmRectifiers -50V- 1000V
SC8!%&' !@PACKAGE**

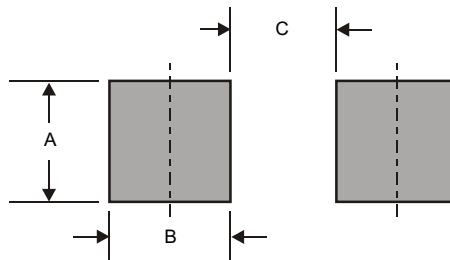
Pinning information

Pin	Simplified outline	Symbol
Pin1 cathode Pin2 anode		

Marking

Type number	Marking code
FFM101-M	F1
FFM102-M	F2
FFM103-M	F3
FFM104-M	F4
FFM105-M	F5
FFM106-M	F6
FFM107-M	F7

Suggested solder pad layout



Dimensions in inches and (millimeters)

PACKAGE	A	B	C
SOD-123-L	0.075 (1.90)	0.055 (1.40)	0.075 (1.90)