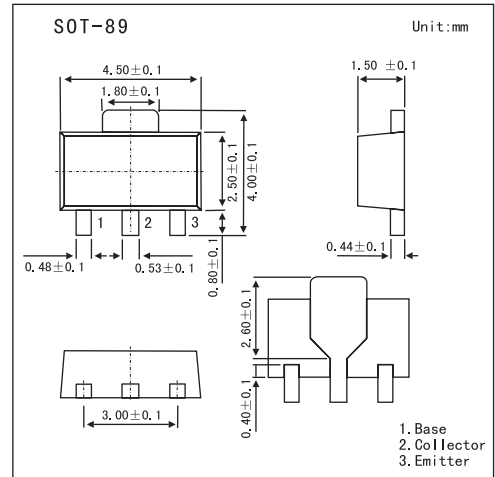


# 2SD1622

### ■ Features

- Adoption of FBET process..
- Very small size making it easy to provide highdensity hybrid ICs.



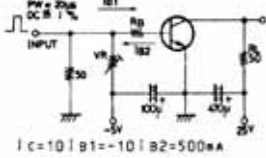
### ■ Absolute Maximum Ratings Ta = 25°C

Parameter	Symbol	Rating	Unit
Collector-base voltage	V <sub>CBO</sub>	60	V
Collector-emitter voltage	V <sub>CEO</sub>	50	V
Emitter-base voltage	V <sub>EB0</sub>	5	V
Collector current	I <sub>C</sub>	1	A
Collector current (pulse)	I <sub>CP</sub>	2	A
Collector dissipation	P <sub>C</sub>	500	mW
	P <sub>C</sub> *	1.3	W
Junction temperature	T <sub>J</sub>	150	°C
Storage temperature	T <sub>stg</sub>	-55 to +150	°C

\* Mounted on ceramic board(250mm2X0.8mm)

## 2SD1622

■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Testconditons	Min	Typ	Max	Unit	
Collector cutoff current	IcBO	V <sub>CB</sub> = 50 V, I <sub>E</sub> =0			100	nA	
Emitter cutoff current	I <sub>E</sub> BO	V <sub>EB</sub> = 4 V, I <sub>C</sub> =0			100	nA	
DC current gain	h <sub>FE</sub>	V <sub>CE</sub> = 2 V, I <sub>C</sub> = 100 mA	100		560		
Gain bandwidth product	f <sub>T</sub>	V <sub>CE</sub> = 10 V, I <sub>C</sub> = 50 mA		150		MHz	
Output capacitance	C <sub>ob</sub>	V <sub>CB</sub> = 10 V, f = 1.0MHz		8.5		pF	
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>	I <sub>C</sub> = 500 mA, I <sub>B</sub> = 50 mA		120	300	mV	
Base-emitter saturation voltage	V <sub>BE(sat)</sub>	I <sub>C</sub> = 500 mA, I <sub>B</sub> = 50 mA		0.9	1.2	V	
Collector-base breakdown voltage	V <sub>(BR)CBO</sub>	I <sub>C</sub> = 10μA, I <sub>E</sub> = 0	60			V	
Collector-emitter breakdown voltage	V <sub>(BR)CEO</sub>	I <sub>C</sub> = 1mA, R <sub>BE</sub> = ∞	50			V	
Emitter-base breakdown voltage	V <sub>(BR)EBO</sub>	I <sub>E</sub> = 10μA, I <sub>C</sub> = 0	5			V	
Turn-on timie	ton	<b>Switching Time Test Circuit</b> 		40		ns	
Storage time	tstg				350		ns
Turn-off time	tf				30		ns

■ hFE Classification

Marking	DE			
	R	S	T	U
hFE	100~200	140~280	200~400	280~560