

Silicon NPN Power Transistors

BU2506AF

DESCRIPTION

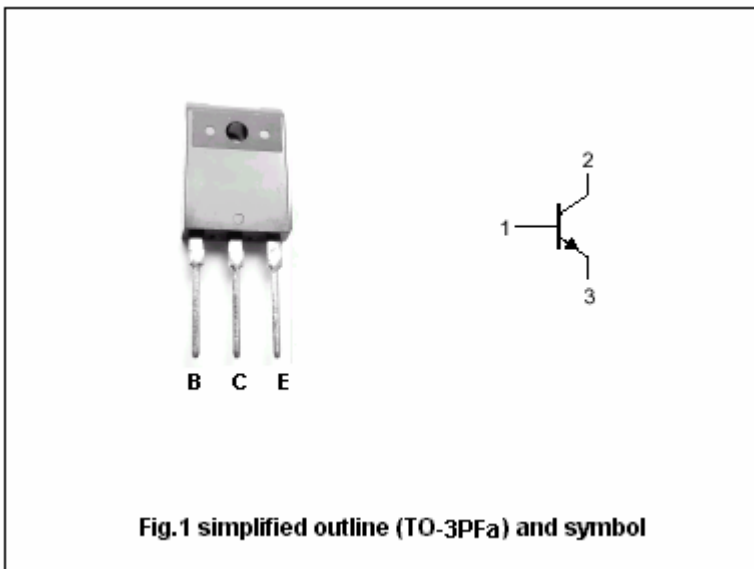
- With TO-3PFa package
- High voltage
- High speed switching

APPLICATIONS

- Intended for use in horizontal deflection circuits of colour TV receivers

PINNING

PIN	DESCRIPTION
1	Base
2	Collector
3	Emitter



Absolute maximum ratings(Ta=25°C)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V _{CBO}	Collector-base voltage	Open emitter	1500	V
V _{CEO}	Collector-emitter voltage	Open base	700	V
I _C	Collector current (DC)		5	A
I _{CM}	Collector current (Pulse)		8	A
I _B	Base Collector current (DC)		3	A
I _{BM}	Base current (Pulse)		5	A
P _{tot}	Total power dissipation	T _C =25°C	45	W
T _j	Max.operating junction temperature		150	°C
T _{stg}	Storage temperature		-65~150	°C

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CHARACTERISTICS

T_j=25 °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CEQ(SUS)}	Collector-emitter sustaining voltage	I _C =100mA ; I _B =0,	700			V
V _{(BR)EBO}	Emitter-base breakdown voltage	I _E =1mA ; I _C =0	7.5			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =3A ; I _B =0.79A			5.0	V
V _{BEsat}	Base-emitter saturation voltage	I _C =3A ; I _B =0.79A			1.1	V
I _{CEs}	Collector cut-off current	V _{CE} =RatedV _{CE} ; V _{BE} =0 T _j =125 °C			1.0 2.0	mA
I _{EBO}	Emitter cut-off current	V _{EB} =7.5V ; I _C =0			0.1	mA
h _{FE-1}	DC current gain	I _C =0.3A ; V _{CE} =5V		12		
h _{FE-2}	DC current gain	I _C =3A ; V _{CE} =5V	3.8	5.5	7.5	

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PACKAGE OUTLINE



Fig.2 Outline dimensions (unindicated tolerance:±0.30mm)