

Silicon NPN Power Transistors

2SD627

**DESCRIPTION**

- With TO-3 package
- High voltage,high speed

**APPLICATIONS**

- For use in horizontal deflection output stages for color TV receivers

**PINNING(see Fig.2)**

PIN	DESCRIPTION
1	Base
2	Emitter
3	Collector

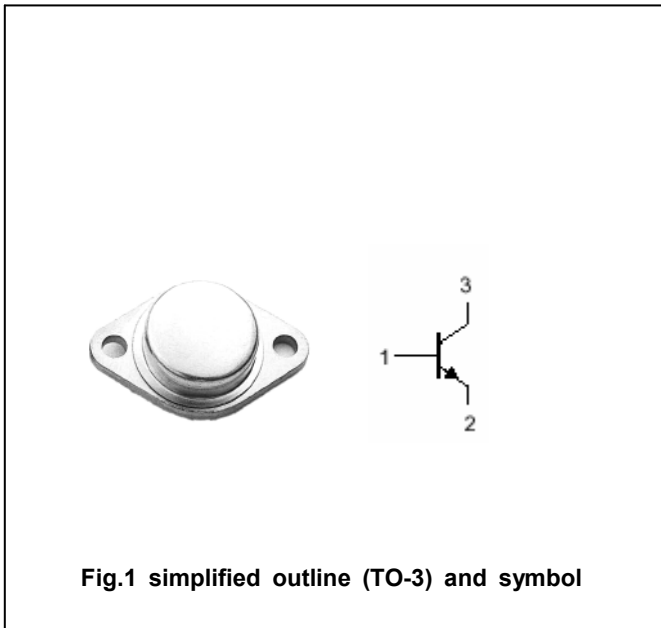


Fig.1 simplified outline (TO-3) and symbol

**Absolute maximum ratings(Ta=□)**

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V <sub>CBO</sub>	Collector-base voltage	Open emitter	1500	V
V <sub>CEO</sub>	Collector-emitter voltage	Open base	600	V
V <sub>EBO</sub>	Emitter-base voltage	Open collector	6	V
I <sub>C</sub>	Collector current		3	A
I <sub>CM</sub>	Collector current-peak		6	A
P <sub>C</sub>	Collector power dissipation	T <sub>C</sub> =25□	50	W
T <sub>j</sub>	Junction temperature		150	□
T <sub>stg</sub>	Storage temperature		-45~150	□

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## CHARACTERISTICS

T<sub>j</sub>=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>CEO(SUS)</sub>	Collector-emitter sustaining voltage	I <sub>C</sub> =100mA ; I <sub>B</sub> =0	600			V
V <sub>CEsat</sub>	Collector-emitter saturation voltage	I <sub>C</sub> =2.5A; I <sub>B</sub> =0.6A			10	V
V <sub>BEsat</sub>	Base-emitter saturation voltage	I <sub>C</sub> =2.5A; I <sub>B</sub> =0.6A			1.6	V
I <sub>CBO</sub>	Collector cut-off current	V <sub>CB</sub> =1000V; I <sub>E</sub> =0			50	μA
I <sub>EBO</sub>	Emitter cut-off current	V <sub>EB</sub> =6V; I <sub>C</sub> =0			1.0	mA
h <sub>FE</sub>	DC current gain	I <sub>C</sub> =2A ; V <sub>CE</sub> =10V	5		25	
t <sub>f</sub>	Fall time	I <sub>CP</sub> =2.5A			1.0	μs

PACKAGE OUTLINE

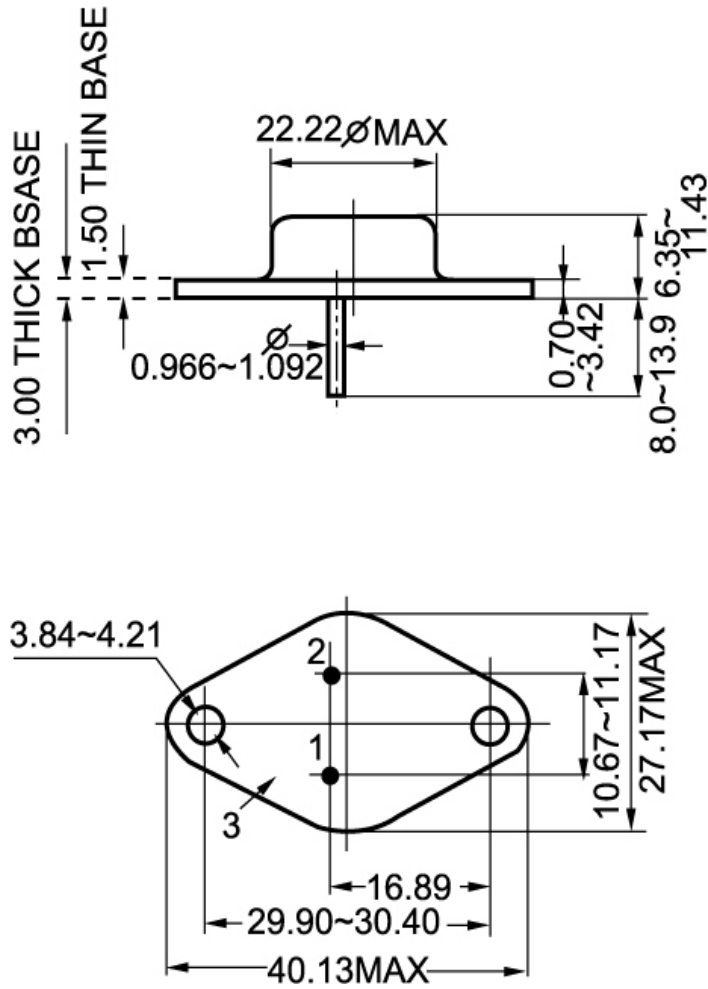


Fig.2 outline dimensions (unindicated tolerance:±0.1mm)