

Silicon NPN Power Transistors

2SC1514

DESCRIPTION

- With TO-220F package
- High Voltage
- High frequency

APPLICATIONS

- TV chroma, video, audio output applications

PINNING

PIN	DESCRIPTION
1	Base
2	Collector
3	Emitter

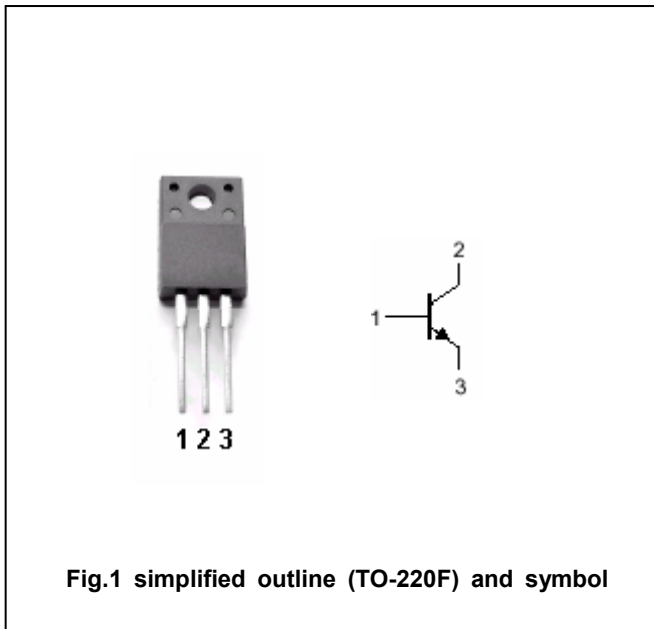


Fig.1 simplified outline (TO-220F) and symbol

Absolute maximum ratings (Ta=25°C)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V _{CBO}	Collector-base voltage	Open emitter	300	V
V _{CEO}	Collector-emitter voltage	Open base	300	V
V _{EBO}	Emitter-base voltage	Open collector	7	V
I _C	Collector current		0.1	A
I _{CM}	Collector current-peak		0.2	A
P _C	Collector power dissipation	T _a =25°C	1.25	W
		T _C =25°C	15	
T _j	Junction temperature		150	°C
T _{stg}	Storage temperature		-40~150	°C

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CHARACTERISTICS

T_j=25 °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CEsat}	Collector-emitter saturation voltage	I _C =20mA I _B =2mA			1.5	V
V _{BE}	Base-emitter on voltage	I _C =20mA ; V _{CE} =20V			1.2	V
V _{(BR)CBO}	Collector-base breakdown voltage	I _C =10μA; I _E =0	300			V
V _{(BR)CEO}	Collector-emitter breakdown voltage	I _C =100μA; I _B =0	300			V
V _{(BR)EBO}	Emitter-base breakdown voltage	I _E =10μA; I _C =0	7			V
h _{FE}	DC current gain	I _C =20mA ; V _{CE} =20V	30		200	
C _{OB}	Output capacitance	I _E =0; V _{CB} =30V; f=1MHz			4	pF
f _T	Transition frequency	I _E =20mA ; V _{CB} =20V	50			MHz

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PACKAGE OUTLINE

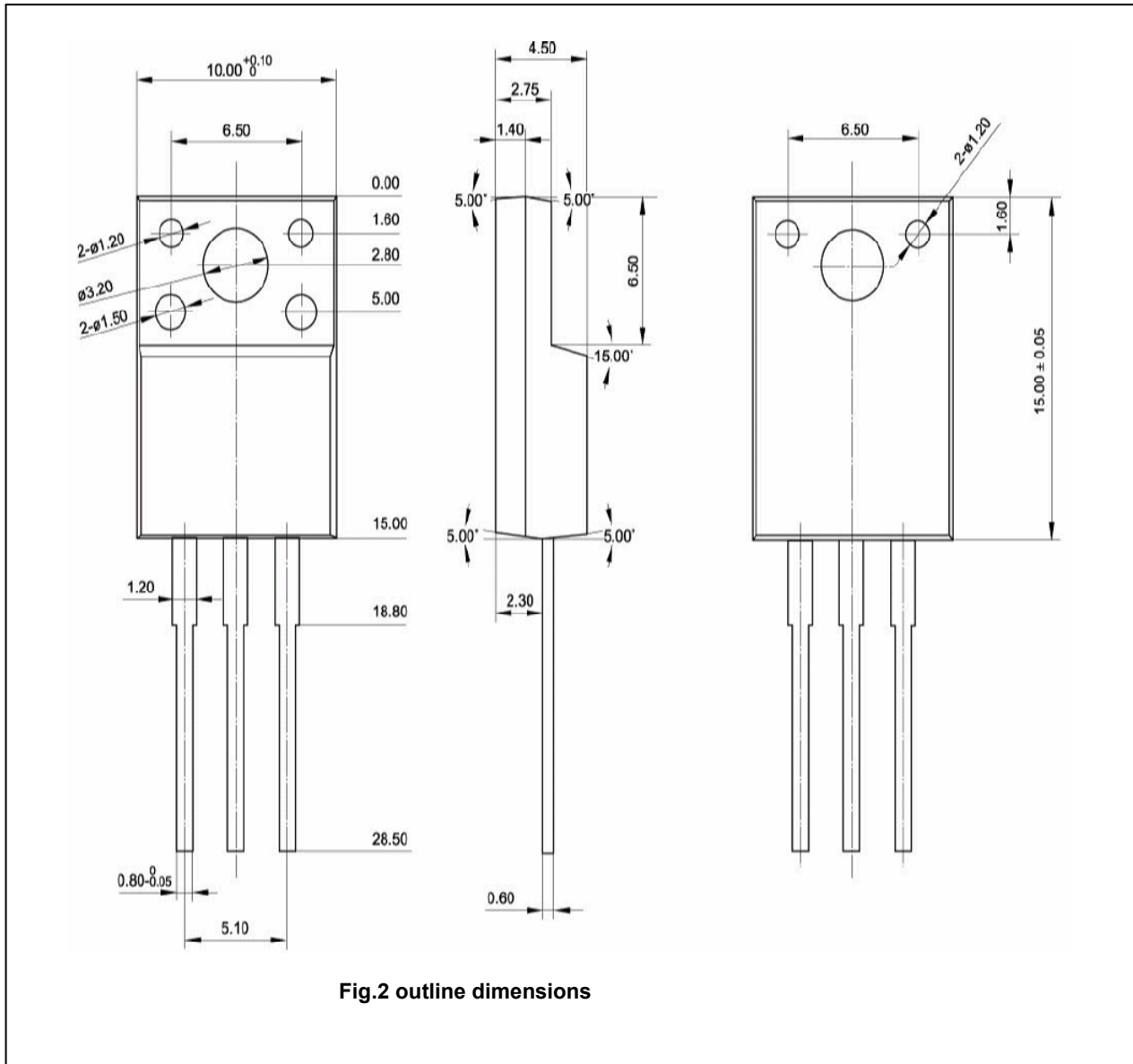


Fig.2 outline dimensions