

Silicon PNP Power Transistors

2SB1086A

DESCRIPTION

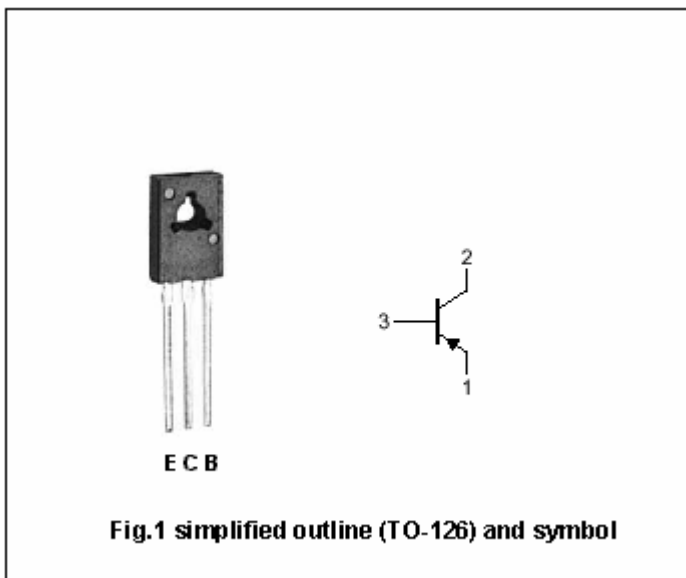
- With TO-126 package
- Complement to type 2SD1563A
- Low collector saturation voltage
- Large current capability

APPLICATIONS

- Designed for use in low frequency power amplifier applications

PINNING

PIN	DESCRIPTION
1	Emitter
2	Collector;connected to mounting base
3	Base



Absolute maximum ratings(Ta=25°C)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V _{CBO}	Collector-base voltage	Open emitter	-160	V
V _{CEO}	Collector-emitter voltage	Open base	-160	V
V _{EBO}	Emitter-base voltage	Open collector	-5	V
I _C	Collector current (DC)		-1.5	A
I _{CM}	Collector current -peak		-3.0	A
P _D	Total power dissipation	T _C =25°C	10	W
T _j	Junction temperature		150	°C
T _{stg}	Storage temperature		-55~150	°C

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CHARACTERISTICS

T_j=25 °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-emitter breakdown voltage	I _C =-1mA ; I _B =0	-160			V
V _{(BR)CBO}	Collector-base breakdown voltage	I _C =-50μA ; I _E =0	-160			V
V _{(BR)EBO}	Emitter-base breakdown voltage	I _E =-50μA ; I _C =0	-5			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =-1.0A ; I _B =-0.1A			-2.0	V
V _{BEsat}	Base-emitter saturation voltage	I _C =-1.0A ; I _B =-0.1A			-1.5	V
I _{CBO}	Collector cut-off current	V _{CB} =-120V ; I _E =0			-1.0	μA
I _{EBO}	Emitter cut-off current	V _{EB} =-4V ; I _C =0			-1.0	μA
h _{FE}	DC current gain	I _C =-0.1A ; V _{CE} =-5V	56		270	
f _T	Transition frequency	I _C =-0.1A ; V _{CE} =-5V		50		MHz
C _{OB}	Output capacitance	I _E =0 ; V _{CB} =-10V ; f=1MHz		30		pF

PACKAGE OUTLINE

