

Silicon PNP Power Transistors

2SA1489

DESCRIPTION

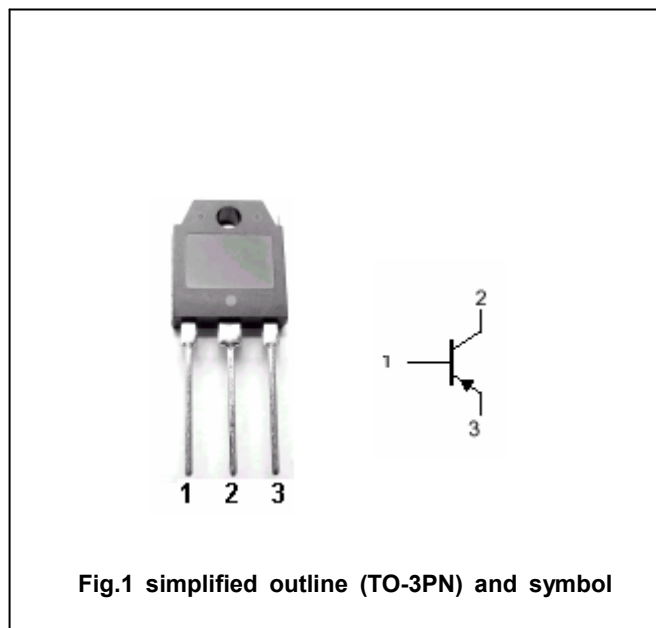
- With TO-3PN package
- Complement to type 2SC3853

APPLICATIONS

- Audio and general purpose

PINNING

PIN	DESCRIPTION
1	Base
2	Collector;connected to mounting base
3	Emitter

**Absolute maximum ratings(Ta=□)**

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V_{CBO}	Collector-base voltage	Open emitter	-80	V
V_{CEO}	Collector-emitter voltage	Open base	-80	V
V_{EBO}	Emitter-base voltage	Open collector	-6	V
I_C	Collector current		-6	A
I_B	Base current		-3	A
P_C	Collector power dissipation	$T_C=25^\circ$	60	W
T_j	Junction temperature		150	□
T_{stg}	Storage temperature		-55~150	□

Silicon PNP Power Transistors

2SA1489

CHARACTERISTICS

T_j=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-emitter breakdown voltage	I _C =-50mA ; I _B =0	-80			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =-2A; I _B =-0.2A			-1.5	V
I _{CBO}	Collector cut-off current	V _{CB} =-80V; I _E =0			-100	μA
I _{EBO}	Emitter cut-off current	V _{EB} =-6V; I _C =0			-100	μA
h _{FE}	DC current gain	I _C =-2A ; V _{CE} =-4V	50			
f _T	Transition frequency	I _C =0.5A ; V _{CE} =-12V		20		MHz

Switching times

t _{on}	Turn-on time	I _C =-3A; R _L =10Ω I _{B1} =-I _{B2} =-0.3A V _{CC} =30V		0.25		μs
t _s	Storage time			0.5		μs
t _f	Fall time			0.1		μs

PACKAGE OUTLINE

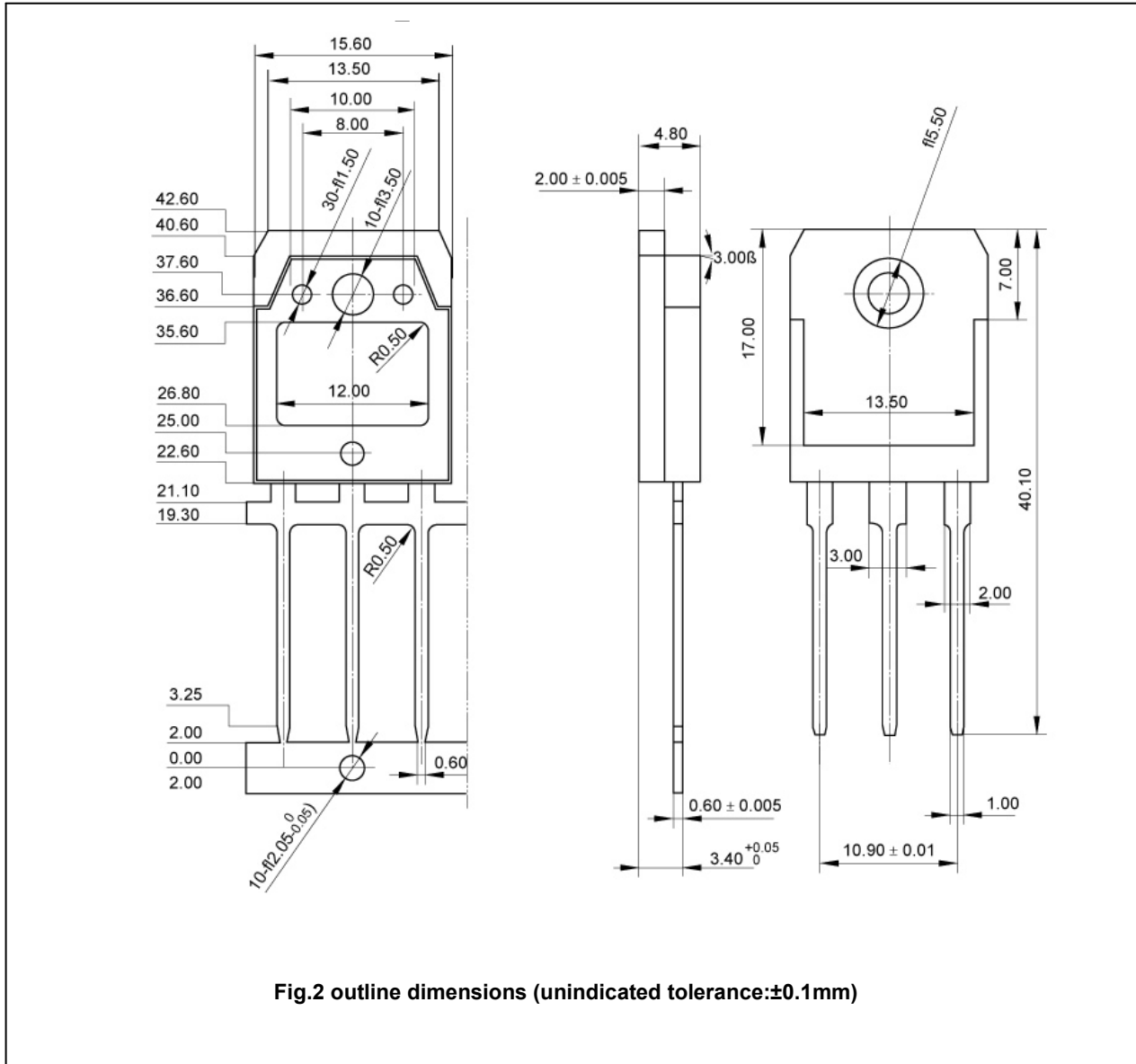


Fig.2 outline dimensions (unindicated tolerance:±0.1mm)