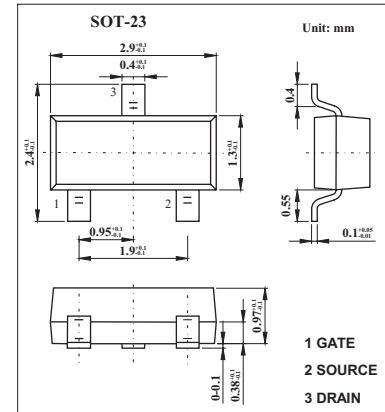


## MOS Fied Effect Transistor

### 2SJ210

#### ■ Features

- Directly driven by lcs having a 5V poer supply.
- Not necessary to consider driving current because of its high input impedance.
- Possible to reduce the number of parts by omitting the biasresistor.



#### ■ Absolute Maximum Ratings Ta = 25°C

Parameter	Symbol	Rating	Unit
Drain to source voltage V <sub>Gs</sub> =0	V <sub>DSS</sub>	-60	V
Gate to source voltage V <sub>Ds</sub> =0	V <sub>GSS</sub>	±20	V
Drain current (DC)	I <sub>D</sub>	±200	m A
Drain current(pulse) *	I <sub>D</sub>	±400	m A
Power dissipation	P <sub>D</sub>	200	m W
Channel temperature	T <sub>ch</sub>	150	°C
Storage temperature	T <sub>stg</sub>	-55 to +150	°C

\* PW ≤ 10 ms; d ≤ 50%.

#### ■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Testconditons	Min	Typ	Max	Unit
Drain cut-off current	I <sub>DSS</sub>	V <sub>Ds</sub> =-60V, V <sub>Gs</sub> =0			-10	μ A
Gate leakage current	I <sub>GSS</sub>	V <sub>Gs</sub> =±20V, V <sub>Ds</sub> =0			±10	μ A
Gate cut-off voltage	V <sub>Gs(off)</sub>	V <sub>Ds</sub> =-5.0V, I <sub>D</sub> =-1 μ A	-1.4	-1.8	-2.4	V
Forward transfer admittance	Y <sub>fs</sub>	V <sub>Ds</sub> =-5.0V, I <sub>D</sub> =-10mA	20	45		ms
Drain to source on-state resistance	R <sub>Ds(on)</sub>	V <sub>Gs</sub> =-4.0V, I <sub>D</sub> =-10mA		10	15	Ω
		V <sub>Gs</sub> =-10V, I <sub>D</sub> =-10mA		6	10	Ω
Input capacitance	C <sub>iss</sub>	V <sub>Ds</sub> =-5.0V, V <sub>Gs</sub> =0, f=1MHZ		27		pF
Output capacitance	C <sub>oss</sub>			21		pF
Reverse transfer capacitance	C <sub>rss</sub>			3		pF
Turn-on delay time	t <sub>d(on)</sub>	V <sub>Gs(on)</sub> =-4V, R <sub>G</sub> =10 Ω, V <sub>DD</sub> =-5V, I <sub>D</sub> =-10mA R <sub>L</sub> =500 Ω		120		ns
Rise time	t <sub>r</sub>			190		ns
Turn-off delay time	t <sub>d(off)</sub>			150		ns
Fall time	t <sub>f</sub>			180		ns

#### ■ Marking

Marking	H16
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