Single P-channel MOSFET

ELM32407LA-S

■General description

ELM32407LA-S uses advanced trench technology to provide excellent Rds(on), low gate charge and low gate resistance.

■Features

- Vds=-40V
- Id=-18A
- Rds(on) < 25.8mΩ (Vgs=-10V)
- Rds(on) < 40.0mΩ (Vgs=-7V)

■Maximum absolute ratings

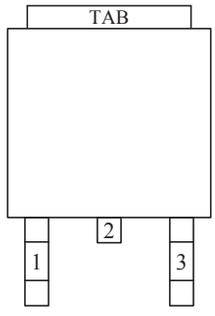
Parameter		Symbol	Limit	Unit	Note
Drain-source voltage		Vds	-40	V	
Gate-source voltage		Vgs	±20	V	
Continuous drain current	Ta=25°C	Id	-18.0	A	
	Ta=70°C		-13.5		
Pulsed drain current		Idm	-40	A	3
Power dissipation	Ta=25°C	Pd	42	W	
	Ta=70°C		27		
Junction and storage temperature range		Tj, Tstg	-55 to 150	°C	

■Thermal characteristics

Parameter		Symbol	Typ.	Max.	Unit	Note
Maximum junction-to-case	Steady-state	Rθjc		3	°C/W	
Maximum junction-to-ambient	Steady-state	Rθja		75	°C/W	

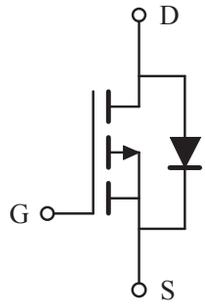
■Pin configuration

TO-252-3(TOP VIEW)



Pin No.	Pin name
1	GATE
2	DRAIN
3	SOURCE

■Circuit



■Electrical characteristics

Ta=25°C

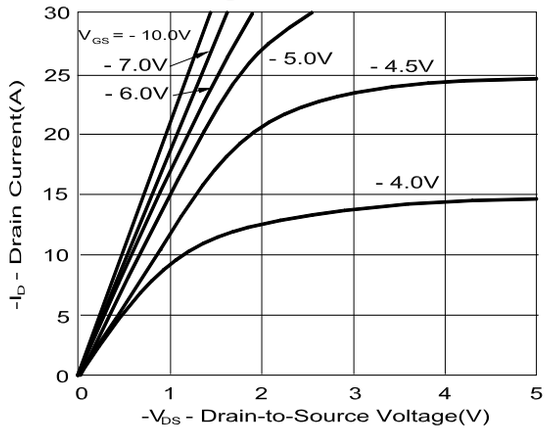
Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit	Note
STATIC PARAMETERS							
Drain-source breakdown voltage	BVdss	Id=-250μA, Vgs=0V	-40			V	
Zero gate voltage drain current	Idss	Vds=-32V, Vgs=0V			-1	μA	
		Vds=-30V, Vgs=0V, Tj=125°C			-10		
Gate-body leakage current	Igss	Vds=0V, Vgs=±20V			±250	nA	
Gate threshold voltage	Vgs(th)	Vds=Vgs, Id=-250μA	-1.2	-2.2	-3.0	V	
On state drain current	Id(on)	Vgs=-10V, Vds=-5V	-40			A	1
Static drain-source on-resistance	Rds(on)	Vgs=-10V, Id=-18A		22.0	25.8	mΩ	1
		Vgs=-7V, Id=-10A		30.0	40.0	mΩ	
Forward transconductance	Gfs	Vds=-5V, Id=-18A		20		S	1
Diode forward voltage	Vsd	Is=If, Vgs=0V			-1.3	V	1
Max. body-diode continuous current	Is				-18	A	
Pulsed body-diode current	Ism				-40	A	3
DYNAMIC PARAMETERS							
Input capacitance	Ciss	Vgs=0V, Vds=-15V, f=1MHz		1570		pF	
Output capacitance	Coss			320		pF	
Reverse transfer capacitance	Crss			210		pF	
SWITCHING PARAMETERS							
Total gate charge	Qg	Vgs=-10V, Vds=-20V Id=-18A		29		nC	2
Gate-source charge	Qgs			6		nC	2
Gate-drain charge	Qgd			7		nC	2
Turn-on delay time	td(on)	Vgs=-10V, Vds=-20V Id≈-1A, Rl=1Ω, Rgen=6Ω		12		ns	2
Turn-on rise time	tr			29		ns	2
Turn-off delay time	td(off)			42		ns	2
Turn-off fall time	tf			33		ns	2
Body diode reverse recovery time	trr	If=-18A, dI/dt=100A/μs		29		ns	
Body diode reverse recovery charge	Qrr			21		nC	

NOTE :

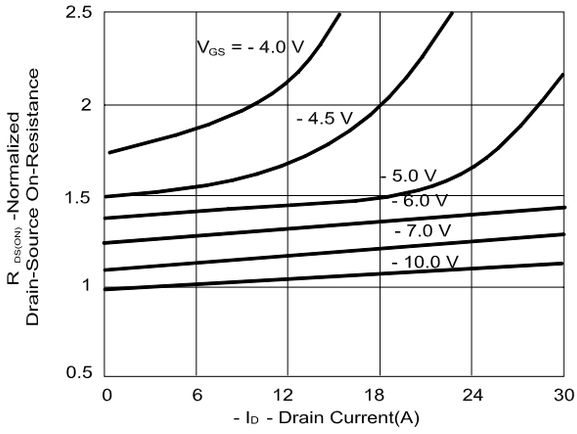
1. Pulse test : Pulsed width ≤ 300μsec and Duty cycle ≤ 2%.
2. Independent of operating temperature.
3. Pulsed width limited by maximum junction temperature.
4. Duty cycle ≤ 1%.

■Typical electrical and thermal characteristics

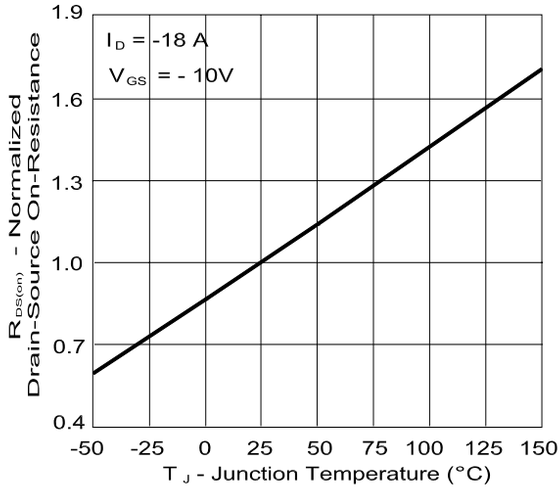
On-Region Characteristics



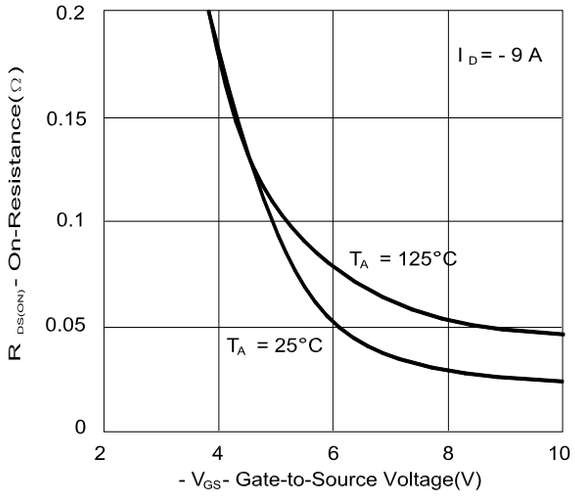
On-Resistance Variation with Drain Current and Gate Voltage



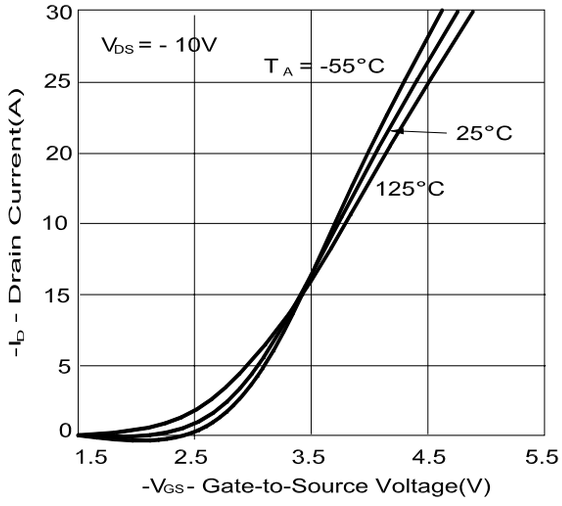
On-Resistance Variation with Temperature



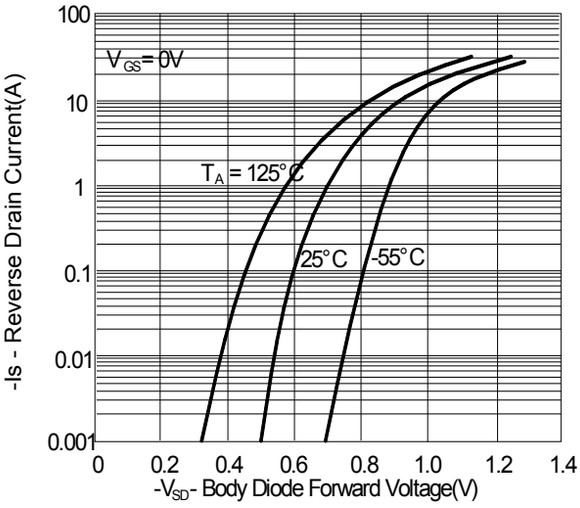
On-Resistance Variation with Gate-to-Source Voltage



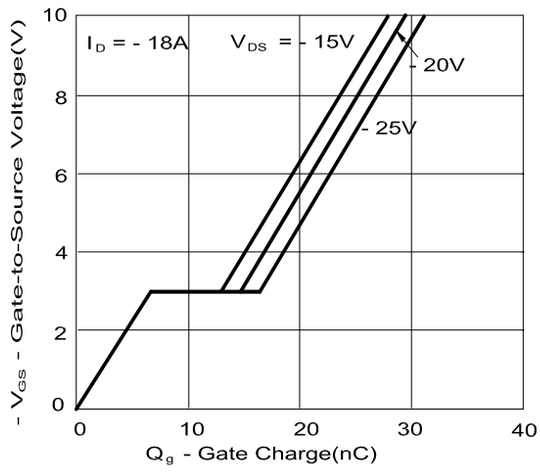
Transfer Characteristics



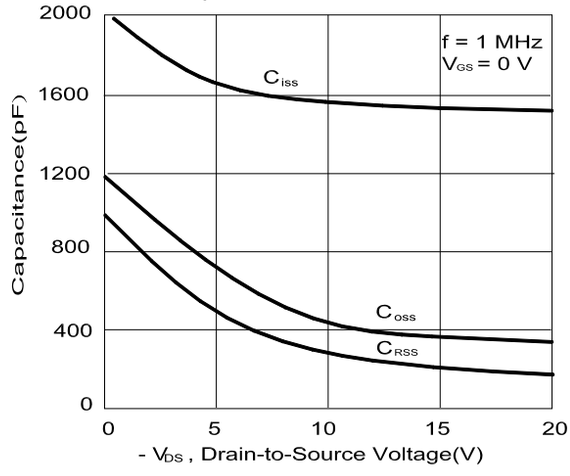
Body Diode Forward Voltage Variation with Source Current and Temperature



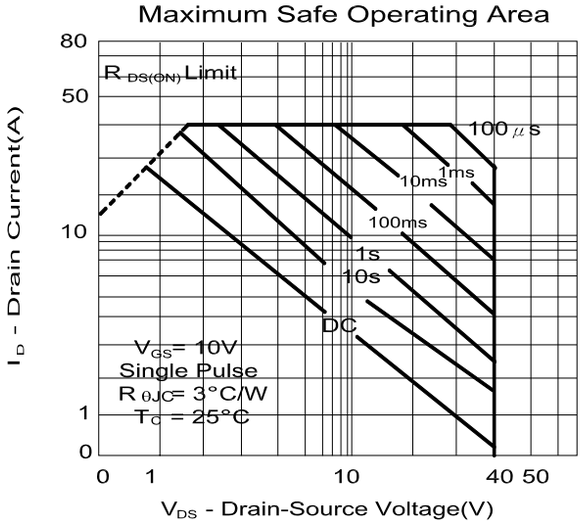
Gate Charge Characteristics



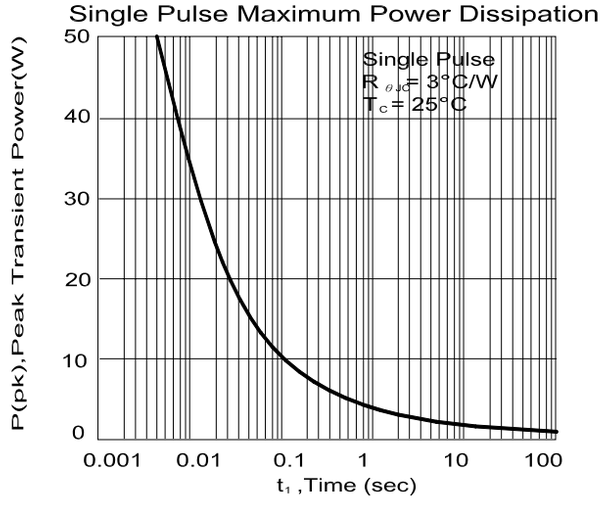
Capacitance Characteristics



Maximum Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve