

Single N-channel MOSFET

ELM544634A-N

■General description

ELM544634A-N uses advanced trench technology to provide excellent $R_{ds(on)}$, low gate charge and low gate resistance.

■Features

- $V_{ds}=30V$
- $I_d=18A$
- $R_{ds(on)} = 5.8m\Omega$ ($V_{gs}=10V$)
- $R_{ds(on)} = 7.2m\Omega$ ($V_{gs}=4.5V$)

■Maximum absolute ratings

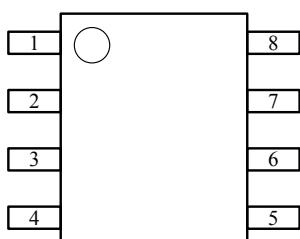
Parameter	Symbol	Limit	Unit
Drain-source voltage	V_{ds}	30	V
Gate-source voltage	V_{gs}	± 20	V
Continuous drain current($T_j=150^{\circ}C$)	$T_a=25^{\circ}C$	18	A
	$T_a=70^{\circ}C$	15	
Pulsed drain current	I_{dm}	50	A
Power dissipation	$T_a=25^{\circ}C$	2.8	W
	$T_a=70^{\circ}C$	1.8	
Operating junction and storage temperature range	T_j, T_{stg}	-55 to 150	°C

■Thermal characteristics

Parameter	Symbol	Typ.	Max.	Unit
Maximum junction-to-ambient	$R_{\theta ja}$		62.5	°C/W

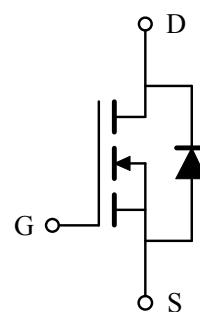
■Pin configuration

SOP-8(TOP VIEW)



Pin No.	Pin name
1	SOURCE
2	SOURCE
3	SOURCE
4	GATE
5	DRAIN
6	DRAIN
7	DRAIN
8	DRAIN

■Circuit



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■ Electrical characteristics

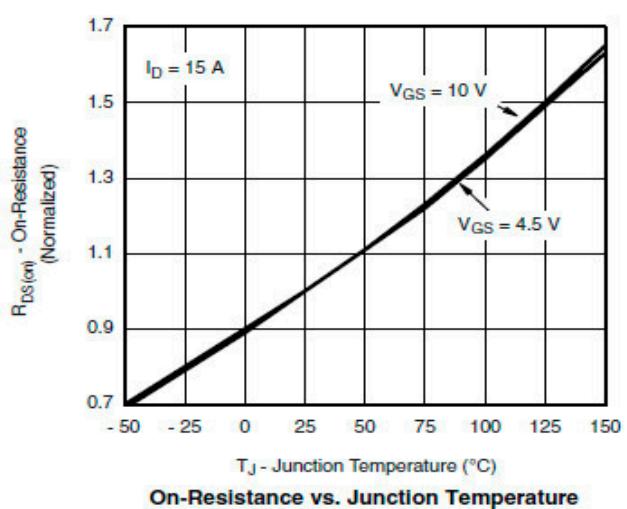
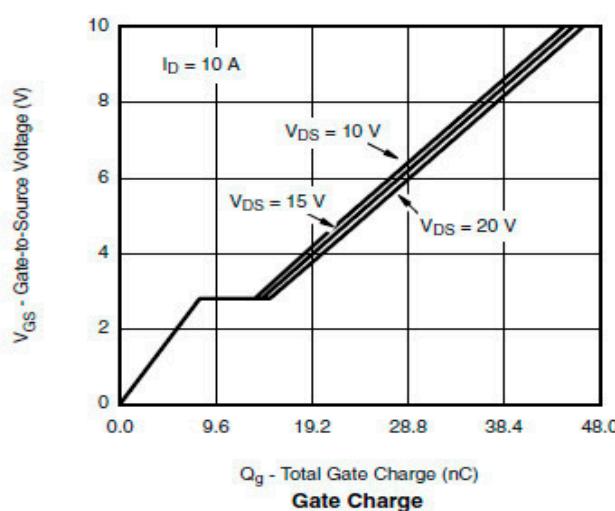
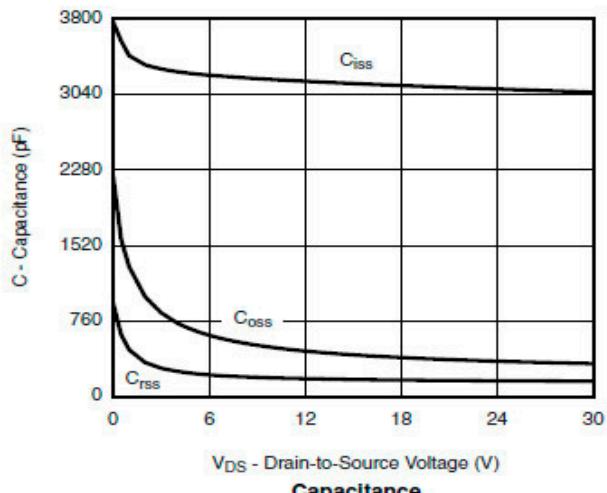
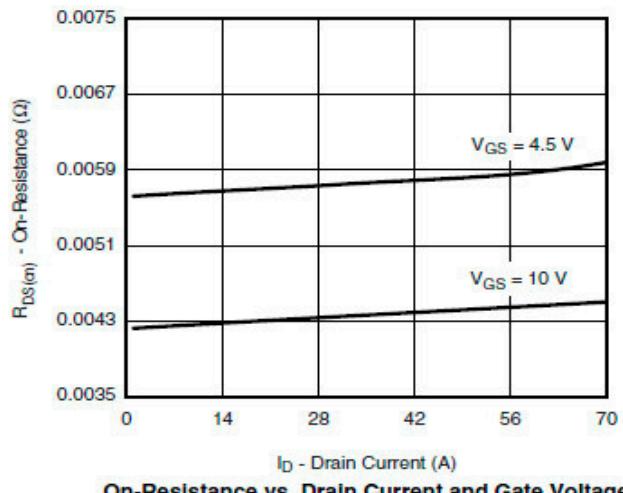
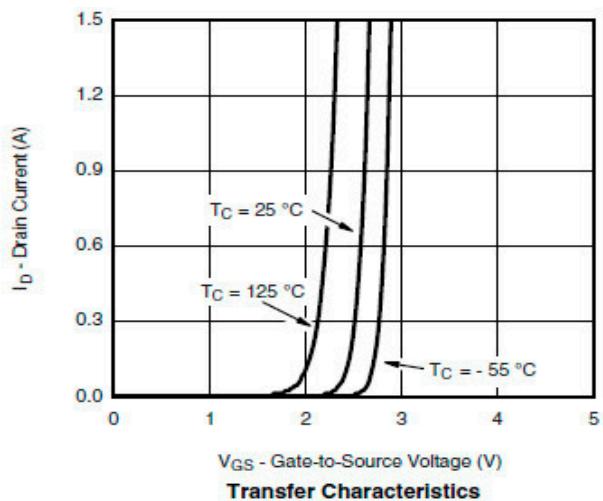
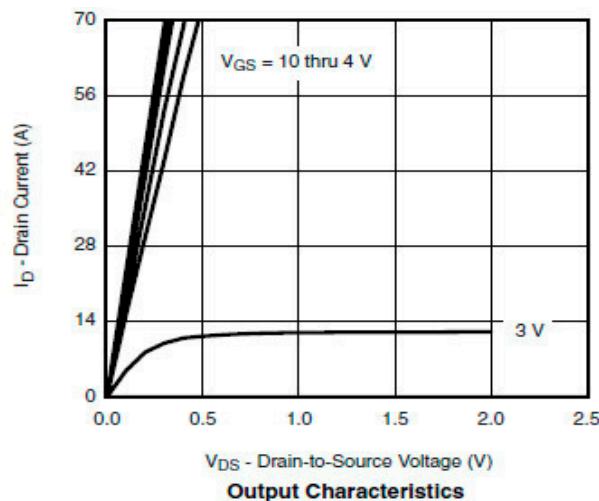
T_a=25°C

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
STATIC PARAMETERS						
Drain-source breakdown voltage	BV _{dss}	I _d =250μA, V _{gs} =0V	30			V
Zero gate voltage drain current	I _{dss}	V _{ds} =24V, V _{gs} =0V			1	μA
		V _{ds} =24V, V _{gs} =0V, T _j =85°C			10	
Gate-body leakage current	I _{gss}	V _{ds} =0V, V _{gs} =±20V			±100	nA
Gate threshold voltage	V _{gs(th)}	V _{ds} =V _{gs} , I _d =250μA	1.0		2.0	V
On state drain current	I _{d(on)}	V _{gs} =10V, V _{ds} =5V	15			A
Static drain-source on-resistance	R _{d(on)}	V _{gs} =10V, I _d =18A		4.8	5.8	mΩ
		V _{gs} =4.5V, I _d =15A		6.0	7.2	
Forward transconductance	G _{fs}	V _{ds} =15V, I _d =10A		24		S
Diode forward voltage	V _{sd}	I _s =10A, V _{gs} =0V		0.8	1.3	V
Max. body-diode continuous current	I _s				3.8	A
DYNAMIC PARAMETERS						
Input capacitance	C _{iss}	V _{gs} =0V, V _{ds} =15V, f=1MHz		2800		pF
Output capacitance	C _{oss}			480		pF
Reverse transfer capacitance	C _{rss}			300		pF
SWITCHING PARAMETERS						
Total gate charge	Q _g	V _{ds} =15V, V _{gs} =4.5V I _d =10A		22	42	nC
Gate-source charge	Q _{gs}			8		nC
Gate-drain charge	Q _{gd}			7		nC
Turn-on delay time	t _{d(on)}	V _{gs} =10V, V _{ds} =15V RL=1.5Ω, I _d =10A R _{gen} =1.0Ω		15	30	ns
Turn-on rise time	t _r			12	20	ns
Turn-off delay time	t _{d(off)}			30	50	ns
Turn-off fall time	t _f			10	20	ns

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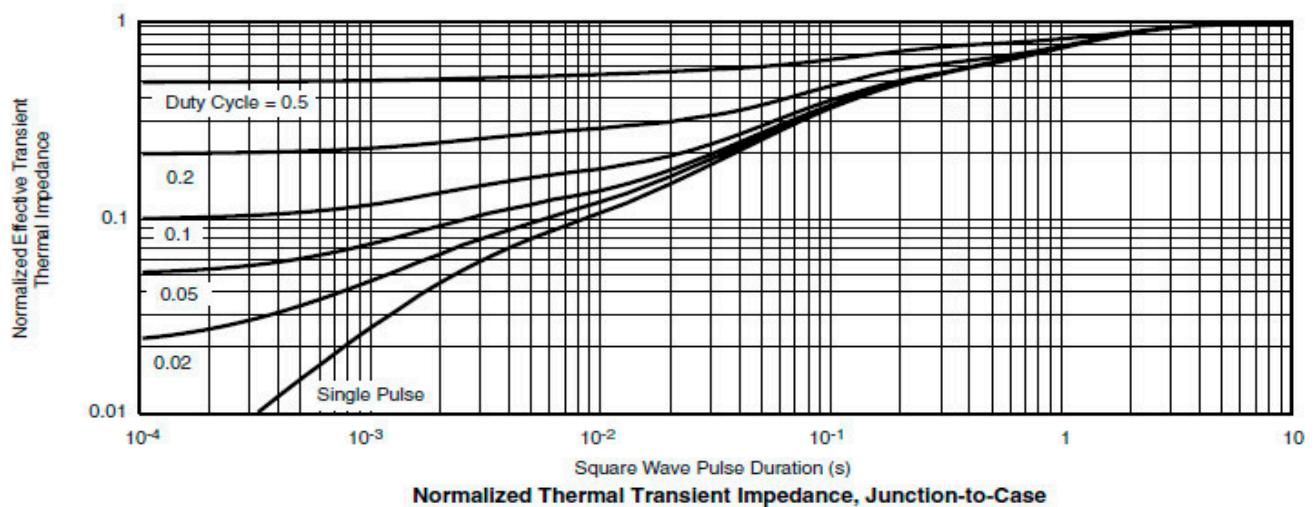
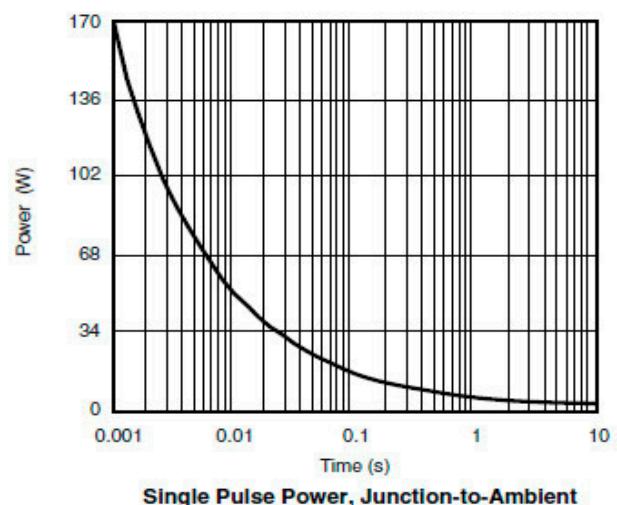
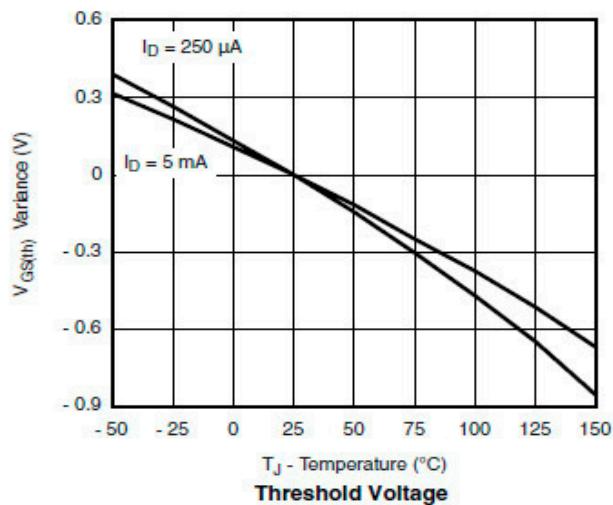
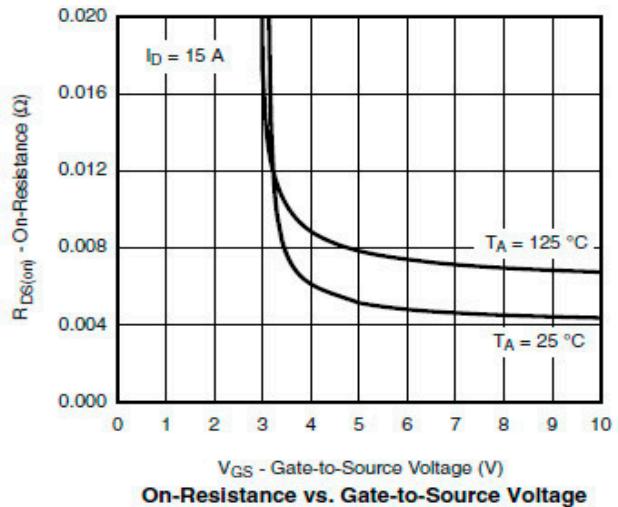
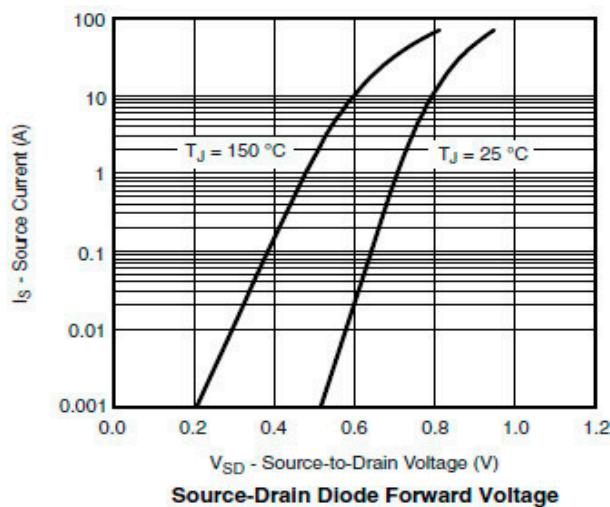
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■ Typical electrical and thermal characteristics



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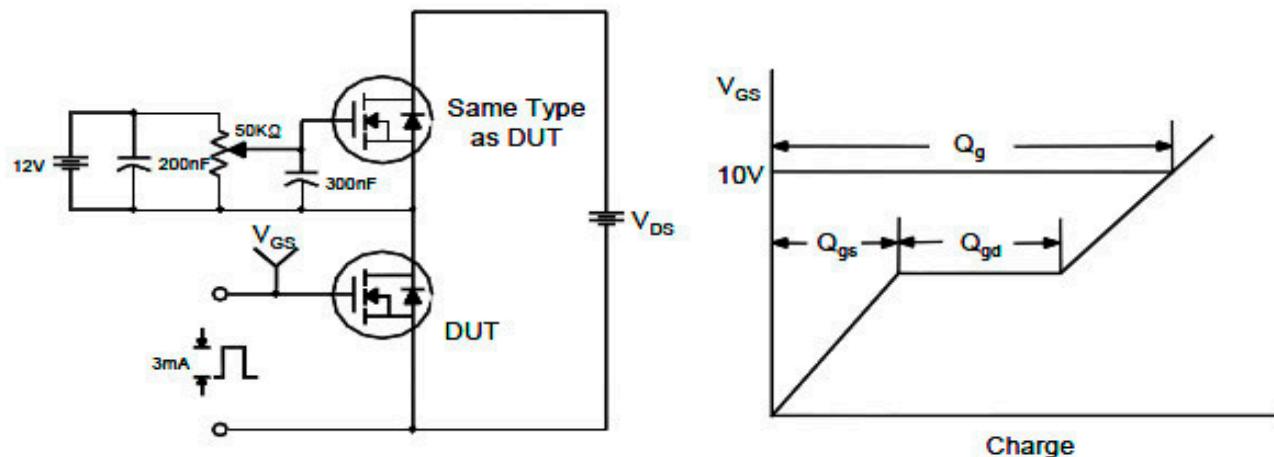


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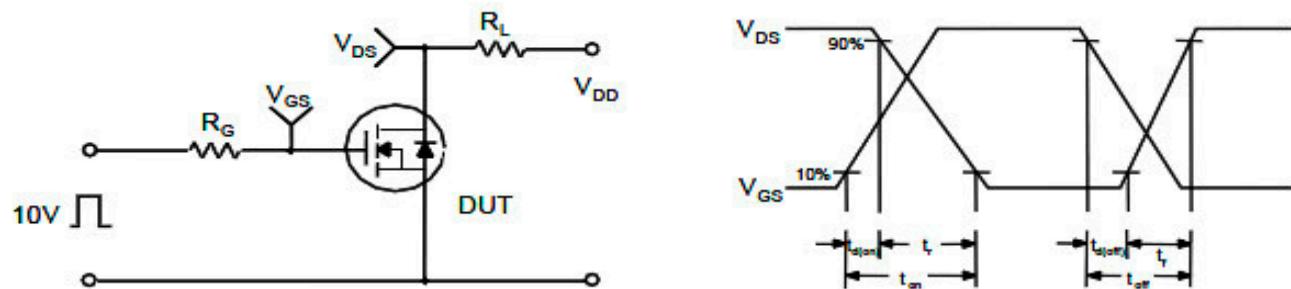
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■ Test circuit and waveform

Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching Test Circuit & Waveforms

