

Single N-channel MOSFET

ELM34418AA-N

■General description

ELM34418AA-N uses advanced trench technology to provide excellent $R_{ds(on)}$, low gate charge and low gate resistance.

■Features

- $V_{ds}=30V$
- $I_d=22A$
- $R_{ds(on)} < 4.0m\Omega$ ($V_{gs}=10V$)
- $R_{ds(on)} < 5.0m\Omega$ ($V_{gs}=4.5V$)

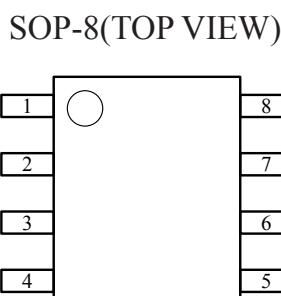
■Maximum absolute ratings

Parameter	Symbol	Limit	Unit	Note
Drain-source voltage	V_{ds}	30	V	
Gate-source voltage	V_{gs}	± 20	V	
Continuous drain current	I_d	22	A	3
Ta=70°C		17		
Pulsed drain current	I_{dm}	100	A	3
Avalanche current	I_{as}	49	A	
Avalanche energy	E_{as}	119	mJ	
Power dissipation	P_d	2.7	W	3
Ta=70°C		1.7		
Operating junction and storage temperature range	T_j, T_{stg}	-55 to 150	°C	

■Thermal characteristics

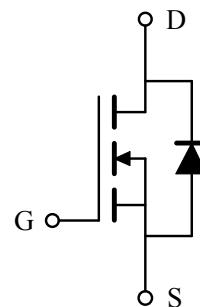
Parameter	Symbol	Typ.	Max.	Unit	Note
Maximum junction-to-case	$R_{\theta jc}$	25	°C/W	°C/W	
Maximum junction-to-ambient	$R_{\theta ja}$				

■Pin configuration



Pin No.	Pin name
1	SOURCE
2	SOURCE
3	SOURCE
4	GATE
5	DRAIN
6	DRAIN
7	DRAIN
8	DRAIN

■Circuit



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■Electrical characteristics

T_a=25°C

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit	Note
STATIC PARAMETERS							
Drain-source breakdown voltage	BV _{dss}	Id=250μA, V _{gs} =0V	30			V	
Zero gate voltage drain current	Id _{ss}	V _{ds} =24V, V _{gs} =0V			1	μA	
		V _{ds} =20V, V _{gs} =0V, T _j =55°C			10		
Gate-body leakage current	I _{gss}	V _{ds} =0V, V _{gs} =±20V			±100	nA	
Gate threshold voltage	V _{gs(th)}	V _{ds} =V _{gs} , Id=250μA	1.0	1.5	3.0	V	
Static drain-source on-resistance	R _{ds(on)}	V _{gs} =10V, Id=20A		3.2	4.0	mΩ	1
		V _{gs} =4.5V, Id=16A		3.7	5.0		
Forward transconductance	G _{fs}	V _{ds} =5V, Id=20A		100		S	1
Diode forward voltage	V _{sd}	I _f =20A, V _{gs} =0V			1	V	1
Max. body-diode continuous current	I _s				22	A	
DYNAMIC PARAMETERS							
Input capacitance	C _{iss}	V _{gs} =0V, V _{ds} =15V, f=1MHz		2700		pF	
Output capacitance	C _{oss}			392		pF	
Reverse transfer capacitance	C _{rss}			302		pF	
Gate resistance	R _g	V _{gs} =0V, V _{ds} =0V, f=1MHz		0.9		Ω	
SWITCHING PARAMETERS							
Total gate charge(V _{gs} =10V)	Q _g	V _{ds} =15V, Id=20A		60		nC	2
Total gate charge(V _{gs} =4.5V)				31		nC	2
Gate-source charge	Q _{gs}			9		nC	2
Gate-drain charge	Q _{gd}			14		nC	2
Turn-on delay time	t _{d(on)}	V _{gs} =20V, V _{ds} =15V, Id≈20A, R _{gen} =6Ω		25		ns	2
Turn-on rise time	t _r			12		ns	2
Turn-off delay time	t _{d(off)}			56		ns	2
Turn-off fall time	t _f			10		ns	2
Body diode reverse recovery time	t _{rr}	I _f =20A, dI/dt=100A/μs		27		ns	
Body diode reverse recovery charge	Q _{rr}			15		nC	

NOTE :

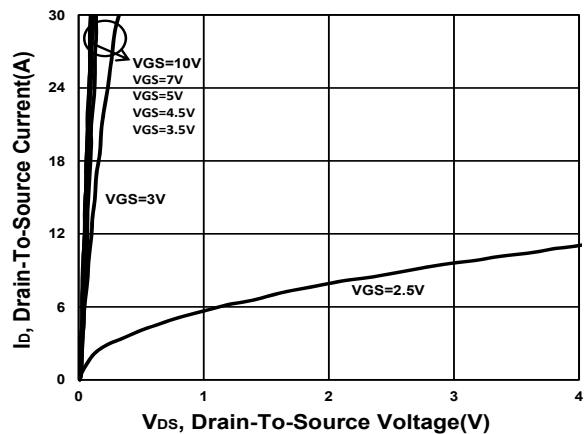
1. Pulsed width≤300μsec and Duty cycle≤2%;
2. Independent of operating temperature;
3. Pulsed width limited by maximum junction temperature.
4. Duty cycle ≤ 1%.

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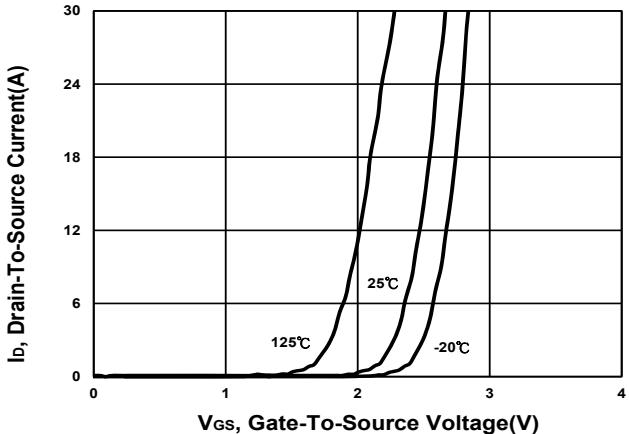
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■ Typical electrical and thermal characteristics

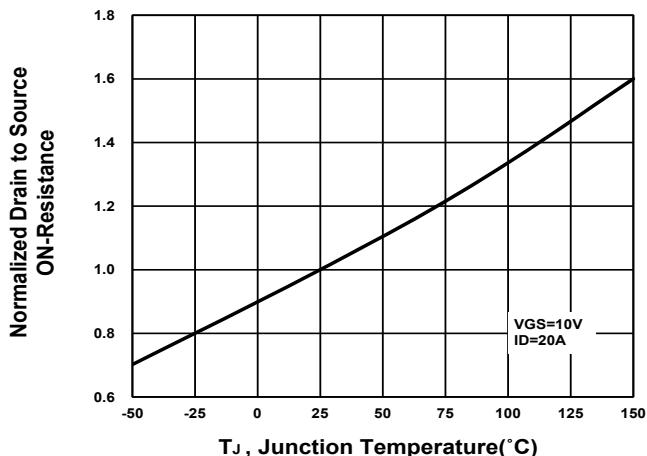
Output Characteristics



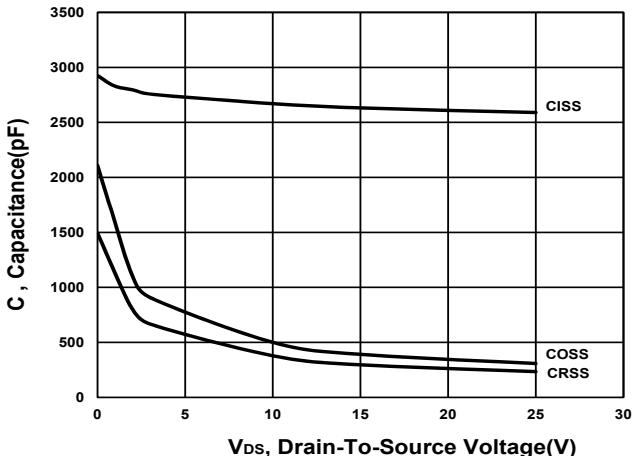
Transfer Characteristics



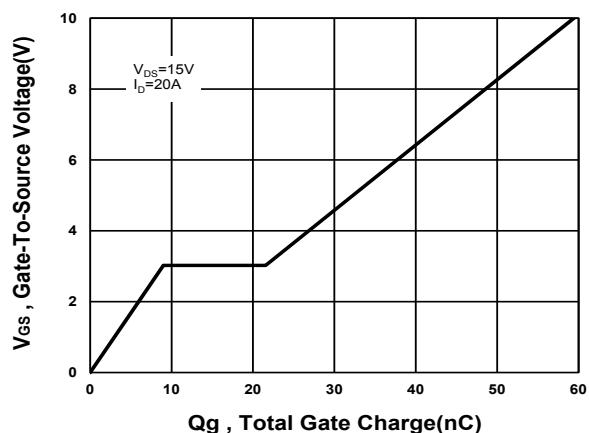
On-Resistance VS Temperature



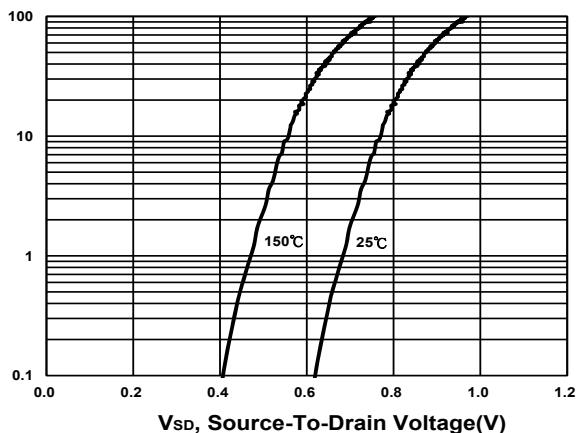
Capacitance Characteristic



Gate charge Characteristics



Source-Drain Diode Forward Voltage



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