



JIANGSU CHANGJIANG ELECTRONICS TECHNOLOGY CO., LTD

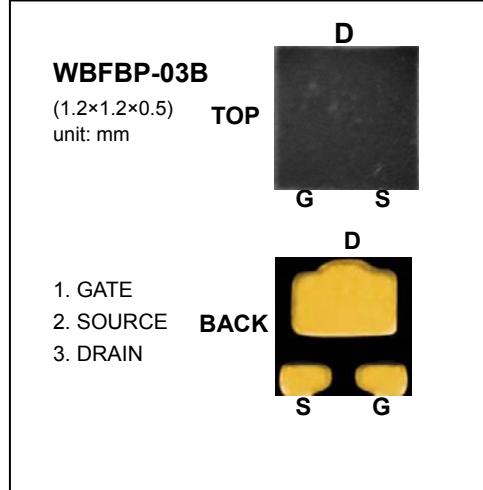
WBFBP-03B Plastic-Encapsulate MOSFETs

M7002NND03

MOSFET(N-Channel)

DESCRIPTION

High cell density, DMOS technology. These products have been designed to minimize on-state resistance while provide rugged, reliable, and fast switching performance. They can be used in most applications requiring up to 400mA DC and can deliver pulsed currents up to 2A. These products are particularly suited for low voltage, low current applications such as small servo motor control, power MOSFET gate drivers, and other switching applications.



FEATURES

High density cell design for low $R_{DS(ON)}$

Voltage controlled small signal switch

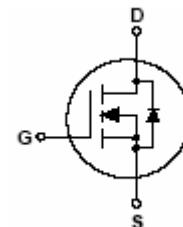
Rugged and reliable

High saturation current capability

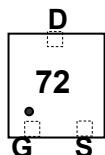
APPLICATION

N-Channel Enhancement Mode Field Effect Transistor

For portable equipment:(i.e. Mobile phone,MP3, MD,CD-ROM, DVD-ROM, Note book PC, etc.)

Chip size: 0.388mm*0.349mm

MARKING: 72



MAXIMUM RATINGS ($T_a=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Value	Units
V_{DS}	Drain-Source Voltage	60	V
V_{GSS}	Gate-Source Voltage - Continuous	± 20	V
I_D	Maximum Drain Current - Pulsed	115	mA
P_D	Power Dissipation	150	mW
$R_{\theta JA}$	Thermal Resistance from Junction to Ambient	833	$^\circ\text{C}/\text{W}$
T_J	Junction Temperature	150	$^\circ\text{C}$
T_{stg}	Storage Temperature	-55~+150	$^\circ\text{C}$

ELECTRICAL CHARACTERISTICS (Ta=25°C unless otherwise specified)

Parameter	Symbol	Test conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =10μA	60			V
Gate-Threshold Voltage*	V _{th(GS)}	V _{DS} =V _{GS} , I _D =250μA	1			
Gate-body Leakage	I _{GSS}	V _{DS} =0V, V _{GS} =±25V			±80	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =60V, V _{GS} =0V			0.08	μA
		V _{DS} =60V, V _{GS} =0V, T _j =125°C			500	
On-state Drain Current*	I _{D(ON)}	V _{GS} =10V, V _{DS} =7V	500			mA
Drain-Source On-Resistance*	R _{DS(on)}	V _{GS} =10V, I _D =500mA	1		7.2	Ω
		V _{GS} =5V, I _D =50mA	1		7.2	
Drain-Source On- Voltage *	V _{DS(on)}	V _{GS} =10V, I _D =500mA	0.5		3.75	V
		V _{GS} =5V, I _D =50mA	0.05		0.375	
Forward Tran conductance*	g _{fs}	V _{DS} =10V, I _D =200mA	80		500	ms
Diode Forward Voltage	V _{SD}	I _S =115mA, V _{GS} =0V	0.55		1.2	V
Input Capacitance	C _{iss}	V _{DS} =25V, V _{GS} =0V,f=1MHz			50	pF
Output Capacitance	C _{oss}				25	
Reverse Transfer Capacitance	C _{rss}				5	

*Pulse test : pulse width≤300μs, duty cycle≤2%.

SWITCHING TIME

Turn-on Time	t _{d(on)}	V _{DD} =25V, R _G =25Ω I _D =500mA, V _{GEN} =10V R _L =50Ω			20	ns
Turn-off Time	t _{d(off)}				40	

Typical Characteristics

M7002NND03

