

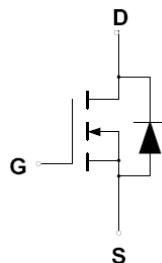


TO-252-2L Plastic-Encapsulate MOSFETs

CJU4828 N-Channel MOSFET

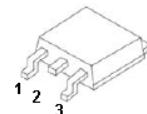
DESCRIPTION

The CJU4828 uses advanced trench technology to provide excellent $R_{DS(ON)}$ and low gate charge. This device is suitable for use as a load switch or in PWM applications.



TO-252-2L

1. GATE
2. DRAIN
3. SOURCE



Maximum ratings ($T_a=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ($t \leq 10\text{s}$) (note 1)	I_D	4.5	A
Pulsed Drain Current (note 2)	I_{DM}	20	A
Power Dissipation	P_D	1.25	W
Thermal Resistance from Junction to Ambient ($t \leq 10\text{s}$) (note 1)	$R_{\theta JA}$	100	$^\circ\text{C/W}$
Avalanche Current (note 2)	I_{AR}, I_{AS}	19	A
Repetitive Avalanche Energy 0.1mH (note 2)	E_{AR}, E_{AS}	18	mJ
Junction Temperature	T_J	150	$^\circ\text{C}$
Storage Temperature	T_{STG}	-55~+150	$^\circ\text{C}$

Electrical characteristics ($T_a=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
STATIC PARAMETERS						
Drain-source breakdown voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_{\text{D}} = 250\mu\text{A}$	60			V
Zero gate voltage drain current	I_{DSS}	$V_{\text{DS}} = 60\text{V}, V_{\text{GS}} = 0\text{V}$			1	μA
Gate-body leakage current	I_{GSS}	$V_{\text{GS}} = \pm 20\text{V}, V_{\text{DS}} = 0\text{V}$			± 100	nA
Gate threshold voltage (note 3)	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_{\text{D}} = 250\mu\text{A}$	1		3	V
Drain-source on-resistance (note 3)	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = 10\text{V}, I_{\text{D}} = 4.5\text{A}$			56	$\text{m}\Omega$
		$V_{\text{GS}} = 4.5\text{V}, I_{\text{D}} = 3\text{A}$			77	$\text{m}\Omega$
Forward transconductance (note 3)	g_{FS}	$V_{\text{DS}} = 5\text{V}, I_{\text{D}} = 4.5\text{A}$	4			S
Diode forward voltage (note 3)	V_{SD}	$I_{\text{S}} = 1\text{A}, V_{\text{GS}} = 0\text{V}$			1	V
DYNAMIC PARAMETERS (note 4)						
Input Capacitance	C_{iss}	$V_{\text{DS}} = 30\text{V}, V_{\text{GS}} = 0\text{V}, f = 1\text{MHz}$			540	pF
Output Capacitance	C_{oss}			60		pF
Reverse Transfer Capacitance	C_{rss}			25		pF
SWITCHING PARAMETERS (note 4)						
Turn-on delay time	$t_{\text{d}(\text{on})}$	$V_{\text{GS}} = 10\text{V}, V_{\text{DS}} = 30\text{V}, R_{\text{L}} = 6.7\Omega, R_{\text{GEN}} = 3\Omega$		4.7		ns
Turn-on rise time	t_{r}			2.3		ns
Turn-off delay time	$t_{\text{d}(\text{off})}$			15.7		ns
Turn-off fall time	t_{f}			1.9		ns
Total Gate Charge (10V)	Q_{g}	$V_{\text{DS}} = 30\text{V}, V_{\text{GS}} = 10\text{V}, I_{\text{D}} = 4.5\text{A}$			10.5	nC
Total Gate Charge (4.5V)					5.5	nC
Gate-Source Charge	Q_{gs}			1.6		nC
Gate-Drain Charge	Q_{gd}			2.2		nC

Notes :

1. The value in any given application depends on the user's specific board design.
2. Repetitive rating : Pulse width limited by junction temperature.
3. Pulse Test : Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 0.5\%$.
4. These parameters have no way to verify.