

JIANGSU CHANGJIANG ELECTRONICS TECHNOLOGY CO., LTD

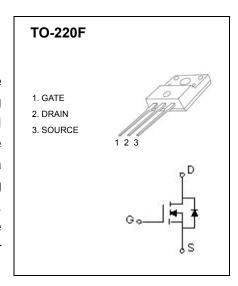
TO-220F Plastic-Encapsulate MOSFETS

CJPF02N60

N-Channel Power MOSFET

General Description

The high voltage MOSFET uses an advanced termination scheme to provide enhanced voltage-blocking capability without degrading performance over time. In addition, this advanced MOSFET is designed to withstand high energy in avalanche and commutation modes. The new energy efficient design also offers a drain-to-source diode with a fast recovery time. Designed for high voltage, high speed switching applications in power suppliers, converters and PWM motor controls, these devices are particularly well suited for bridge circuits where diode speed and commutating safe operating areas are critical and offer additional and safety margin against unexpected voltage transients.



FEATURES

- Robust High Voltage Termination
- Avalanche Energy Specified
- Source-to-Drain Diode Recovery Time Comparable to a Discrete Fast Recovery Diode
- Diode is Characterized for Use in Bridge Circuits
- I_{DSS} and V_{DS(on)} Specified at Elevated Temperature

Maximum ratings (T_a=25℃ unless otherwise noted)

Parameter	Symbol	Value	Unit	
Drain-Source Voltage	V _{DS}	600	V	
Gate-Source Voltage	V _{GS}	±20		
Continuous Drain Current	I _D	2	A	
Pulsed Drain Current	I _{DM}	9		
Power Dissipation	P _D	2	W	
Single Pulsed Avalanche Energy	E _{AS} *	128	mJ	
Thermal Resistance from Junction to Ambient	Reja	62.5	°C/W	
Junction Temperature	TJ	150	°C	
Storage Temperature	Тѕтс	-55 ~+150		

^{*} E_{AS} condition: T_j =25°C, V_{DD} =50V,L=64mH, I_L =2A, R_G =25 Ω

Electrical characteristics (T_a =25°C unless otherwise noted)

Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
Drain-Source Breakdown Voltage	V(BR) DSS	$V_{GS} = 0V$, $I_{D} = 250\mu A$ $V_{DS} = V_{GS}$, $I_{D} = 250\mu A$	600			٧
Gate-Threshold Voltage (note1)	V _{GS(th)}		2.0		4.0	
Gate-Body Leakage Current (note1)	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =600V, V _{GS} =0V			25	μΑ
Drain-Source On-State Resistance (note1)	RDS(on)	V _G S =10V, I _D =1A			4.4	Ω
Forward Transconductance (note1)	g fs	V _{DS} =50V, I _D =1A	1			S
Input Capacitance (note 2)	C _{iss}	V _{DS} =25V,V _{GS} =0V, f =1MHz		435		pF
Output Capacitance (note 2)	Coss			56		
Reverse Transfer Capacitance (note 2)	C _{rss}			9.2		
Turn-On Delay Time (note 2)	t _{d(on)}	V_{DD} =300V,I _D =2A, V_{GS} =10V,R _G =18 Ω		12		ns ns
Rise Time (note 2)	tr			21		
Turn-Off Delay Time (note 2)	td(off)			30		
Fall Time (note 2)	t f			24		
Forward on Voltage(note1)	V _{SD}	V _{GS} =0V, I _S =2A			1.6	V

Notes:

- 1. Pulse Test : Pulse width≤300µs, duty cycle ≤2%.
- 2. These parameters have no way to verify.

Typical Characteristics

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