

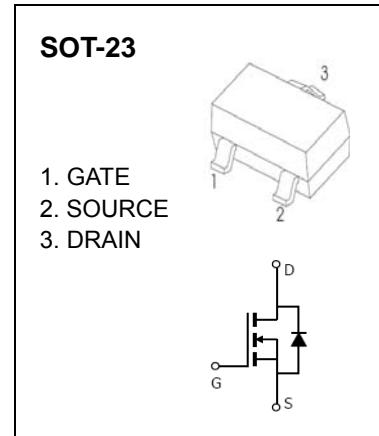


SOT-23 Plastic-Encapsulate MOSFETs

CJ3404 N-Channel Enhancement Mode Field Effect Transistor

DESCRIPTION

The CJ3404 use advanced trench technology to provide excellent $R_{DS(ON)}$ and low gate charge. This device is suitable for use as a load switch or in PWM applications. The source leads are separated to allow a Kelvin connection to the source, which may be used to bypass the source inductance.



MARKING: R4

Maximum ratings ($T_a=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-source voltage	V_{DS}	30	V
Gate-source voltage	V_{GS}	± 20	V
Continuous drain current ($t \leq 10\text{s}$)	I_D	5.8	A
Pulsed drain current *	I_{DM}	30	A
Thermal resistance from junction to ambient	$R_{\theta,JA}$	357	$^\circ\text{C/W}$
Junction temperature	T_J	150	$^\circ\text{C}$
Storage temperature	T_{stg}	-55~150	$^\circ\text{C}$

* Repetitive rating : Pulse width limited by maximum junction temperature.

Electrical characteristics ($T_a=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Test Condition	Min	Typ	Max	Units
STATIC PARAMETERS						
Drain-source breakdown voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	30			V
Zero gate voltage drain current	I_{DSS}	$V_{\text{DS}} = 30\text{V}, V_{\text{GS}} = 0\text{V}$			1	μA
Gate-body leakage current	I_{GSS}	$V_{\text{GS}} = \pm 20\text{V}, V_{\text{DS}} = 0\text{V}$			± 100	nA
Gate threshold voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	1		3	V
Drain-source on-resistance (note 1)	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = 10\text{V}, I_D = 5.8\text{A}$			30	$\text{m}\Omega$
		$V_{\text{GS}} = 4.5\text{V}, I_D = 4.8\text{A}$			42	$\text{m}\Omega$
Forward transconductance (note 1)	g_{FS}	$V_{\text{DS}} = 5\text{V}, I_D = 5.8\text{A}$	5			S
Diode forward voltage	V_{SD}	$I_S = 1\text{A}$			1	V
DYNAMIC PARAMETERS (note 2)						
Input capacitance	C_{iss}	$V_{\text{DS}} = 15\text{V}, V_{\text{GS}} = 0\text{V}, f = 1\text{MHz}$			820	pF
Output capacitance	C_{oss}			118		pF
Reverse transfer capacitance	C_{rss}			85		pF
Gate resistance	R_g	$V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = 0\text{V}, f = 1\text{MHz}$			1.5	Ω
SWITCHING PARAMETERS (note 2)						
Turn-on delay time	$t_{\text{d}(\text{on})}$	$V_{\text{GS}} = 10\text{V}, V_{\text{DS}} = 15\text{V}, R_L = 2.6\Omega, R_{\text{GEN}} = 3\Omega$			6.5	ns
Turn-on rise time	t_r			3.1		ns
Turn-off delay time	$t_{\text{d}(\text{off})}$			15.1		ns
Turn-off fall time	t_f			2.7		ns

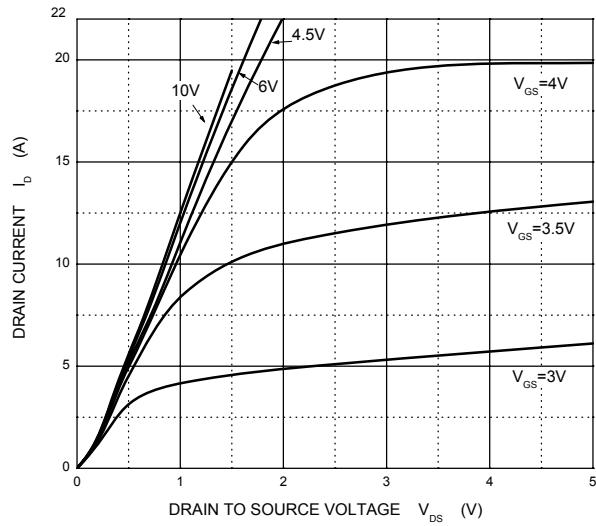
Note :

1. Pulse Test : Pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 0.5\%$.
2. These parameters have no way to verify.

Typical Characteristics

CJ3404

Output Characteristics



Transfer Characteristics

