

STA3350F

PNP Silicon Transistor

Applications

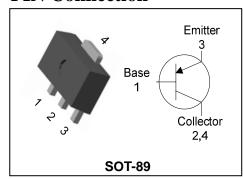
- Power amplifier application
- High current switching application

Features

- Low saturation voltage: $V_{CE(sat)}$ =-0.15V Typ. @ I_C =-1A, I_B =-50mA
- Large collector current capacity: I_C=-3A
- Small and compact SMD type package
- Complementary pair with STC4350F
- "Green" device and RoHS compliant device
- Available in full lead (Pb)-free device



PIN Connection



Ordering Information

Type NO.	Marking	Package Code
STA3350F	HW7 YWW	SOT-89

HW7: DEVICE CODE, YWW(Y: Year code, WW: Weekly code)

Absolute Maximum Ratings

[Ta=25 $^{\circ}$ C]

Characteristic	Symbol	Rating	Unit	
Collector-base voltage	V_{CBO}	-50	V	
Collector-emitter voltage	V_{CEO}	-50	V	
Emitter-base voltage	V_{EBO}	-6	V	
Collector current	I_{C}	-3	A(DC)	
Collector current	I _{CP} *	-6	A(Pulse)	
Collector Power dissipation	P _C	0.5	W	
Collector Power dissipation	P _C ** 1		W	
Junction temperature	T _J	150	°C	
Storage temperature range	T_{stg}	-55~150	°C	

^{* :} Single pulse, tp= 300 μ s

^{**:} Device mounted on ceramic substrate (250mm² x 0.8t)

Characteristic		Symbol	Тур.	Max	Unit
Thermal resistance	Junction-ambient	$R_{th(J-a)}$	1	250	°C/W
			1	125**	°C/W

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Electrical Characteristics

[Ta=25℃]

Charac	Characteristic Symbol Test Condition		Min.	Тур.	Max.	Unit	
Collector-emitter breakdown voltage		BV _{CEO}	I _C =-1mA, I _B =0	-50	-	ı	٧
Collector cut-off cu	Collector cut-off current		V _{CB} =-50V, I _E =0	-	-	-1	μΑ
Emitter cut-off cur	rent	I_{EBO}	V _{EB} =-6V, I _C =0	-	-	-1	μΑ
DC current gain		h _{FE}	V _{CE} =-2V, I _C =-0.5A*	120	-	240	
		h _{FE}	V _{CE} =-2V, I _C =-2A*	40	-	1	
Collector-emitter saturation voltage		$V_{\text{CE(sat)}}$	I _C =-1A, I _B =-0.05A*	-	-	-0.35	V
Base-emitter saturation voltage		$V_{BE(sat)}$	I _C =-2A, I _B =-0.1A*	-	-0.97	-1.2	V
Transition frequen	Transition frequency		V _{CE} =-10V, I _C =-0.05A	-	250	-	MHz
Collector output capacitance		C _{ob}	V _{CB} =-10V, I _E =0, f=1MHz	-	28	-	pF
Switching Time	Turn-on Time	t _{on}	Ise indict is OUTPUT	-	100	-	
	Storage Time	t _{stg}	Ise INPUT Ise OUTPUT Source Source	-	300	-	ns
	Fall Time	t _f	-IB=IB≃IUUMA -30V DUTY CYCLE ≤1%	-	50	-	

^{*:} Pulse test : $t_P \le 300 \mu s$, Duty cycle $\le 2\%$

Electrical Characteristic Curves

Fig. 1 $P_{\rm C}\,$ - T_a

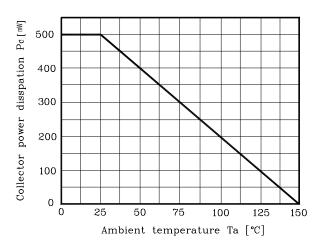


Fig. 2 $I_{C}\;$ - V_{BE}

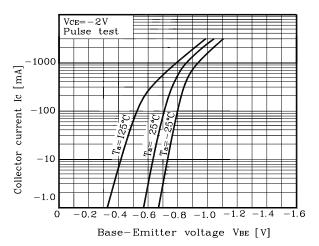


Fig. 3 $I_{\rm C}~$ - $V_{\rm CE}$

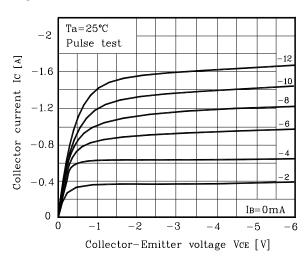


Fig. 4 h_{FE} - $I_{C}\,$

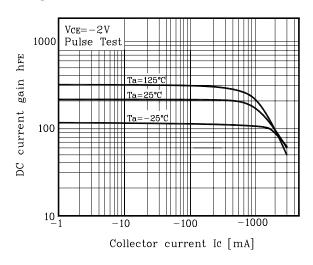


Fig. 5 $V_{\text{CE}(\text{sat})}$ - I_{C}

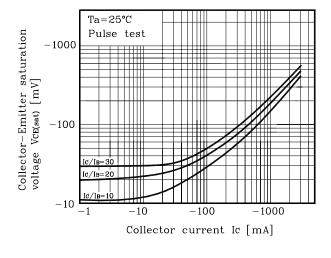
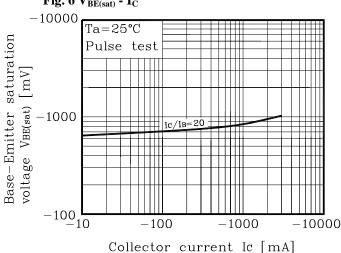


Fig. 6 $V_{BE(sat)}$ - I_{C}



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Electrical Characteristic Curves

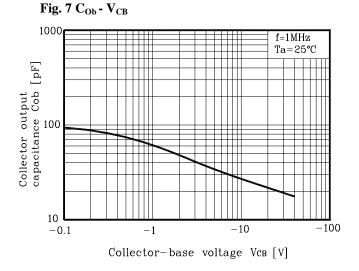
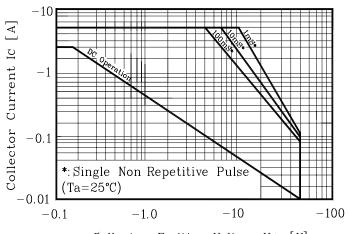
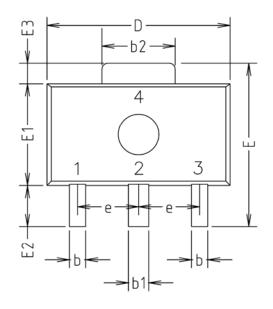


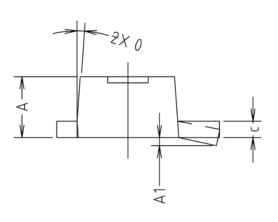
Fig. 8 Safe Operating Area



Collector-Emitter Voltage VCE [V]

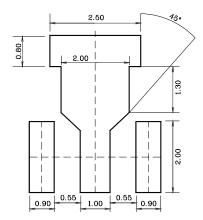
Outline Dimension(mm)





	MILLIMETERS			
SYMBOL	MINIMUM	NOMINAL	MAXIMUM	NOTE
Α	1.40	1.50	1.60	
A1	0.00	_	0.10	
b	0.38	0.42	0.48	
b1	0.48	0.52	0.58	
b2	1.79	1.82	1.87	
С	0.40	0.42	0.46	
D	4.40	4.50	4.70	
Ε	3.70	4.00	4.30	
E1	2.40	2.50	2.70	
E2	0.80	1.00	1.20	
E3	0.40	0.50	0.60	
е		1.50 TYP.		
0		4° TYP.		

*Recommend PCB solder land [Unit: mm]



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