

NPN Silicon Transistor

(Ta=25°C)

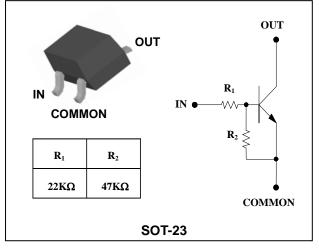
Descriptions

- Switching application
- Interface circuit and driver circuit application

Features

- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process
- High packing density

PIN Connection



Ordering Information

Type NO.	Marking	Package Code
SRC1208S	<u>RC8</u> □ ① ②	SOT-23

1 Device Code 2 Year Week Code

Absolute Maximum Ratings

Characteristic	Symbol	Rating	Unit		
Output voltage	Vo	50	V		
Input voltage	Vı	40,-7	V		
Output current	Ι _ο	100	mA		
Power dissipation	P _D	200	mW		
Junction temperature	TJ	150	°C		
Storage temperature range	T _{stg}	-55 ~ 150	°C		

Electrical Characteristics

(Ta=25°C)

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Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Output cut-off current	I _{O(OFF)}	V ₀ =50V, V ₁ =0	-	-	500	nA
DC current gain	Gı	V ₀ =5V, I ₀ =10mA	80	150	-	-
Output voltage	V _{O(ON)}	I _o =10mA, I _I =0.5mA	-	0.1	0.3	V
Input voltage (ON)	V _{I(ON)}	V ₀ =0.2V, I ₀ =5mA	-	1.8	2.6	V
Input voltage (OFF)	V _{I (OFF)}	V ₀ =5V, I ₀ =0.1mA	0.6	0.88	-	V
Transition frequency	f_{T}^{*}	$V_0=10V$, $I_0=5mA$, f=1MHz	-	200	-	MHz
Input current	I ₁	V ₁ =5V, I ₀ =0	-	-	0.36	mA
Input resistor (Input to base)	R ₁	-	15.4	22	28.6	KΩ
Input resistor (Base to common)	R_2	-	33	47	61	KΩ

* : Characteristic of transistor only

Electrical Characteristic Curves

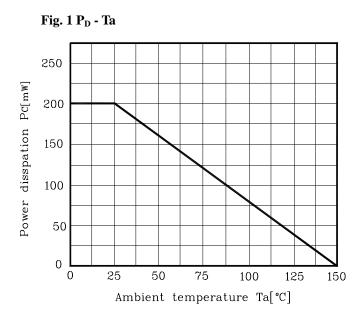


Fig. 3 I_O - V_{I(OFF)}

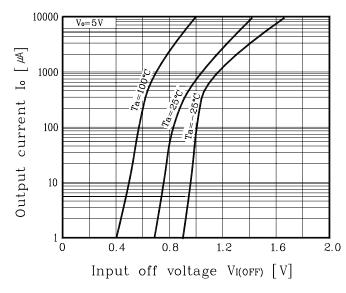


Fig. 2 I_O - V_{I(ON)}

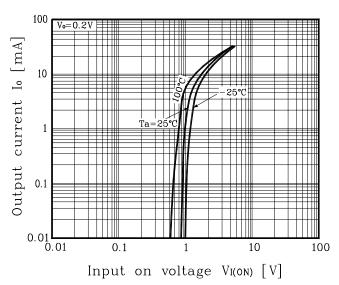
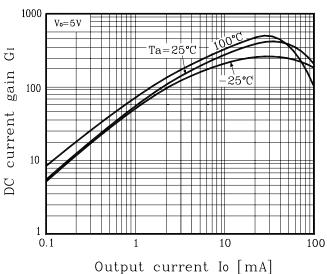
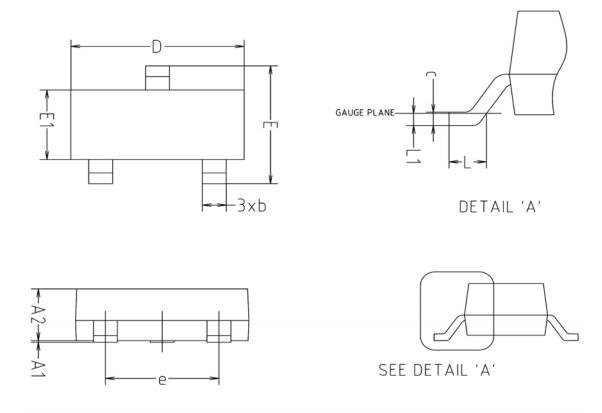


Fig. 4 G_I - I_O

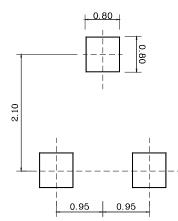


Outline Dimension



SYMBOL	MILLIMETERS			NOTE
	MINIMUM	NOMINAL	MAXIMUM	NOTE
A1	0.00	-	0.10	
A2	0.82	-	1.02	
b	0.39	0.42	0.45	
С	0.09	0.12	0.15	
D	2.80	2.90	3.00	
E	2.20	2.40	2.60	
E1	1.20	1.30	1.40	
е	1.90BSC			
L	0.20	-	-	
L1	0.12BSC			

*Recommend PCB solder land [Unit: mm]



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