

SRC1206SF

NPN Silicon Transistor

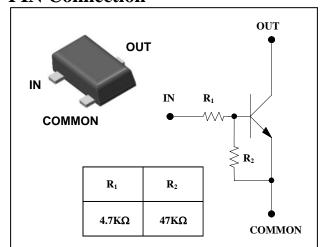
Descriptions

- Switching application
- Interface circuit and driver circuit application

Features

- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process
- High packing density

PIN Connection



Ordering Information

Type NO.	Marking	Package Code	
SRC1206SF	<u>RC6</u> □ ②	SOT-23F	

1 Device Code 2 Year & Week Code

Absolute Maximum Ratings

(Ta=25°C)

Characteristic	Symbol	Rating	Unit
Output voltage	Vo	50	V
Input voltage	V _I	20,-5	V
Output current	Io	100	mA
Power dissipation	P_{D}	200	mW
Junction temperature	T_J	150	°C
Storage temperature range	T_{stg}	-55 ~ 150	°C

Electrical Characteristics

(Ta=25°C)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Output cut-off current	I _{O(OFF)}	$V_0 = 50V, V_1 = 0$	-	-	500	nA
DC current gain	G _I	$V_0 = 5V$, $I_0 = 10mA$	80	200	-	-
Output voltage	$V_{O(ON)}$	$I_0 = 10 \text{mA}, I_1 = 0.5 \text{mA}$	-	0.1	0.3	V
Input voltage (ON)	V _{I(ON)}	$V_0 = 0.2V$, $I_0 = 5mA$	-	0.9	1.3	V
Input voltage (OFF)	$V_{I(OFF)}$	$V_0 = 5V$, $I_0 = 0.1 \text{mA}$	0.5	0.65	-	V
Transition frequency	f_T^*	$V_0=10V$, $I_0=5mA$, $f=1MHz$	-	200	-	MHz
Input current	I ₁	$V_1 = 5V, I_0 = 0$	-	-	1.8	mA
Input resistor (Input to base)	R ₁	-	3.3	4.7	6.1	ΚΩ
Input resistor (Base to common)	R_2	-	33	47	61	ΚΩ

^{* :} Characteristic of transistor only

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Electrical Characteristic Curves

Fig. 1 P_D - Ta

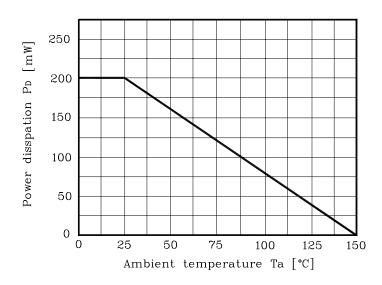


Fig. 2 $I_{\rm O}$ - $V_{\rm I(ON)}$

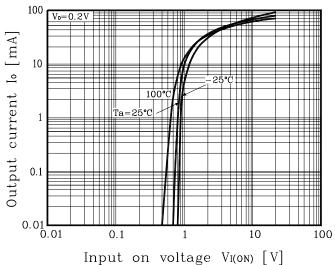


Fig. 3 $I_{\rm O}$ - $V_{I(OFF)}$

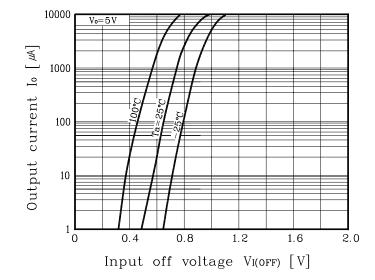
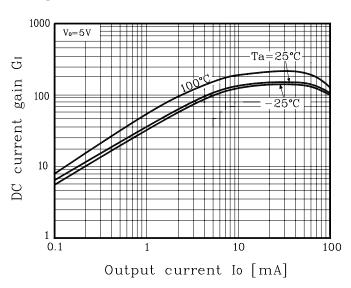


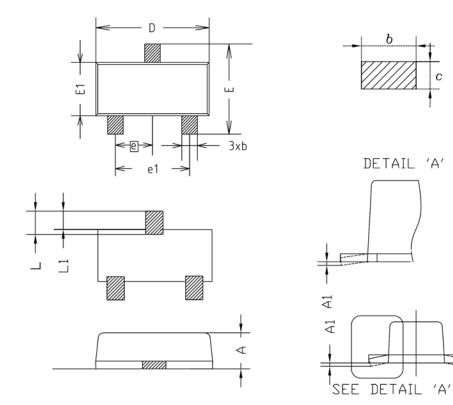
Fig. 4 G_I - I_O



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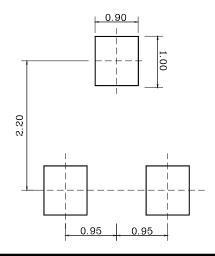
SECTION

Outline Dimension



SYMBOL	MILLIMETER(mm)			NOTE	
STINBUL	MINIMUM	NDMINAL	MAXIMUM	NUIE	
Α	0.80	0.90	1.00		
A1	0.00	-	0.10		
b	0.35	0.40	0.45		
C	0.10	0.15	0.20		
D	2.80	2.90	3.00		
Ε	2.30	2.40	2.50		
E1	1.50	1.60	1.70		
е	0.95BSC				
e1	1.80	1.90	2.00		
L	0.48	0.58	0.68		
L1	0.30	-	0.50		

*Recommend PCB solder land [Unit: mm]



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