

SRC1205M

NPN Silicon Transistor

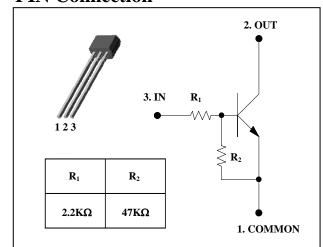
Descriptions

- Switching application
- Interface circuit and driver circuit application

Features

- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process
- High packing density

PIN Connection



Ordering Information

Type NO.	Marking	Package Code	
SRC1205M	1205	TO-92M	

Absolute Maximum Ratings

 $(Ta=25^{\circ}C)$

Characteristic	Symbol	Rating	Unit
Output voltage	Vo	50	V
Input voltage	V _I	15,-5	V
Output current	I _O	100	mA
Power dissipation	P_{D}	400	mW
Junction temperature	TJ	150	°C
Storage temperature range	T_{stg}	-55 ~ 150	°C

Electrical Characteristics

(Ta=25°C)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Output cut-off current	I _{O(OFF)}	$V_0 = 50V, V_1 = 0$	-	-	500	nA
DC current gain	Gı	$V_0 = 5V$, $I_0 = 10mA$	80	200	-	-
Output voltage	$V_{O(ON)}$	$I_0 = 10 \text{mA}, I_1 = 0.5 \text{mA}$	-	0.1	0.3	V
Input voltage (ON)	V _{I(ON)}	$V_0 = 0.2V$, $I_0 = 5mA$	-	ı	1.1	V
Input voltage (OFF)	$V_{I(OFF)}$	$V_0 = 5V$, $I_0 = 0.1 \text{mA}$	0.5	ı	-	V
Transition frequency	f _T *	$V_O=10V$, $I_O=5$ mA, $f=1$ MHz	-	200	-	MHz
Input current	I_1	$V_1 = 5V, I_0 = 0$	-	-	3.6	mA
Input resistor (Input to base)	R ₁	-	1.54	2.2	2.86	KΩ
Input resistor (Base to common)	R ₂	-	33	47	61	ΚΩ

^{* :} Characteristic of transistor only

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Electrical Characteristic Curves

Fig. 1 P_D - Ta

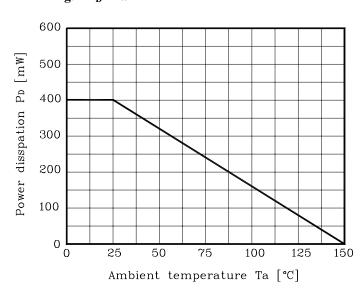


Fig. 2 $I_{\rm O}$ - $V_{\rm I(ON)}$

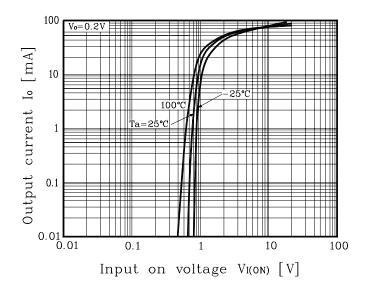


Fig. 3 $I_{\rm O}$ - $V_{I(OFF)}$

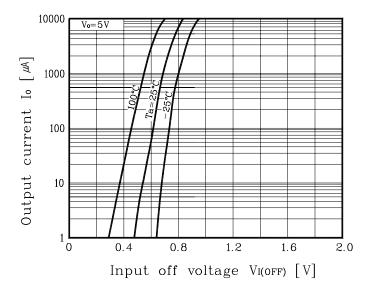
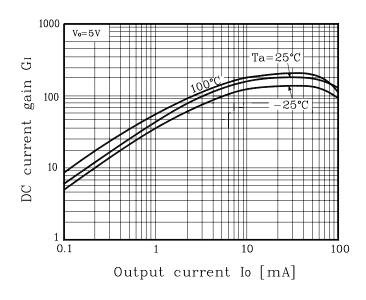
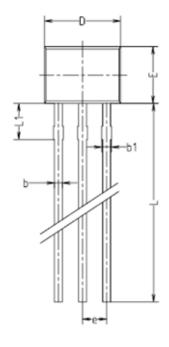


Fig. 4 G_I - I_O

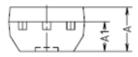


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Outline Dimension







	TO-92M				
SYMBOL	MINIMUM	NOMINAL	MAXIMUM		
Α	2.25	2.30	2.35		
A1	1.50	1.55	1.60		
b	0.40	0.42	0.44		
ь1	0.40	-	0.50		
С	0.40	0.42	0.44		
D	3.93	4.00	4.07		
Ε	2.93	3.00	3.07		
е	1.17	1.27	1.37		
L	14.30	14.50	14.70		
L1	2.05	2.15	2.25		

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