

SRA2204EF

PNP Silicon Transistor

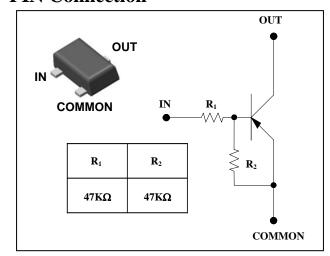
Descriptions

- Switching application
- Interface circuit and driver circuit application

Features

- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process
- High packing density

PIN Connection



Ordering Information

Type NO.	Marking	Package Code	
SRA2204EF	<u>4R</u> □ ① ②	SOT-523F	

①Device Code ② Year&Week Code

Absolute Maximum Ratings

(Ta=25°C)

Characteristic	Symbol	Rating	Unit
Output voltage	Vo	-50	V
Input voltage	V _I	-40, 10	V
Output current	Io	-100	mA
Power dissipation	P _D	150	mW
Junction temperature	T _J	150	°C
Storage temperature range	T _{stg}	-55 ~ 150	°C

Electrical Characteristics

(Ta=25°C)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Output cut-off current	I _{O(OFF)}	$V_0 = -50V, V_1 = 0$	-	-	-500	nA
DC current gain	G _I	$V_0 = -5V$, $I_0 = -10$ mA	80	200	-	-
Output voltage	V _{O(ON)}	I _O =-10mA, I _I =-0.5mA	-	-0.1	-0.3	V
Input voltage (ON)	V _{I(ON)}	$V_0 = -0.2V$, $I_0 = -5mA$	-	-2.8	-5.0	V
Input voltage (OFF)	$V_{I(OFF)}$	$V_0 = -5V$, $I_0 = -0.1$ mA	-1.0	-1.2	-	V
Transition frequency	f_T^*	$V_0 = -10V$, $I_0 = -5mA$, $f = 1MHz$	-	200	-	MHz
Input current	I ₁	$V_1 = -5V, I_0 = 0$	-	-	-0.18	mA
Input resistor (Input to base)	R ₁	-	33	47	61	KΩ
Input resistor (Base to common)	R_2	-	33	47	61	KΩ

^{* :} Characteristic of transistor only

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Electrical Characteristic Curves

Fig. 1 P_D - Ta

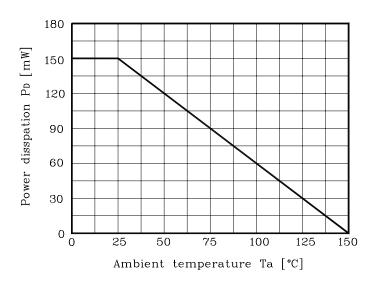


Fig. 2 $I_{\rm O}$ - $V_{\rm I(ON)}$

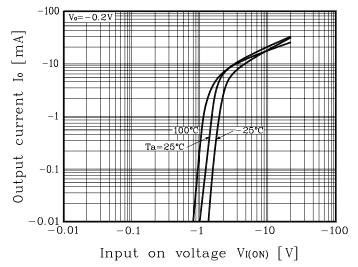


Fig. 3 $I_{\rm O}$ - $V_{\rm I(OFF)}$

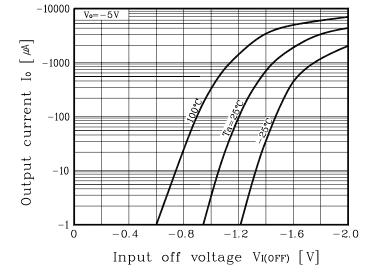
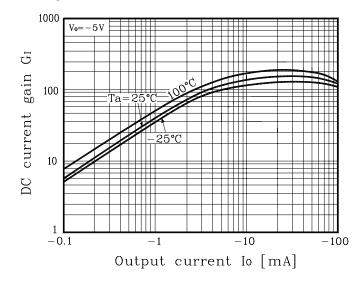


Fig. 4 G_I - I_O

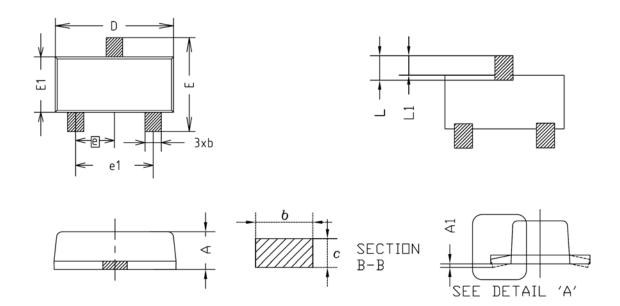


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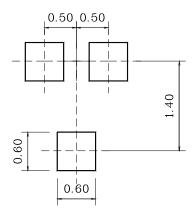
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Outline Dimension



SYMBOL	MILLIMETERS			NOTE
STILL	MINIMUM	NOMINAL	MAXIMUM	NUIL
Α	0.63	0.68	0.73	
A1	0.00	_	0.10	
A2	_	_	_	
b	0.25	0.30	0.35	
_	0.04	0.11	0.20	
D	1.50	1.60	1.70	
Ε	1.50	1.60	1.70	
E1	0.78	0.88	0.98	
е	0.50BSC			
e1	0.90	-	1.10	
L	0.34	0.44	0.54	
L1	0.28	0.34	0.43	

***Recommend PCB solder land [Unit: mm]**



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