

# **SBT3904F**

Base

SOT-23F

**PIN Connection** 

**NPN Silicon Transistor** 

#### **Descriptions**

- General small signal application
- Switching application

#### **Features**

- Low collector saturation voltage
- Collector output capacitance
- Complementary pair with SBT3906F

## **Ordering Information**

Type NO.	Marking	Package Code		
SBT3904F	<u>1A</u> <u></u>	SOT-23F		

① Device Code ② Year&Week Code

#### **Absolute maximum ratings**

Ta=25°C

Collector

Emitter

Characteristic	Symbol	Ratings	Unit
Collector-Base voltage	$V_{CBO}$	60	V
Collector-Emitter voltage	$V_{\sf CEO}$	40	V
Emitter-base voltage	$V_{EBO}$	6	V
Collector current	I <sub>C</sub>	200	mA
Collector dissipation	$P_{C}^{^{\star}}$	350	mW
Junction temperature	Tj	150	°C
Storage temperature range	$T_{stg}$	-55~150	°C

<sup>\* :</sup> Package mounted on 99.5% alumina 10×8×0.6mm

#### **Electrical Characteristics**

Ta=25°C

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Collector-Base breakdown voltage	BV <sub>CBO</sub>	$I_C = 10 \mu A, I_E = 0$	60	-	-	V
Collector-Emitter breakdown voltage	BV <sub>CEO</sub>	$I_C=1$ mA, $I_B=0$	40	-	-	V
Emitter-Base breakdown voltage	BV <sub>EBO</sub>	$I_E=10\mu A,\ I_C=0$	6	-	-	V
Collector cut-off current	I <sub>CEX</sub>	$V_{CE}=30V$ , $V_{EB}=3V$	-	-	50	nA
DC current gain	h <sub>FE</sub>	$V_{CE}=1V$ , $I_{C}=10mA$	100	-	300	-
Collector-Emitter saturation voltage	V <sub>CE(sat)</sub>	$I_C=50\text{mA}, I_B=5\text{mA}$	-	-	0.3	V
Transition frequency	f <sub>T</sub>	$V_{CE}$ =20V, $I_{C}$ =10mA, $f$ =100MHz	300	-	-	MHz
Collector output capacitance	C <sub>ob</sub>	$V_{CB}=5V$ , $I_{E}=0$ , $f=1MHz$	-	-	4	рF
Delay time	t <sub>d</sub>	$V_{CC}=3V_{dc}$ , $V_{BE(off)}=0.5V_{dc}$ .	-	-	35	ns
Rise time	t <sub>r</sub>	$I_C=10\text{mA}_{dc}$ , $I_{B1}=1\text{mA}_{dc}$	-	-	35	ns
Storage time	t <sub>s</sub>	$V_{CC}=3V_{dc}$ , $I_{C}=10mA_{dc}$ ,	-	_	200	ns
Fall Time	t <sub>f</sub>	$I_{B1} = I_{B2} = 1 \text{mA}_{dc}$	-	_	50	ns

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## **Electrical Characteristic Curves**

Fig. 1  $P_{C}$ - $T_a$ 

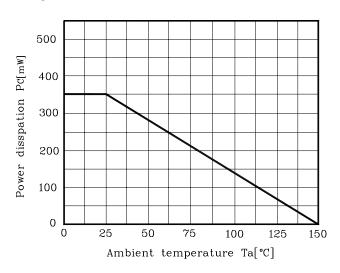


Fig. 2  $h_{FE}$   $I_C$ 

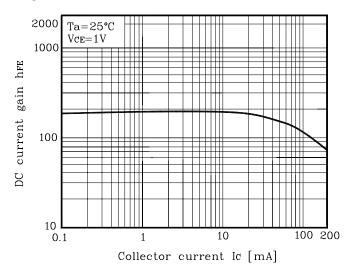
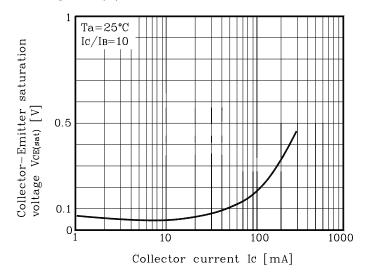
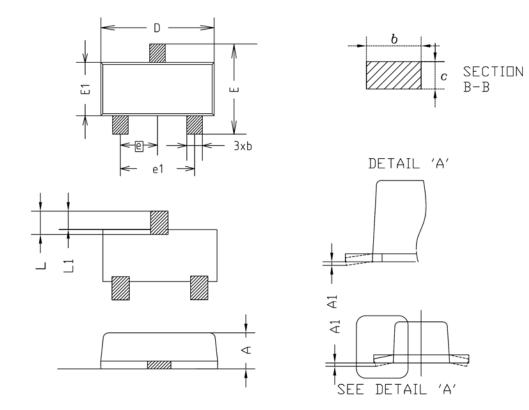


Fig. 3  $V_{\text{CE(sat)}}\text{-}I_{\text{C}}$ 

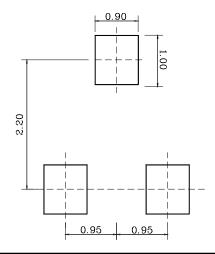


## **Outline Dimension**



SYMBOL	MILLIMETER(mm)			NOTE	
STADUL	MINIMUM	NDMINAL	MAXIMUM	NUIL	
Α	0.80	0.90	1.00		
A1	0.00	-	0.10		
b	0.35	0.40	0.45		
C	0.10	0.15	0.20		
D	2.80	2.90	3.00		
Ε	2.30	2.40	2.50		
E1	1.50	1.60	1.70		
е	0.95BSC				
e1	1.80	1.90	2.00		
L	0.48	0.58	0.68		
L1	0.30	-	0.50		

#### \*Recommend PCB solder land [Unit: mm]



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