

**Schottky Barrier Rectifier** 

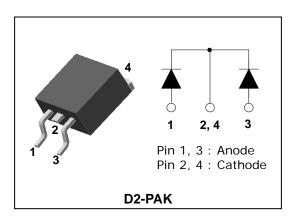
#### **DUAL COMMON CATHODE SCHOTTKY RECTIFIER**

#### **Features**

- Low forward voltage drop and leakage current
- Low power loss and High efficiency
- High surge capability
- · Dual common cathode rectifier
- Full lead(Pb)-free device and RoHS compliant device

#### **Applications**

- Power supply Output rectification
- Converter
- · Free-wheeling diode
- Reverse battery protection
- Power inverters



#### **Product Characteristics**

I <sub>F(AV)</sub>	2 X 5A
$V_{RRM}$	60V
V <sub>FM</sub> at 125℃	0.55V
I <sub>FSM</sub>	120A

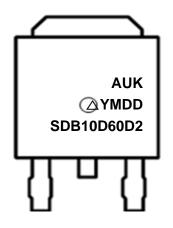
#### **Description**

The SDB10D60D2 has two schottky barriers arranged in a common cathode configuration. Typical applications are in switching power supplies, converters, free-wheeling diodes, and reverse battery protection.

#### **Ordering Information**

Device	Marking Code	Package	Packaging
SDB10D60D2	SDB10D60D2	D2-PAK	Tape & Reel

#### **Marking Information**



AUK = Manufacture Logo

 $\Delta$  = Control Code of Manufacture

YMDD = Date Code Marking

-. Y = Year Code

-. M = Monthly Code

-. DD = Daily Code

SDB10D60D2 = Specific Device Code

KSD-D6O026-000

## **Absolute Maximum Ratings (Limiting Values)**

Characteristic		Symbol	Value	Unit	
Maximum repetitive reverse voltage Maximum working peak reverse voltage Maximum DC blocking voltage		V <sub>RRM</sub> V <sub>RWM</sub> V <sub>R</sub>	60	V	
Maximum average forward rectified current	per diode		5	^	
Maximum average forward rectified current	total device	I <sub>F(AV)</sub>	10	Α	
Peak forward surge current 8.3ms single half sine-wave superimposed on rated load per diode		I <sub>FSM</sub>	120	Α	
Storage temperature range		T <sub>stg</sub>	-55 to +150	$^{\circ}$ C	
Maximum operating junction temperature		Tj	150		

#### **Thermal Characteristics**

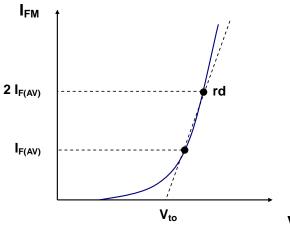
Characteristic		Symbol	Value	Unit
Maximum thermal registeres investige to acco	per diode	D	3.0	℃/W
Maximum thermal resistance junction to case	total device	$R_{th(j-c)}$	2.8	

## **Electrical Characteristics (Per Diode)**

Characteristic	Symbol	Test Condition		Min.	Тур.	Max.	Unit
Peak forward voltage drop	V <sub>FM</sub> <sup>(1)</sup>	I <sub>FM</sub> = 5A	T <sub>j</sub> =25℃	-	ı	0.65	- V
			T <sub>j</sub> =125℃	-	-	0.55	
Reverse leakage current	I <sub>RM</sub> <sup>(1)</sup>	$V_R = V_{RRM}$	T <sub>j</sub> =25℃	-	-	0.5	m ^
			T <sub>j</sub> =125℃	-	-	50	mA
Junction capacitance	C <sub>j</sub>	$V_R = 10V_{DC}$ , $f=1MHz$		-	160	-	pF

Note : (1) Pulse test :  $t_P \le 380 us$ , Duty cycle  $\le 2\%$ 

To evaluate the conduction losses use the following equation:  $P_F$  = 0.36  $I_{F(AV)}$  + 0.043  $I_{F}^{\ 2}_{(RMS)}$ 



Forward Voltage :  $V_{FM} = V_{to} + rd I_{FM}$ 

Conduction Loss :  $P_F = V_{to} I_{F(AV)} + rd I_F^2_{(RMS)}$ 

 $V_{FM}$ 

#### **Rating and Characteristic Curves**

Fig. 1) Typical Forward Characteristics (Per diode)

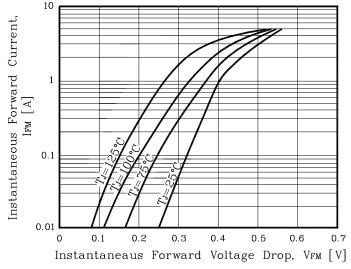


Fig. 2) Typical Reverse Characteristics (Per diode)

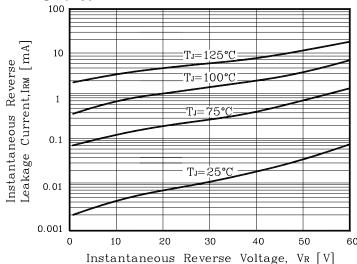


Fig. 3) Maximum Forward Derative Curve

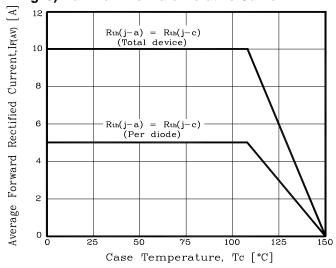


Fig. 4) Forward Power Dissipation (Per diode)

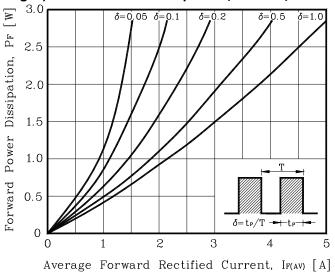


Fig. 5) Maximum Non-Repetitive Peak Forward Surge Current (Per diode)

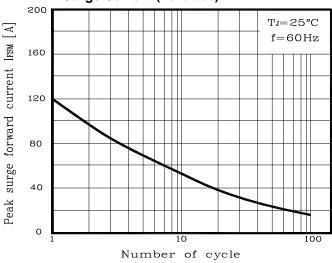
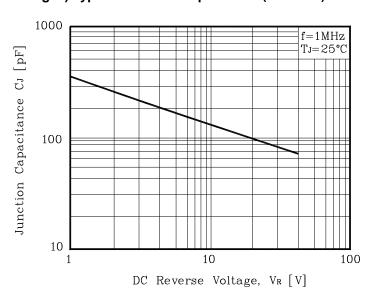


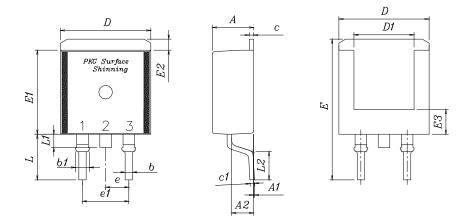
Fig. 6) Typical Junction Capacitance (Per diode)



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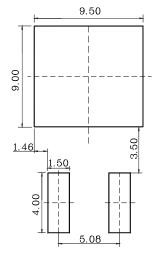
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## **Package Outline Dimension**



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SYMBOL	MINIMUM	NOMINAL	MAXIMUM	
Α	4.35	4.50	4.65	
A1	_	_	0.15	
A2	2.20	2.40	2.60	
b	0.70	0.80	0.90	
b1	1.17	1.27	1.37	
С	0.40	0.50	0.60	
c1	0.40	0.50	0.60	
D	9.80	10.00	10.20	
D1	6.40	6.60	6.80	
E	15.00	15.40	15.80	
E1	9.05	9.20	9.35	
E2	1.00	1.20	1.40	
E3	2.50	2.70	2.90	
е	2.34	2.54	2.74	
e1	4.88	5.08	5.28	
L	4.60	5.00	5.40	
_L1	1.40	1.45	1.50	
L2	2.50	_	_	

#### \* Recommend PCB solder land (Unit : mm)



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