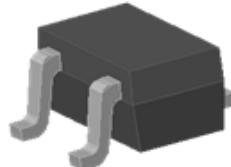


## Small Signal Fast Switching Diode

### General Description

Dual general-purpose switching diodes, fabricated in planar technology, and packaged in small SOT-523 surface mounted device (SMD) packages.



SOT-523

### Features and Benefits

- Silicon epitaxial planar diode
- High switching speed:  $t_{rr} \leq 4\text{ns}$
- Low forward drop voltage and low leakage current
- Full lead (Pb)-free and RoHS compliant device
- Available in "Green" device



RoHS



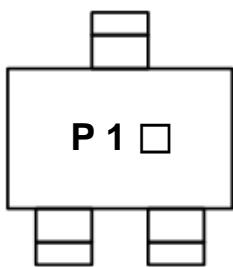
### Applications

- Ultra high speed switching application

### Ordering Information

Part Number	Marking Code	Package	Packaging
<b>SDS7000E</b>	<b>P1 □</b>	<b>SOT-523</b>	<b>Tape &amp; Reel</b>

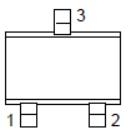
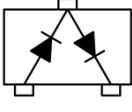
### Marking Information



**P 1** = Specific Device Code

**□** = Year & Week Code Marking

### Pinning Information

Pin	Description	Simplified Outline	Graphic Symbol
1	Anode (Diode 1)		
2	Cathode (Diode 2)		
3	Cathode (Diode 1), Anode (Diode 2)		

**Absolute Maximum Ratings** ( $T_{amb}=25^{\circ}C$ , Unless otherwise specified)

Characteristic	Symbol	Ratings	Unit
Maximum repetitive peak reverse voltage	$V_{RM}$	85	V
Continuous reverse voltage	$V_R$	80	V
Maximum average forward rectified current	$I_O$	100	mA
Forward current (DC)	$I_F$	100	mA
Maximum repetitive peak forward current	$I_{FM}$	300	mA
Non-repetitive peak forward surge current( $t=10ms$ )	$I_{FSM}$	2	A
Power dissipation <sup>1)</sup>	$P_D$	150	mW

<sup>1)</sup> Device mounted on FR-4 board with recommended pad layout.

**Thermal Characteristics** ( $T_{amb}=25^{\circ}C$ , Unless otherwise specified)

Characteristic	Symbol	Ratings	Unit
Thermal resistance, junction to ambient <sup>1)</sup>	$R_{th(j-a)}$	830	°C/W
Operating junction temperature	$T_j$	150	°C
Storage temperature range	$T_{stg}$	-55 ~ 150	°C

<sup>1)</sup> Device mounted on FR-4 board with recommended pad layout.

**Electrical Characteristics** ( $T_{amb}=25^{\circ}C$ , Unless otherwise specified)

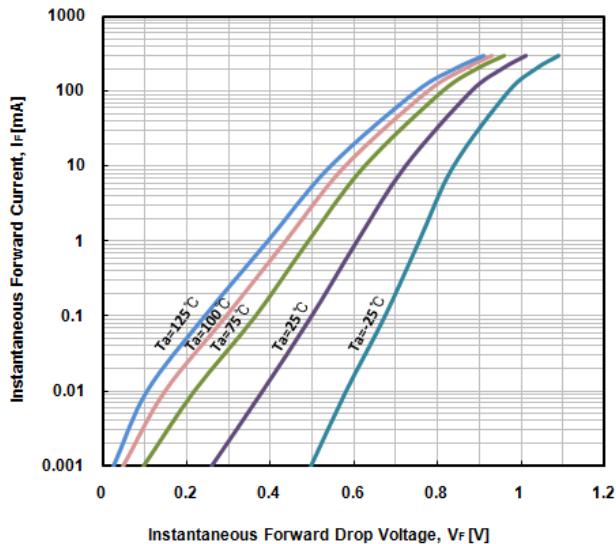
Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Forward voltage <sup>2)</sup>	$V_{F(1)}$	$I_F=1mA$	-	0.6	-	V
	$V_{F(2)}$	$I_F=10mA$	-	0.7	-	V
	$V_{F(3)}$	$I_F=100mA$	-	0.9	1.2	V
Reverse leakage current <sup>3)</sup>	$I_R$	$V_R=80V$	-	-	0.5	uA
Total capacitance	$C_T$	$V_R=0V, f=1MHz$	-	2.2	4.0	pF
Reverse recovery time	$t_{rr}$	$I_F=10mA$ (Fig. 5)	-	1.6	4.0	ns

<sup>2)</sup> Pulse test:  $t_p \leq 380\mu s$ , Duty cycle  $\leq 2\%$

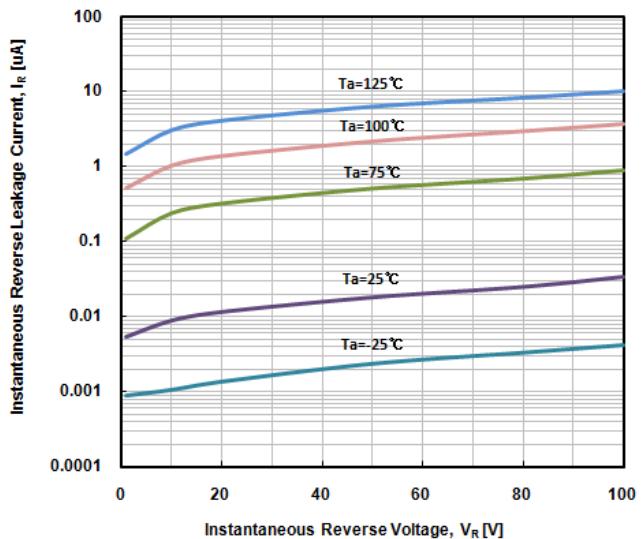
<sup>3)</sup> Pulse test:  $t_p \leq 5ms$ , Duty cycle  $\leq 2\%$

## Rating and Characteristic Curves

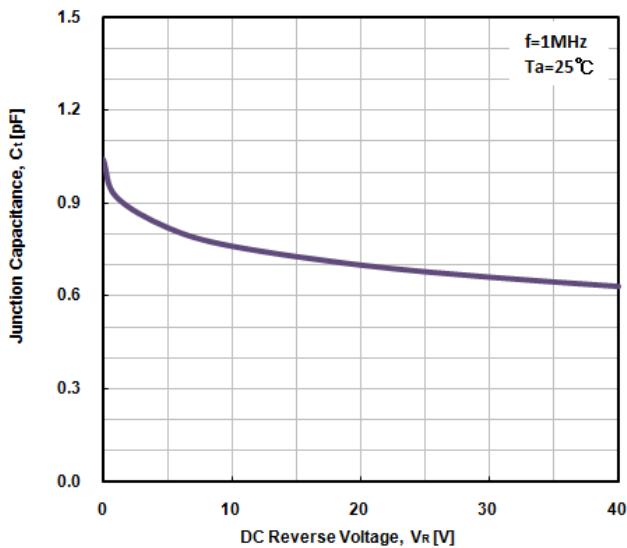
**Fig. 1) Typical Forward Characteristics**



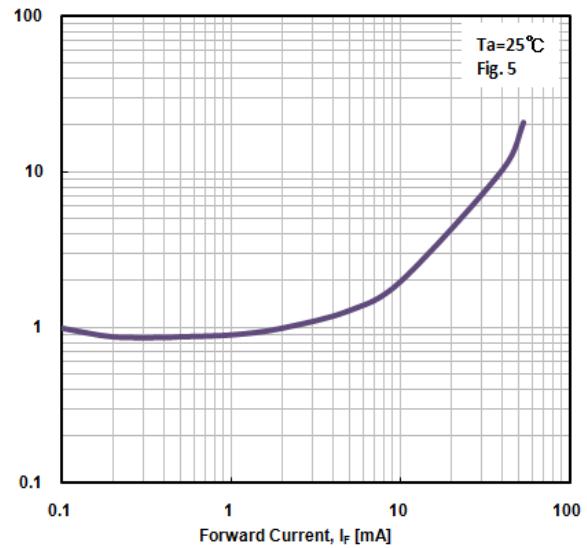
**Fig. 2) Typical Reverse Characteristics**



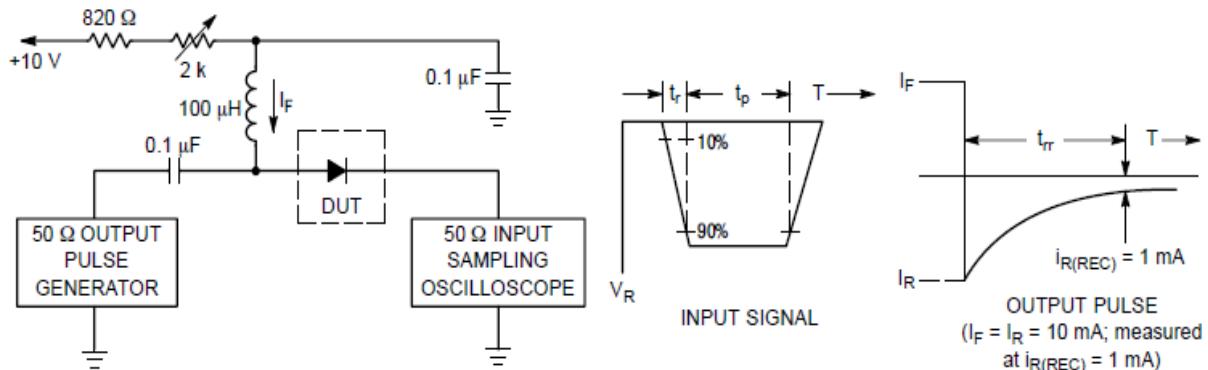
**Fig. 3) Typical Total Capacitance Characteristics**

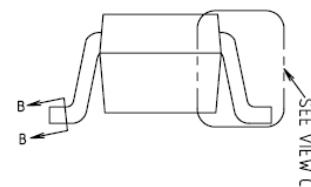
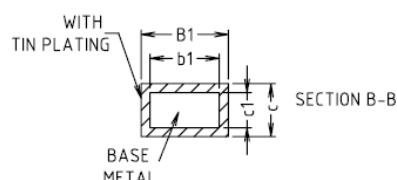
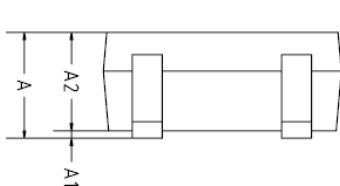
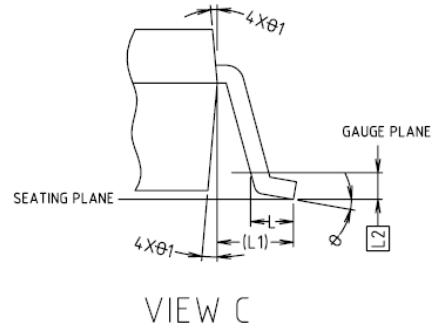
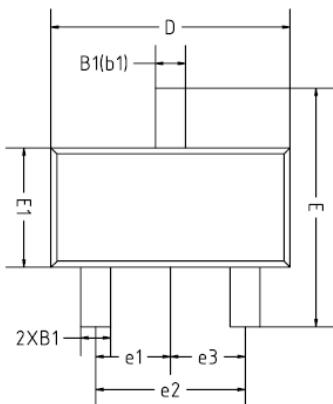


**Fig. 4) Reverse Recovery Time vs. Forward Current**

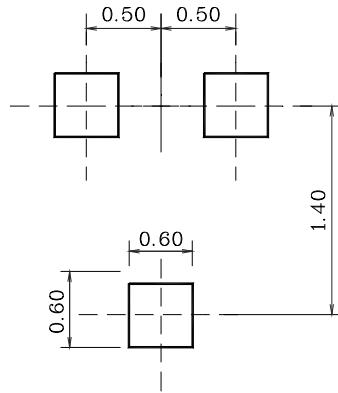


**Fig. 5) Reverse recovery time equivalent test circuit**



**Package Outline Dimensions**

SYMBOL	MILLIMETERS			NOTE
	MINIMUM	NOMINAL	MAXIMUM	
A	—	—	0.80	
A1	0.00	—	0.10	
A2	0.65	0.70	0.75	
B1	0.19	—	0.24	
b1	0.17	—	0.21	
c	0.13	—	0.15	
c1	0.10	—	0.12	
D	1.48	1.58	1.68	
E	1.50	1.60	1.70	
E1	0.66	0.76	0.86	
e1	—	0.50 BSC	—	
e2	—	1.00 BSC	—	
e3	—	0.50 BSC	—	
L	0.15	0.205	0.30	
L1	—	0.40 REF	—	
L2	—	0.15 BSC	—	
$\theta$	0°	—	8°	
$\theta_1$	4°	—	10°	

**※ Recommend PCB solder land (Unit : mm)**

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