

Small Signal Zener Diode

General Description

These diodes small signal Zener diodes, fabricated in planar technology. Miniature surface mount package is excellent for hand-held and portable applications where is space is limited.

Features and Benefits

- Silicon epitaxial planar diode
- · Low Zener impedance and low leakage current
- Standard Zener voltage tolerance is 4.3%.
- Full lead (Pb)-free device and RoHS compliant device
- Available in "Green" device

Applications

• Voltage regulator

Ordering Information

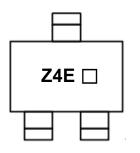






Part Number	Marking Code	Package	Packaging
SDZ6V2WK	Z4E	SOT-23	Tape & Reel

Marking Information



Z4E = Specific Device Code

□ = Year & Week Code Marking

Pinning Information

Pin	Description	Simplified Outline	Graphic Symbol
1	Anode (Diode 1)	3	
2	Anode (Diode 2)		×
3	Common Cathode	1 🗄 🗄 2	

Absolute Maximum Ratings (Tamb=25°C, Unless otherwise specified)

Characteristic	Symbol	Ratings	Unit
Power dissipation ¹⁾	P _D	200	mW
Operating junction temperature	TJ	150	°C
Storage temperature range	T _{stg}	-55℃ to +150℃	°C

¹⁾ Device mounted on FR-4 board with recommended pad layout.

Thermal Characteristics (T_{amb}=25°C, Unless otherwise specified)

Characteristic	Symbol	Ratings	Unit
Thermal resistance, junction to ambient ¹⁾	$R_{th(j-a)}$	625	°C/W

¹⁾ Device mounted on FR-4 board with recommended pad layout.

Electrical Characteristics (T_{amb}=25°C, Unless otherwise specified)

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Zener voltage	Vz	I _Z =5mA	5.93	-	6.47	V
Dynamic impedance	Z _{ZT}	I _Z =5mA	-	-	10	Ω
KNEE dynamic impedance	Z _{ZK}	I _Z =0.25mA	-	-	1300	Ω
Reverse leakage current	I _R	V _R =4V	-	-	1	μA

Rating and Characteristic Curves

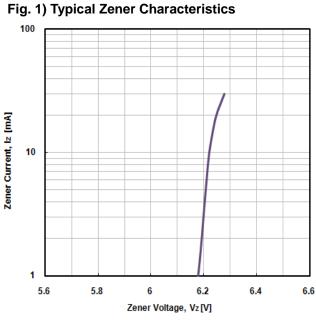
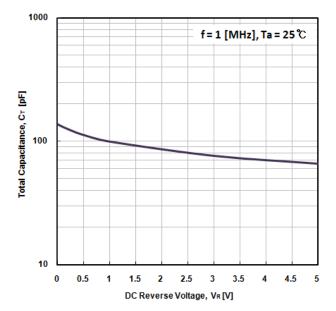


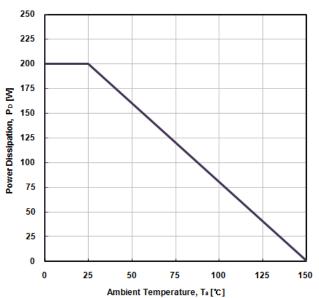
Fig. 3) Typical Capacitance Characteristics



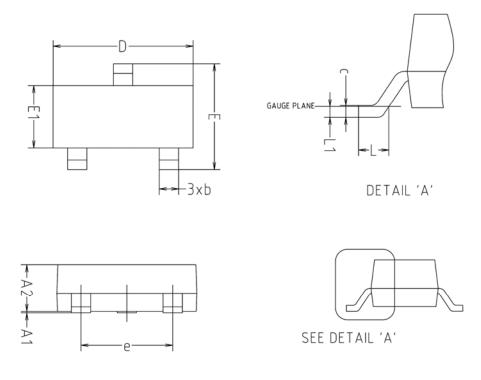
6.8 Vz [V] @ Iz = 5 [mA] 6.6 6.4 Zener Voltage, Vz [V] 6.2 6 5.8 5.6 -25 0 25 50 75 100 125 Ambient Temperature, Ta [°C]

Fig. 2) Zener voltage vs. Ambient Temperature

Fig. 4) Power Dissipation vs. Ambient Temperature

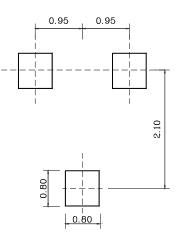


Package Outline Dimensions



SYMBOL		NOTE		
STIDUL	MINIMUM	NOMINAL	MAXIMUM	NOTE
A1	0.00	-	0.10	
A2	0.82	-	1.02	
b	0.39	0.42	0.45	
с	0.09	0.12	0.15	
D	2.80	2.90	3.00	
E	2.20	2.40	2.60	
E1	1.20	1.30	1.40	
е	1.90BSC			
L	0.20	-	-	
L1	0.12BSC			

% Recommend PCB solder land (Unit : mm)



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