

Small Signal Zener Diode

General Description

These diodes small signal Zener diodes, fabricated in planar technology. Miniature surface mount package is excellent for hand-held and portable applications where is space is limited.

Features and Benefits

- Silicon epitaxial planar diode
- Low Zener impedance and low leakage current
- Standard Zener voltage tolerance is 4.3%.
- Full lead (Pb)-free device and RoHS compliant device
- · Available in "Green" device



SOT-23F







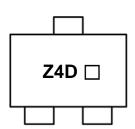
Applications

Voltage regulator

Ordering Information

Part Number	Marking Code	Package	Packaging
SDZ6V2WAF	Z4D □	SOT-23F	Tape & Reel

Marking Information



Z4D = Specific Device Code

☐ = Year & Week Code Marking

Pinning Information

Pin	Description	Simplified Outline	Graphic Symbol
1	Cathode (Diode 1)	3	[3]
2	Cathode (Diode 2)		* *
3	Common Anode	1 2	1 2

Absolute Maximum Ratings (T_{amb}=25°C, Unless otherwise specified)

Characteristic	Symbol	Ratings	Unit
Power dissipation 1)	P_{D}	200	mW
Operating junction temperature	TJ	150	°C
Storage temperature range	T _{stg}	-55°C to +150°C	°C

¹⁾ Device mounted on FR-4 board with recommended pad layout.

Thermal Characteristics (T_{amb}=25°C, Unless otherwise specified)

Characteristic	Symbol	Ratings	Unit
Thermal resistance, junction to ambient 1)	R _{th(j-a)}	625	°C/W

¹⁾ Device mounted on FR-4 board with recommended pad layout.

Electrical Characteristics (T_{amb}=25°C, Unless otherwise specified)

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Zener voltage	V_{Z}	I _Z =5mA	5.93	-	6.47	V
Dynamic impedance	Z _{ZT}	I _Z =5mA	-	-	10	Ω
KNEE dynamic impedance	Z_{ZK}	I _Z =0.25mA	-	-	1300	Ω
Reverse leakage current	I _R	V _R =4V	-	-	1	μА

Rating and Characteristic Curves

Fig. 1) Typical Zener Characteristics

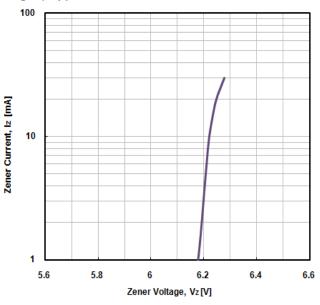


Fig. 3) Typical Capacitance Characteristics

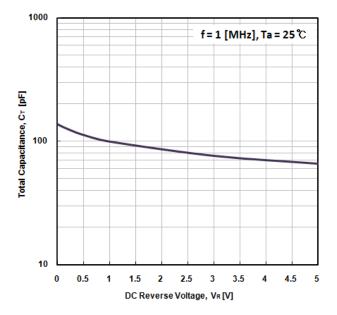


Fig. 2) Zener voltage vs. Ambient Temperature

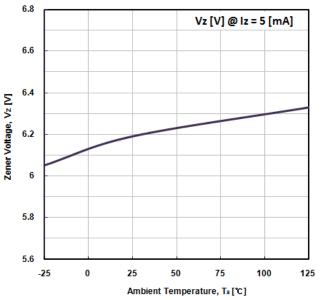
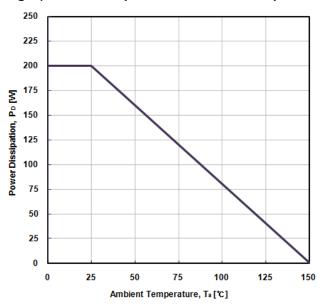
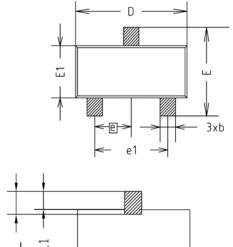
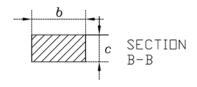


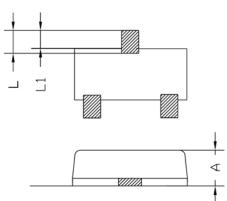
Fig. 4) Power Dissipation vs. Ambient Temperature

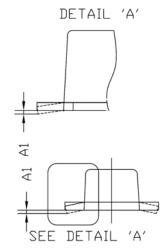


Package Outline Dimensions



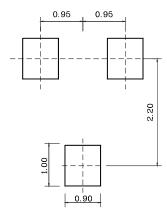






SYMBOL	1	NOTE		
STHIDGE	MINIMUM	NOMINAL	MAXIMUM	NUIL
Α	0.80	0.90	1.00	
A1	0.00	-	0.10	
b	0.35	0.40	0.45	
C	0.10	0.15	0.20	
D	2.80	2.90	3.00	
Ε	2.30	2.40	2.50	
E1	1.50	1.60	1.70	
е	0.95BSC			
e1	1.80	1.90	2.00	
L	0.48	0.58	0.68	
L1	0.30	-	0.50	

X Recommend PCB solder land (Unit: mm)



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