

## 50 Amps, 60Volts N-Channel MOSFET

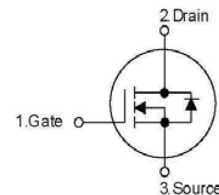
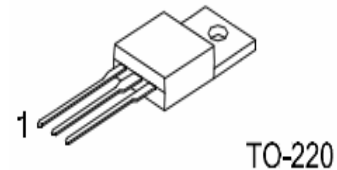
### ■ Description

The HX5N06 is a N-Channel enhancement MOSFET and is designed to have better characteristics, such as superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for low voltage applications such as automotive DC/DC converters, and high efficiency switching for power management in portable and battery operated products.

### ■ Features

- $R_{DS(ON)}=0.023\Omega@V_{GS}=10V$
- Low gate charge(typical 31nC)
- Low reverse transfer capacitance(CRSS=typical 80pF)
- Fast switching capability
- Avalanche energy specified
- Improved dv/dt capability,high ruggedness

### ■ Symbol



### ■ Ordering Information

Order Number		Package	Pin Assignment			Packing
Normal	Lead Free Plating		1	2	3	
HX5N06-TA3-T	HX5N06L-TA3-T	TO-220	G	D	S	Tube

Note:Pin Assignment: G:Gate D:Drain S:Source

HX5N06L-TA3-T	(1) Packing Type (2) Package Type (3) Lead Plating	(1)T:Tube,R:Tape Reel (2)TA3:TO-220 (3)L:Lead Free Plating Blank: Pb/Sn
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### ■ Absolute Maximum Ratings( $T_c=25^\circ\text{C}$ , unless otherwise specified)

Parameter	Symbol	Ratings	Units
Drain-Source Voltage	$V_{DSS}$	60	V
Gate-Source Voltage	$V_{GSS}$	$\pm 20$	V
Drain Current Continuous	$I_D$	$T_c=25^\circ\text{C}$	50
		$T_c=100^\circ\text{C}$	35
Drain Current Pulsed (Note 1)	$I_{DP}$	200	A
Avalanche Energy	$E_{AR}$	Repetitive (Note 1)	13
		Single Pulse(Note 2)	480
Peak Diode Recovery dv/dt(Note 3)	dv/dt	7.0	v/ns
Total Power Dissipation	$P_D$	$T_c=25^\circ\text{C}$	120
		Derate above $25^\circ\text{C}$	0.8
Operation Junction Temperature	$T_J$	-55 to +150	$^\circ\text{C}$
Storage temperature 1/8" from case for 5 seconds	$T_{STG}$	-55~+150	$^\circ\text{C}$

### ■ Thermal Characteristics

Parameter	Symbol	Ratings	Units
Thermal Resistance Junction-Ambient	$R_{thJA}$	62.5	°C/W
Thermal Resistance, Case-to-Sink Typ.	$R_{thCS}$	0.5	
Thermal Resistance Junction-Case	$R_{thJC}$	1.24	

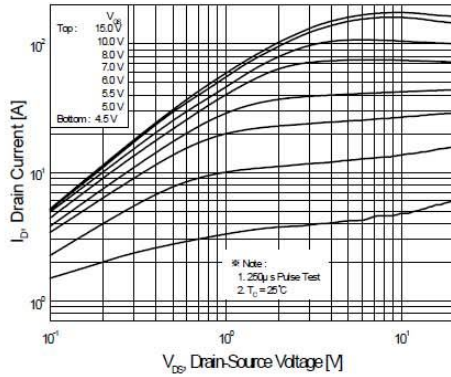
### ■ Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless Otherwise specified.)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=250\mu A$	60	–	–	V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=60V, V_{GS}=0V$	–	–	1	$\mu A$
		$V_{DS}=48V, T_C=150^\circ\text{C}$	–	–	10	$\mu A$
Gate-Body Leakage Current	Forward	$V_{GS}=20V, V_{DS}=0V$	–	–	100	nA
	Reverse	$V_{GS}=-20V, V_{DS}=0V$	–	–	-100	nA
Breakdown Voltage Temperature Coefficient	$\Delta BV_{DSS}/\Delta T_J$	$I_D=250\mu A$	–	0.06	–	V/°C
<b>On Characteristics</b>						
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	2.2	–	3.8	V
Static Drain-Source On-Resistance	$R_{DS(ON)}$	$V_{DS}=10V, I_D=25A$		0.019	0.023	$\Omega$
<b>Dynamic Characteristics</b>						
Input Capacitance	$C_{ISS}$	$V_{DS}=25V, V_{GS}=0V$ $f=1\text{MHz}$	–	900	1220	pF
Output Capacitance	$C_{OSS}$		–	430	550	pF
Reverse Transfer Capacitance	$C_{RSS}$		–	80	100	pF
<b>Switching Characteristics</b>						
Turn-On Delay Time	$t_{D(ON)}$	$V_{DD}=30V, I_D=25A,$ $R_G=50\Omega$ (Note4,5)	–	40	60	ns
Rise Time	$t_R$		–	100	200	ns
Turn-Off Delay Time	$t_{D(OFF)}$		–	90	180	ns
Fall Time	$t_F$		–	80	160	ns
Total Gate Charge	$Q_G$	$V_{DS}=48V, V_{GS}=10V,$ $I_D=50A$ (Note4,5)	–	30	40	nC
Gate-Source Charge	$Q_{GS}$		–	9.6	-	nC
Gate-Drain Charge	$Q_{GD}$		–	10	-	nC
<b>Drain-Source Diode Characteristics</b>						
Drain-Source Diode Forward Voltage	$V_{SD}$	$V_{GS}=0V, I_{SD}=50A$	–	–	1.5	V
Continuous Drain-Source Current	$I_{SD}$				50	A
Pulsed Drain-Source Current	$I_{SM}$				200	A
Reverse Recovery Time	$t_{RR}$	$V_{GS}=0V, I_{SD}=50A,$ $di/dt=100A/\mu s$ (Note4)		54		ns
Reverse Recovery Charge	$Q_{RR}$			81		nC

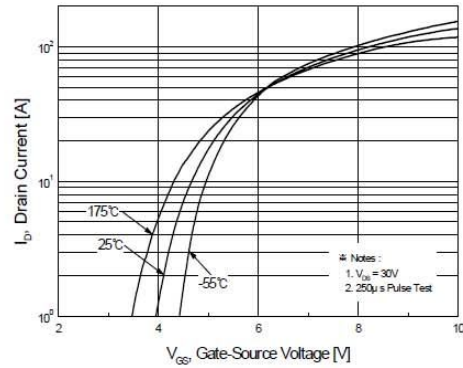
Notes:

- 1.Repetitive Rating: Pulse width limited by maximum junction temperature
- 2.L=5.6mH,  $I_{AS}=50A, V_{DD}=25V, R_G=0\Omega$ , Starting  $T_J=25^\circ\text{C}$
3. $I_{SD}\leq 50A, di/dt\leq 300A/\mu s, V_{DD}\leq BV_{DSS}$ , Starting  $T_J=25^\circ\text{C}$
- 4.Pulse Test: Pulse Width $\leq 300\mu s$ , Duty Cycle $\leq 2\%$
- 5.Essentially Independent of Operating Temperature

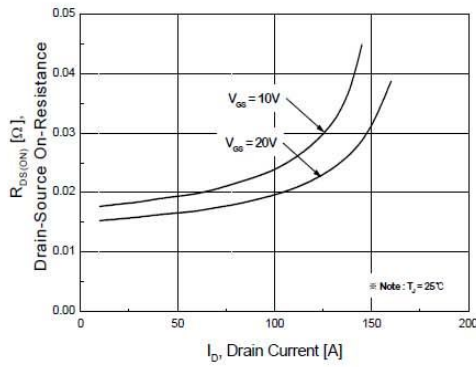
■ **Typical Characteristics**



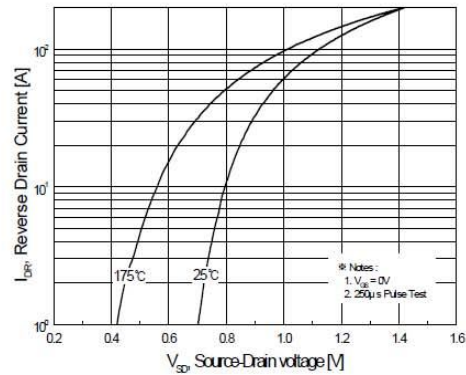
**Figure 1. On-Region Characteristics**



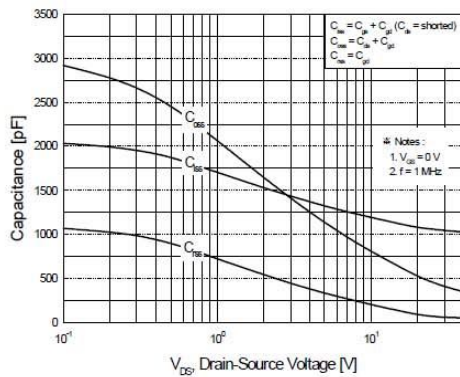
**Figure 2. Transfer Characteristics**



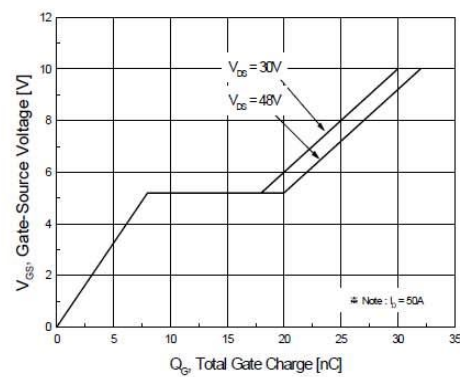
**Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage**



**Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature**

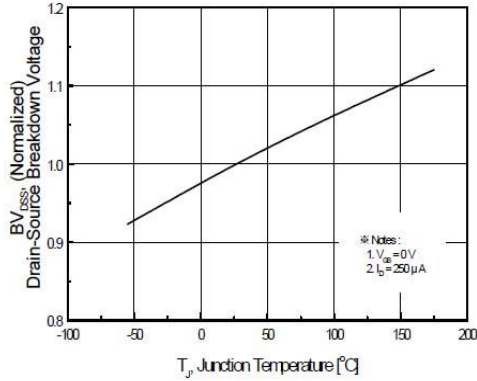


**Figure 5. Capacitance Characteristics**

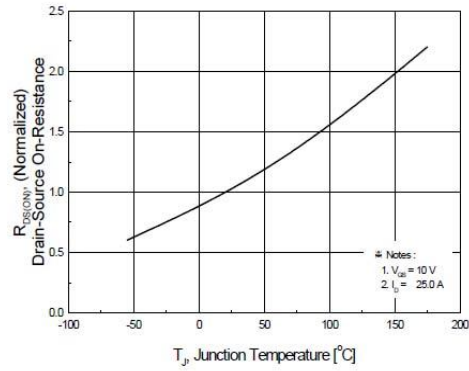


**Figure 6. Gate Charge Characteristics**

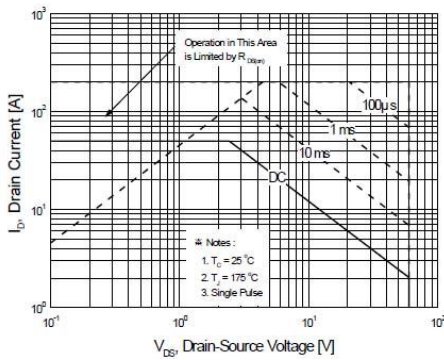
■ **Typical Characteristics (Continued)**



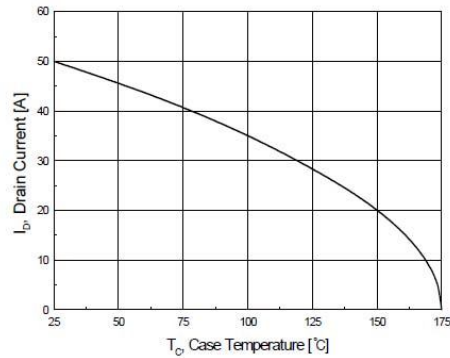
**Figure 7. Breakdown Voltage Variation vs. Temperature**



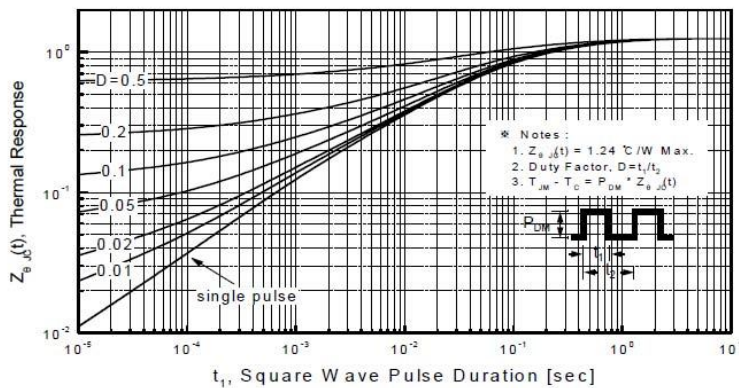
**Figure 8. On-Resistance Variation vs. Temperature**



**Figure 9. Maximum Safe Operating Area**



**Figure 10. Maximum Drain Current vs. Case Temperature**



**Figure 11. Transient Thermal Response Curve**