

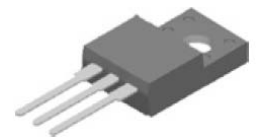
600V/2.0A Power MOSFET (N-Channel)

General Description

- MSU2N60 is a N-Channel enhancement mode power MOSFET with advanced technology. It is designed to have better characteristics, such as fast switching time, low gate charge, minimized on-state resistance and withstanding high energy pulse in the avalanche and commutation modes. These devices are well suited for high efficiency switching mode power supply applications.
- MSU2N60 are available in TO-220, TO-220F, DPAK (TO-252) and IPAK (TO-251) packages.



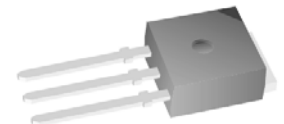
TO-220



TO-220F



DPAK
(TO-252)



IPAK
(TO-251)

Features

- $R_{DS(ON)} \leq 5\Omega @ V_{GS}=10V$
- Ultra low gate charge (Typ.9.0nC)
- Low Crss (Typ.5.0pF)
- Fast switching capability
- 100% Avalanche energy tested
- Improved dv/dt capability, high ruggedness
- RoHS Compliance and Halogen free



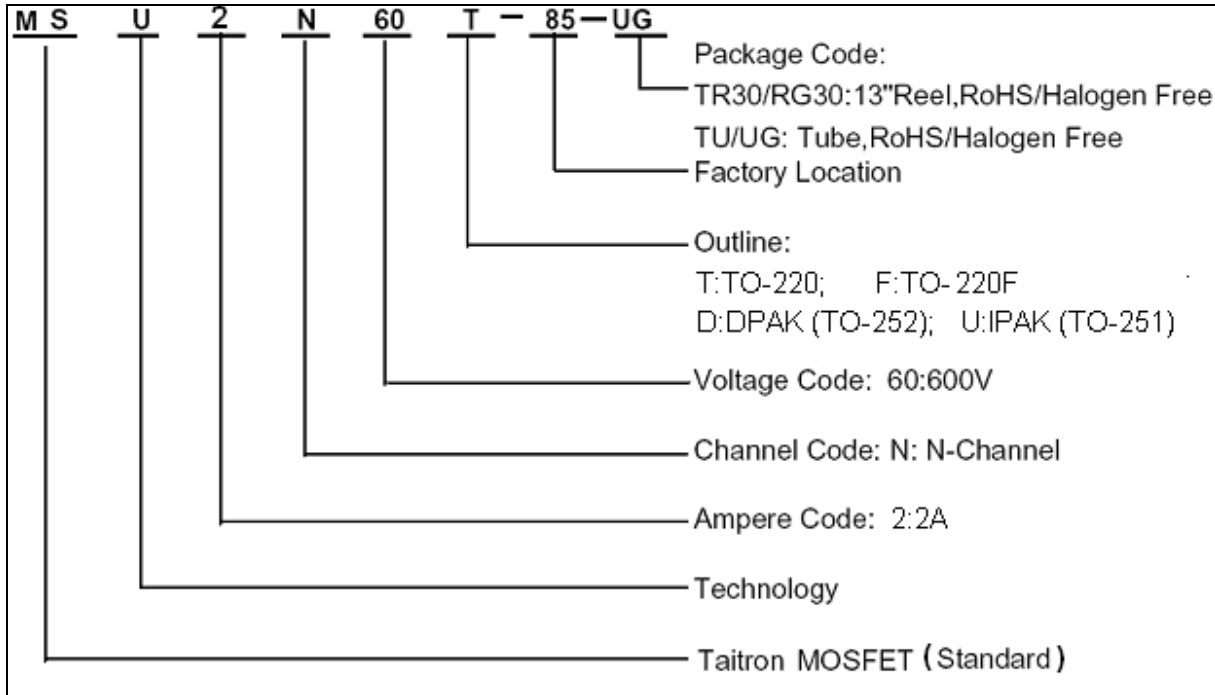
Application

- DC to DC Converter
- Adapter
- SMPS Application.

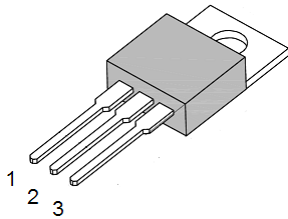
600V/2.0A POWER MOSFET (N-Channel)

MSU2N60

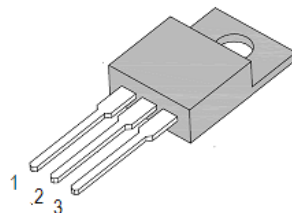
Ordering Information



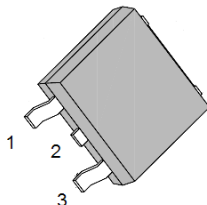
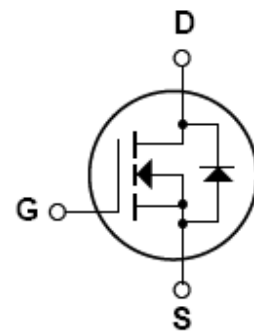
Pin Configuration and Symbol



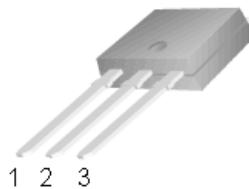
1: GATE 2: DRAIN 3: SOURCE
TO-220



1: GATE 2: DRAIN 3: SOURCE
TO-220F



1: GATE 2: DRAIN 3: SOURCE
DPAK (TO-252)



1: GATE 2: DRAIN 3: SOURCE
IPAK (TO-251)

600V/2.0A POWER MOSFET (N-Channel)

MSU2N60

Absolute Maximum Ratings ($T_C=25^\circ\text{C}$ unless otherwise specified, Note)

| Symbol | Description | | Ratings | Unit |
|------------------------|--|---|-------------|-------|
| V_{DSS} | Drain-Source Voltage | | 600 | V |
| V_{GSS} | Gate-Source Voltage | | ± 30 | V |
| I_D | Drain Current -Continuous | | 2.0 | A |
| I_{DM} | Drain Current -Pulsed (note1) | | 8.0 | A |
| E_{AS} | Avalanche Energy | Single Pulsed (Note2) | 140 | mJ |
| E_{AR} | | Repetitive (Note1) | 4.5 | |
| I_{AR} | Avalanche Current (note1) | | 2.0 | A |
| dv/dt | Peak Diode Recovery dv/dt (note3) | | 4.5 | V/ns |
| P_D | Power Dissipation | TO-220 | 54 | W |
| | | TO-220F | 23 | |
| | | I _{PAK} (TO-251)/D _{PAK} (TO-252) | 44 | |
| R_{θJA} | Thermal Resistance (Junction-to-Ambient) | TO-220/ TO-220F | 62.5 | °C/ W |
| | | I _{PAK} (TO-251)/D _{PAK} (TO-252) | 50 | |
| R_{θJC} | Thermal Resistance (Junction-to-Case) | TO-220 | 2.32 | °C/ W |
| | | TO-220F | 5.5 | |
| | | I _{PAK} (TO-251)/D _{PAK} (TO-252) | 2.87 | |
| T_J | Junction Temperature | | +150 | ° C |
| T_{STG} | Storage Temperature Range | | -55 to +150 | ° C |

Note: Absolute maximum ratings indicate limits beyond which damage to the device may occur.
 For guarantee specification and test conditions, see the Electrical Characteristics.
 The guaranteed specification apply only for the test conditions listed.

Note1: Repetitive Rating: Pulse width limited by maximum junction temperature
 2: L=64mH, I_{AS}=2.0A, V_{DD}=50V, R_G=25Ω, Starting T_J=25°C
 3: I_{sd}≤2.4A, di/dt≤200A/us, V_{DD}≤V_{BR(DSS)}, Starting T_J=25°C

600V/2.0A POWER MOSFET (N-Channel)

MSU2N60

Electrical Characteristics ($T_C=25^\circ\text{C}$ unless otherwise specified)

| Symbol | Description | Min. | Typ. | Max. | Unit | Conditions | |
|---|---|---------|------|------|----------|--|--------------------------|
| OFF CHARACTERISTICS | | | | | | | |
| $V_{(BR)DSS}$ | Drain-Source Breakdown Voltage | 600 | - | - | V | $V_{GS}=0V, I_D=250\mu A$ | |
| $\Delta V_{(BR)DSS} / \Delta T_J$ | Breakdown Voltage Temperature Coefficient | - | 0.4 | - | V/°C | $I_D=250\mu A$, referenced to 25°C | |
| I_{DSS} | Drain-Source leakage Current | - | - | 10 | μA | $V_{DS}=600V, V_{GS}=0V$ | |
| I_{GSS} | Gate-Source leakage Current | Forward | - | - | 100 | nA | $V_{GS}=30V, V_{DS}=0V$ |
| | | Reverse | - | - | -100 | nA | $V_{GS}=-30V, V_{DS}=0V$ |
| ON CHARACTERISTICS | | | | | | | |
| $V_{GS(th)}$ | Gate-Source Threshold Voltage | 2.0 | - | 4.0 | V | $V_{DS}=V_{GS}, I_D=250\mu A$ | |
| $R_{DS(on)}$ | Static Drain-Source On-State Resistance | - | 3.8 | 5.0 | Ω | $V_{GS}=10.0V, I_D=1.0A$ | |
| DYNAMIC CHARACTERISTICS | | | | | | | |
| C_{iss} | Input Capacitance | - | 270 | 350 | pF | $V_{DS}=25V, V_{GS}=0V, f=1.0MHz$ | |
| C_{oss} | Output Capacitance | - | 40 | 50 | pF | | |
| C_{rss} | Reverse Transfer Capacitance | - | 5 | 7 | pF | | |
| SWITCHING CHARACTERISTICS | | | | | | | |
| $t_d(on)$ | Turn-on Delay Time | - | 10 | 30 | nS | $V_{DD}=300V, I_D=2.4A, R_G=25\Omega$ (Note 4, 5) | |
| t_r | Turn-on Rise Time | - | 25 | 60 | nS | | |
| $t_d(off)$ | Turn-off Delay Time | - | 20 | 50 | nS | | |
| t_f | Turn-off Fall Time | - | 25 | 60 | nS | | |
| Q_g | Total Gate Charge | - | 9.0 | 11 | nC | $V_{DS}=480V, I_D=2.4A, V_{GS}=10V$ (Note 4, 5) | |
| Q_{gs} | Gate-Source Charge | - | 1.6 | - | nC | | |
| Q_{gd} | Gate-Drain Charge | - | 4.3 | - | nC | | |
| DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS | | | | | | | |
| V_{SD} | Drain-Source Diode Forward Voltage | - | - | 1.4 | V | $V_{GS}=0V, I_S=2.0A$ | |
| I_S | Maximum Continuous Drain-Source Diode Forward Current | - | - | 2.0 | A | - | |
| I_{SM} | Maximum Pulse Drain-Source Diode Forward Current | - | - | 8.0 | A | - | |
| t_{rr} | Reverse Recovery Time | - | 180 | - | nS | $V_{GS}=0V, I_S=2.4A, di_F/dt=100A/\mu s$ (Note4) | |
| Q_{rr} | Reverse Recovery Charge | - | 0.72 | - | μC | | |

Note 4: Pulse test: Pulse width $\leq 300\mu s$, Duty cycle $\leq 2\%$

5: Essentially independent of operating temperature

600V/2.0A POWER MOSFET (N-Channel)

MSU2N60

Typical Characteristics Curves

Fig.1- On-Region Characteristics

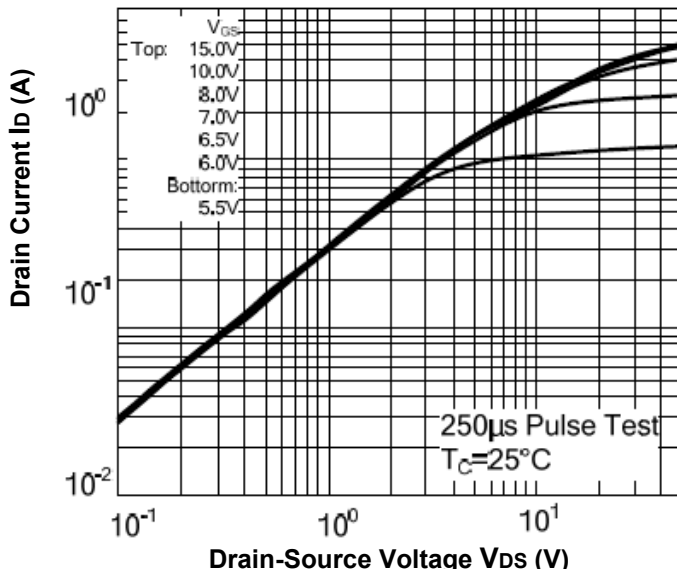


Fig.2- Transfer Characteristics

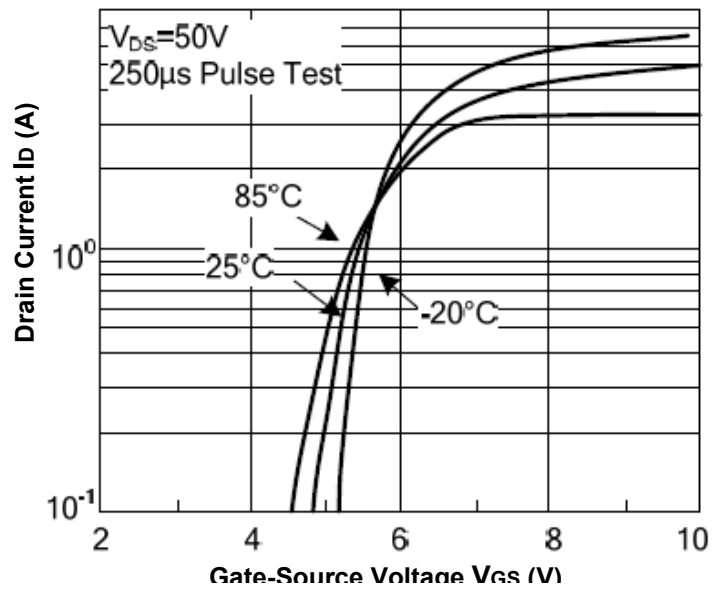


Fig.3- On-Resistance Variation Vs. Drain Current and Gate Voltage

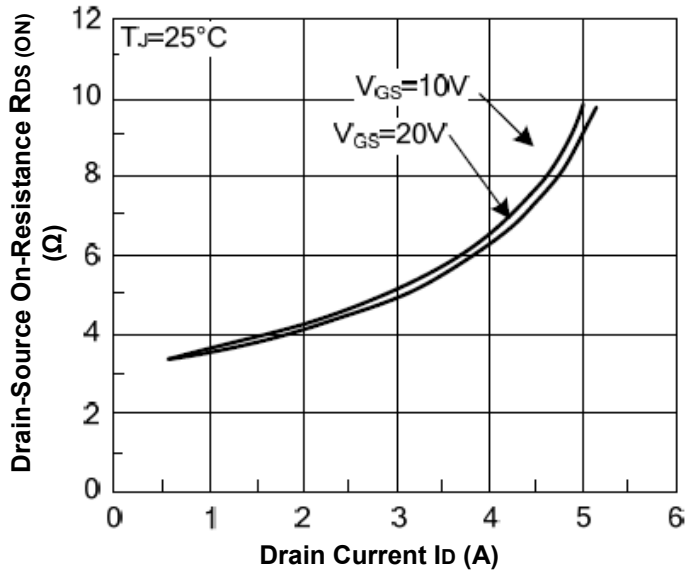
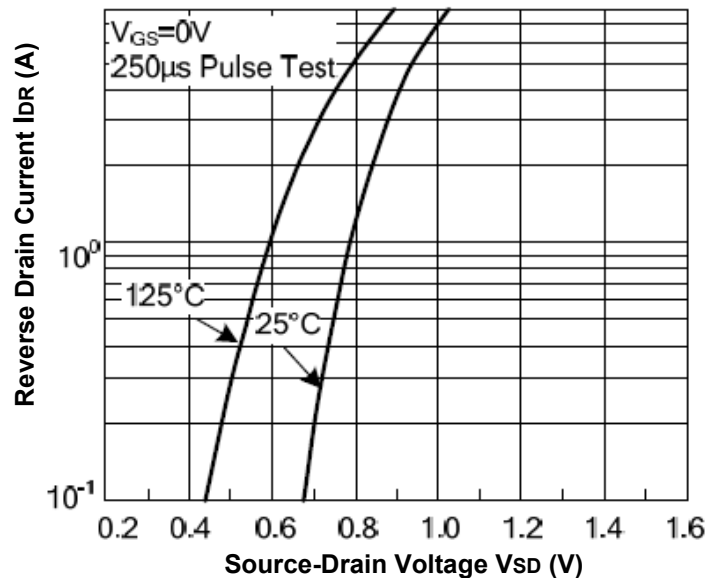


Fig.4- Body Diode Forward Voltage Variation Vs. Source Current and Temperature



600V/2.0A POWER MOSFET (N-Channel)

MSU2N60

Fig.5- Capacitance Characteristics

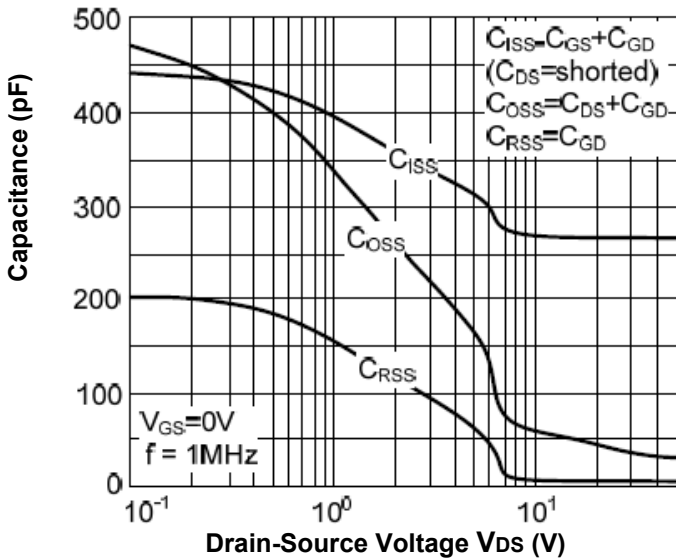


Fig.6- Gate Charge Characteristics

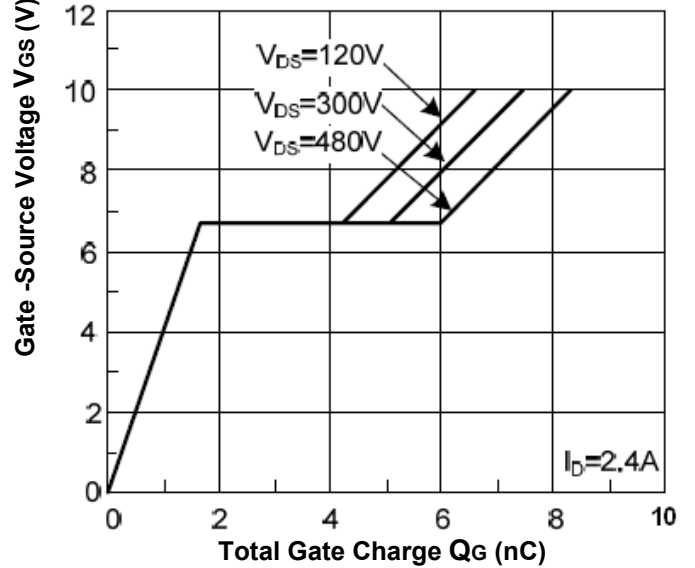


Fig.7- Breakdown Voltage Variation Vs. Temperature

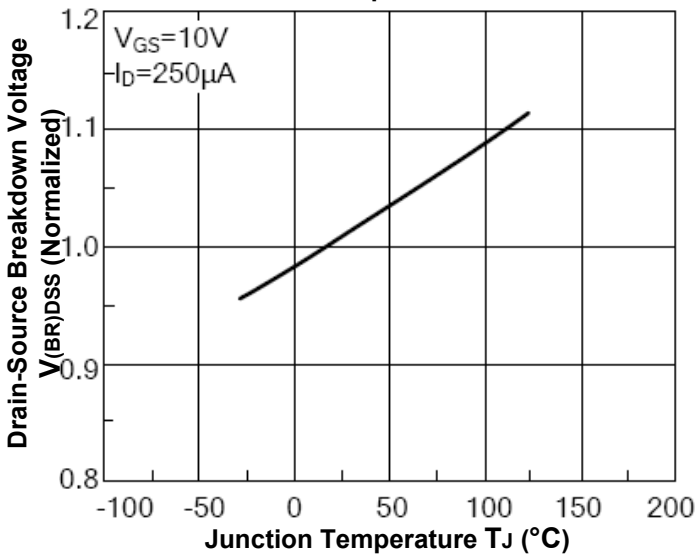
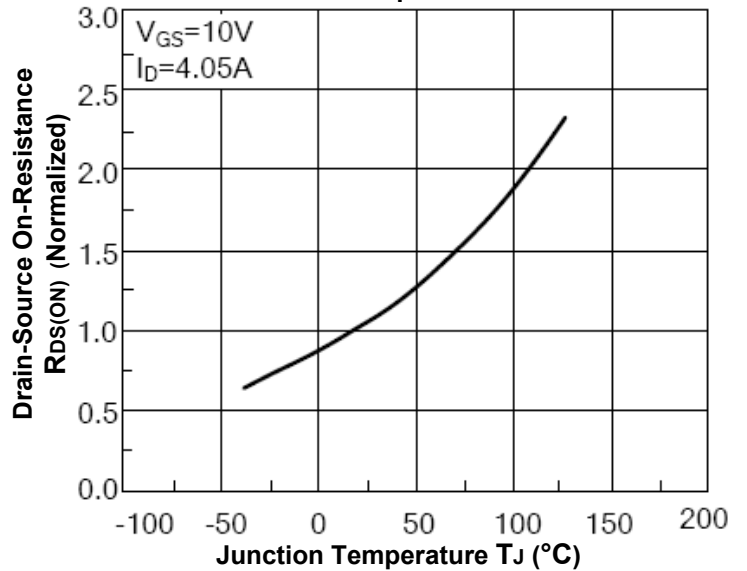


Fig.8- On Resistance Variation Vs. Temperature



600V/2.0A POWER MOSFET (N-Channel)

MSU2N60

Fig.9- Max. Safe Operation Area Vs. Temperature

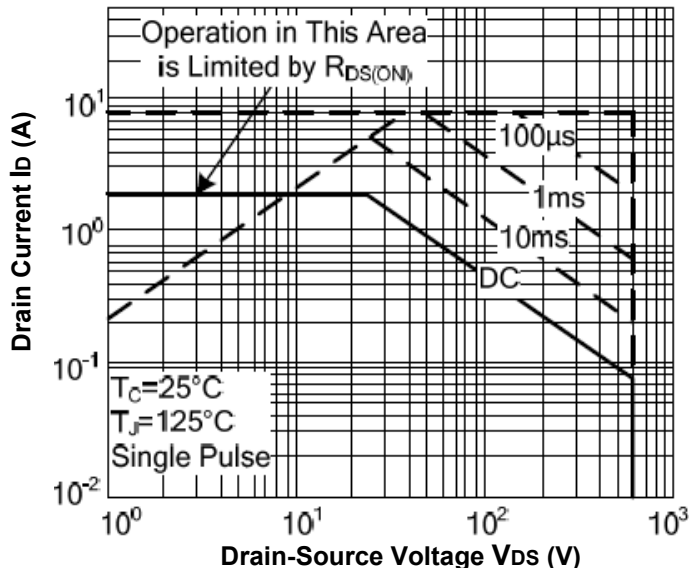


Fig.10- Max. Drain Current Vs. Case Temperature

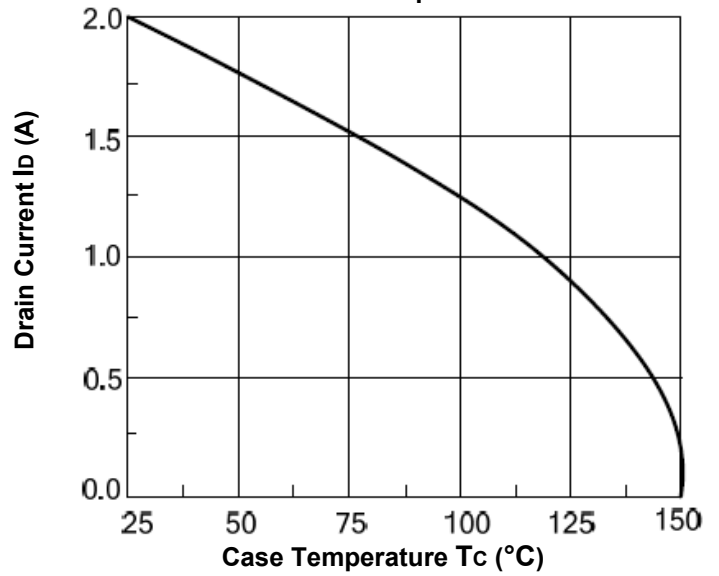
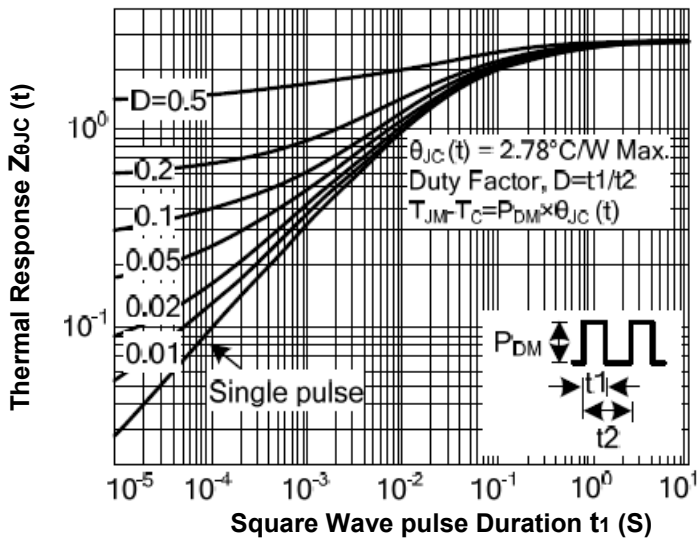


Fig.11- Transient Thermal Response



TEST CIRCUIT AND WAVEFORMS

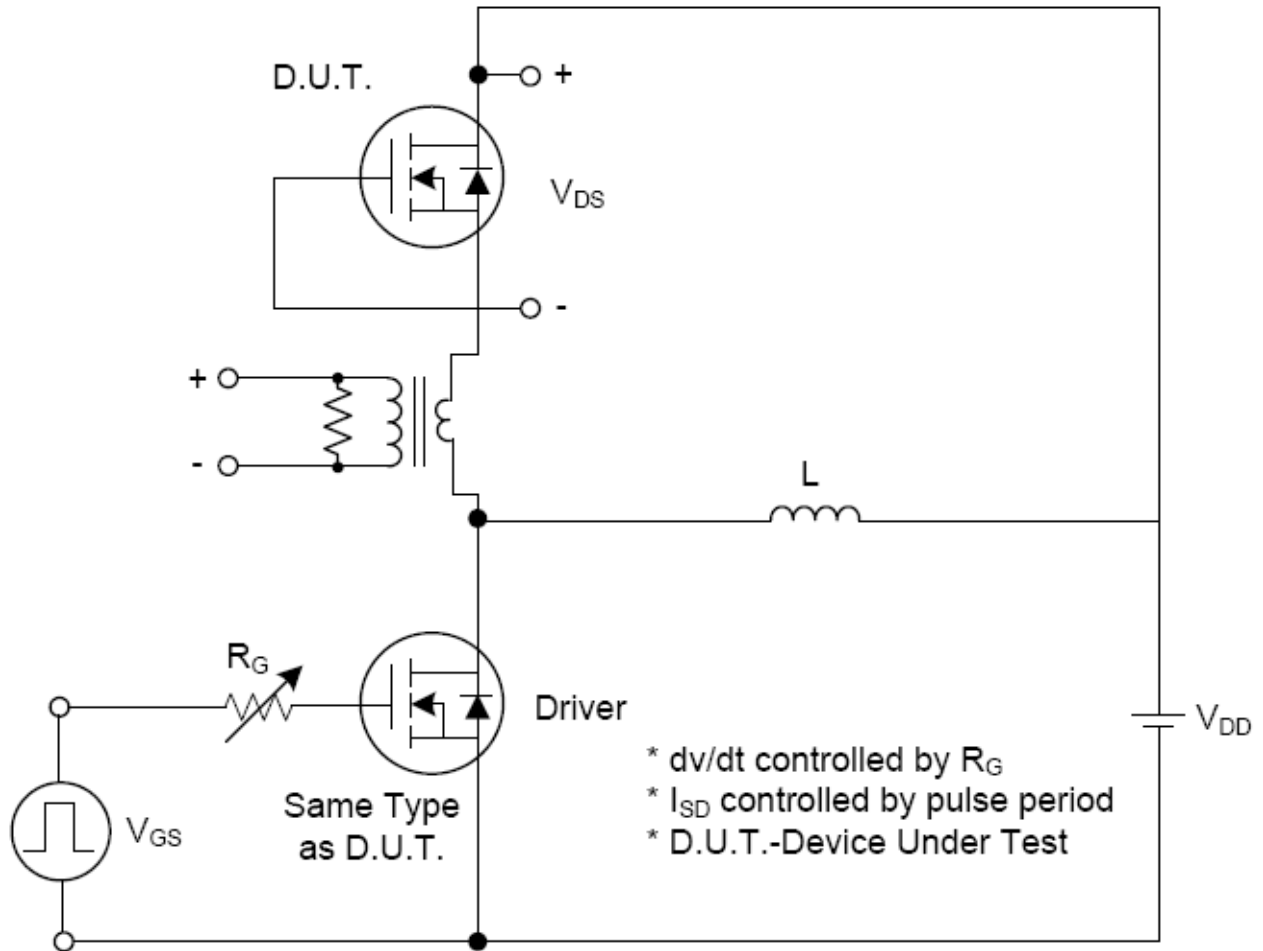


Fig.12- Peak Diode Recovery dv/dt Test Circuit

600V/2.0A POWER MOSFET (N-Channel)

MSU2N60

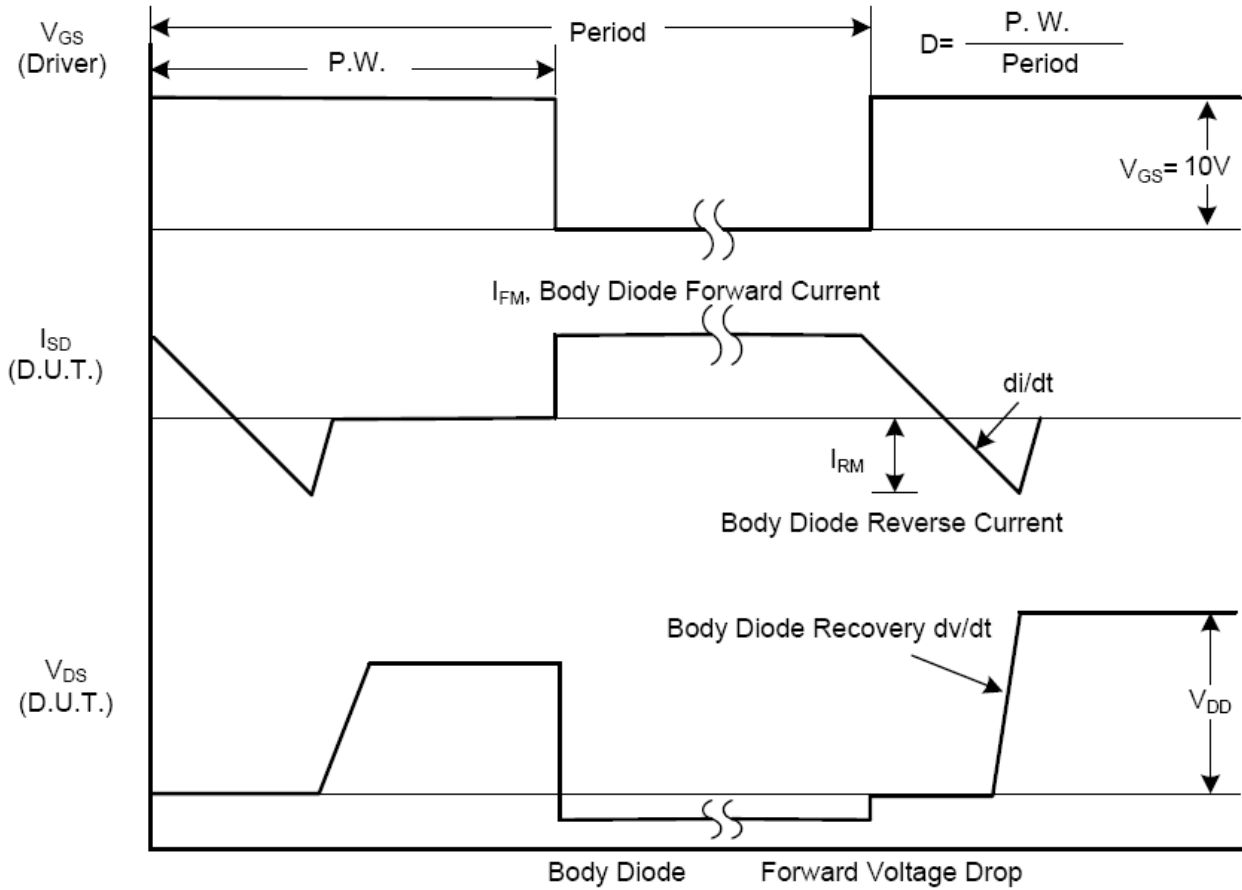


Fig.13- Peak diode Recovery dv/dt Waveform

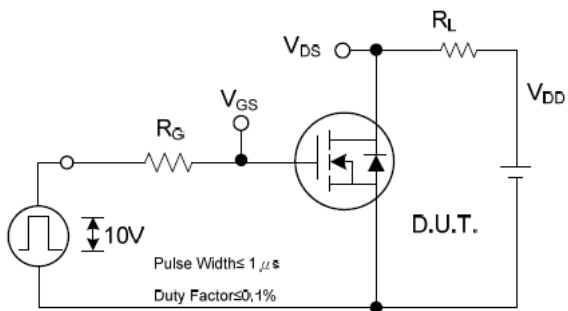


Fig.14- Switching Test Circuit

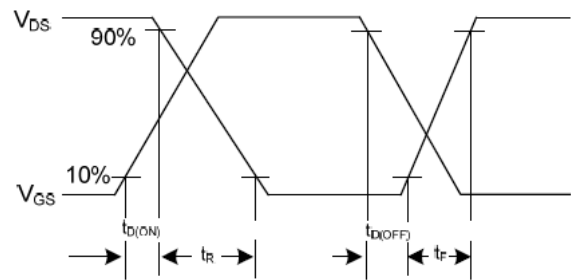


Fig.15- Switching Waveform

600V/2.0A POWER MOSFET (N-Channel)

MSU2N60

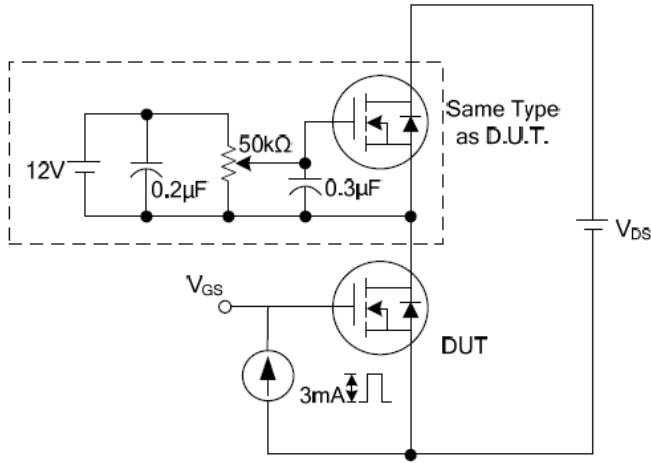


Fig.16- Gate Charge Test Circuit

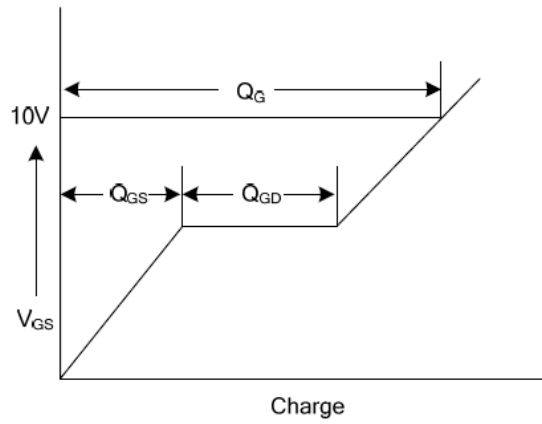


Fig.17- Gate Charge Waveform

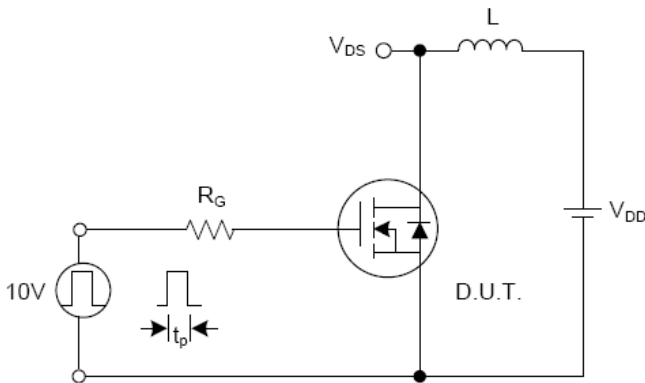


Fig.18- Unclamped Inductive Switching Test Circuit

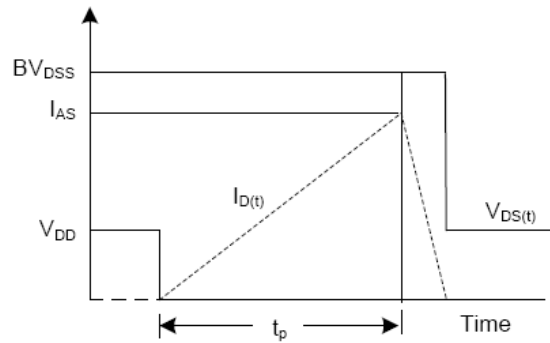
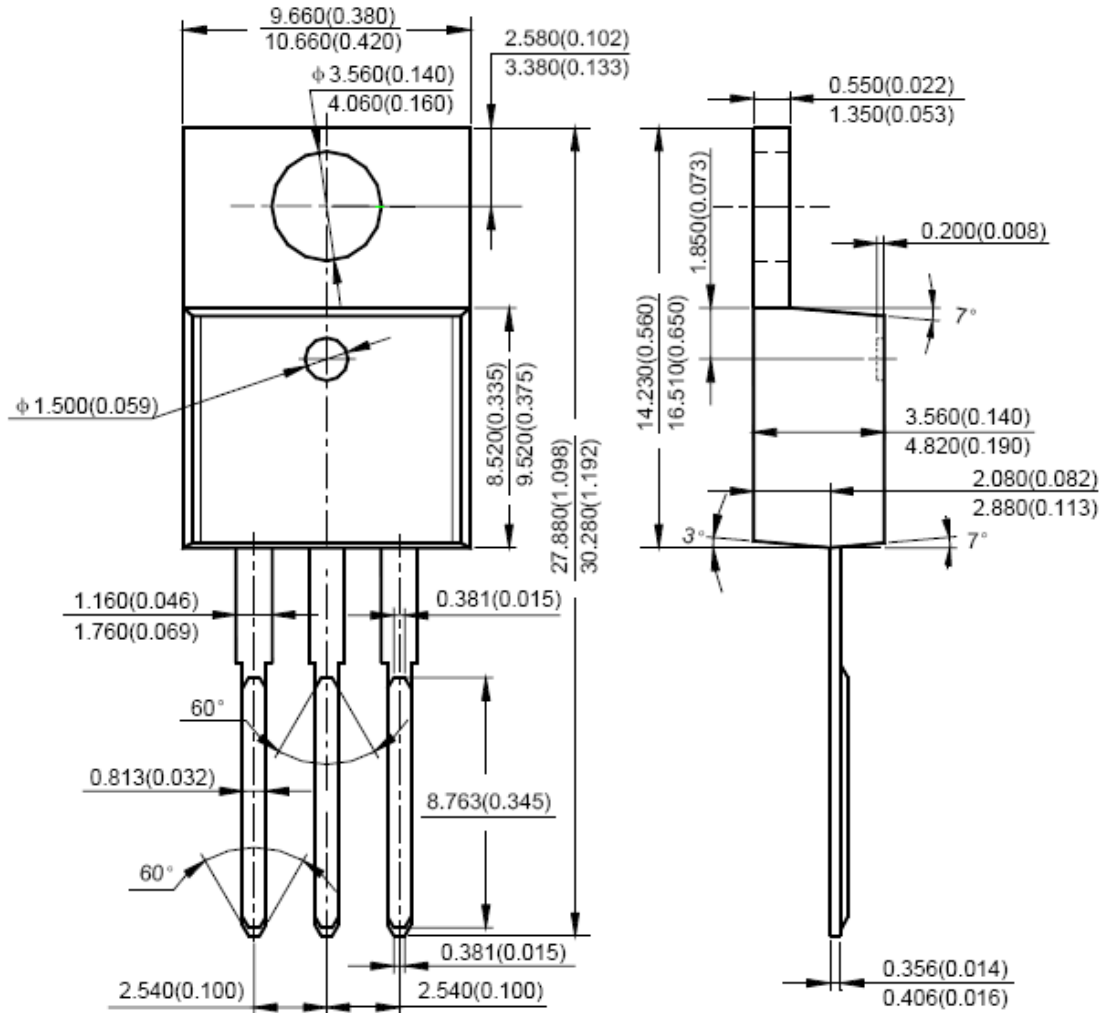


Fig.19- Unclamped Inductive Switching Waveform

600V/2.0A POWER MOSFET (N-Channel)

MSU2N60

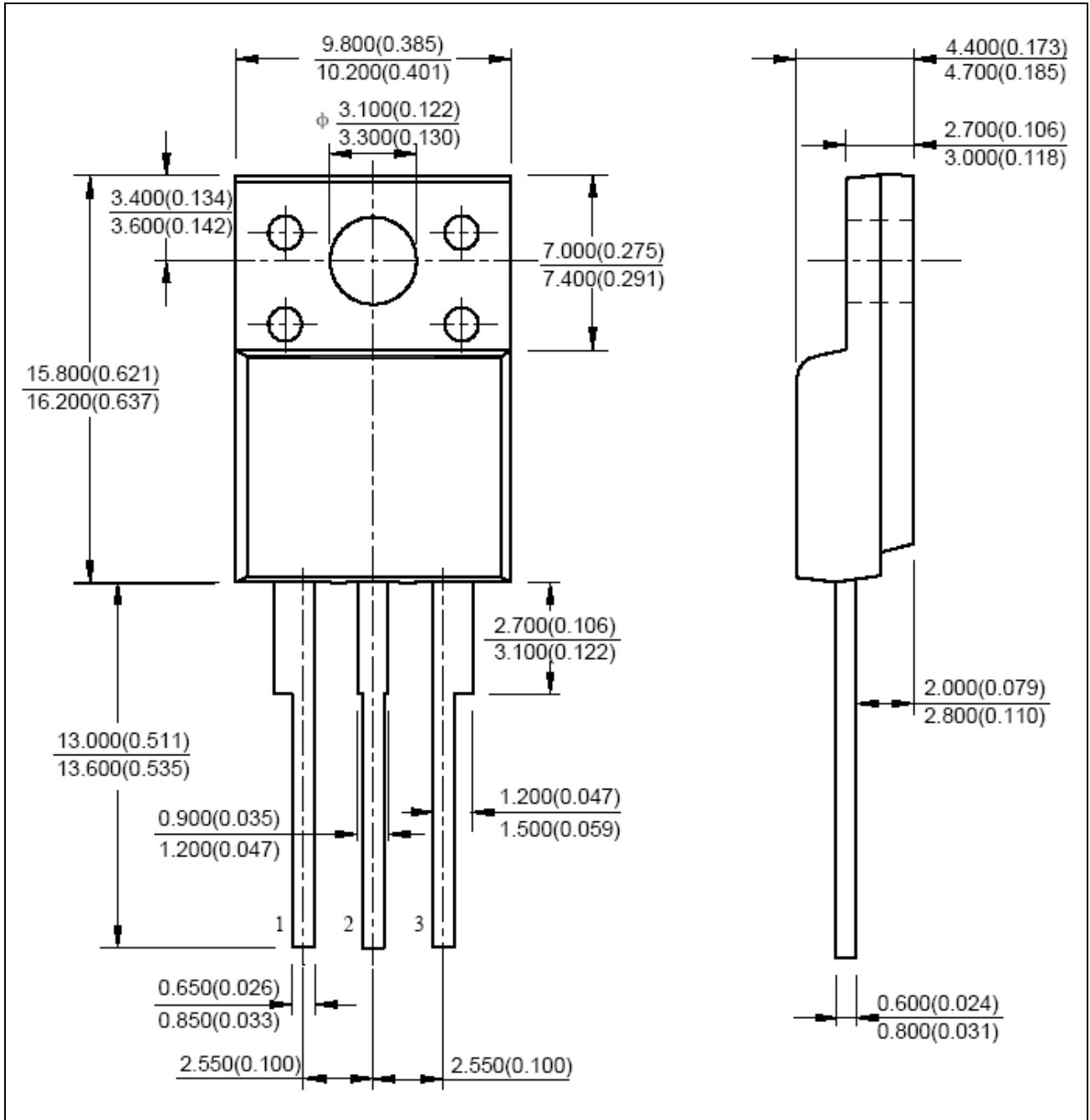
Dimensions in mm (inch)



TO-220

600V/2.0A POWER MOSFET (N-Channel)

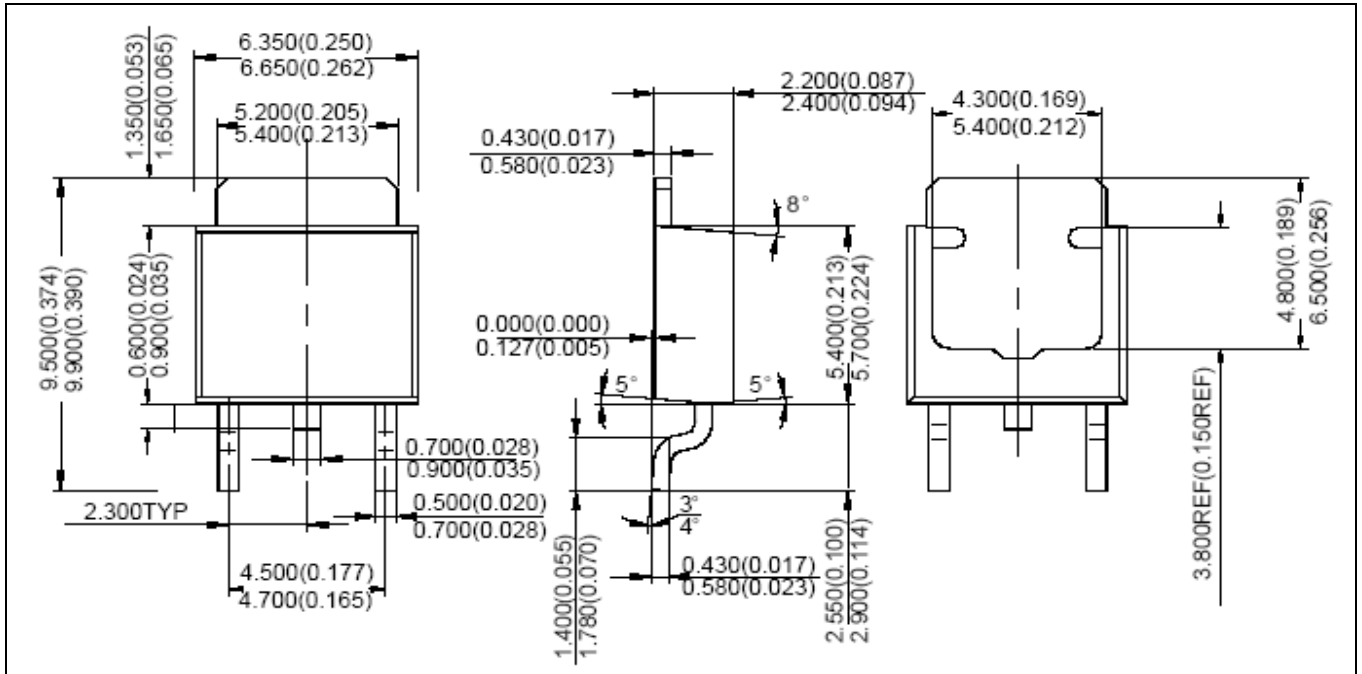
MSU2N60



TO-220F

600V/2.0A POWER MOSFET (N-Channel)

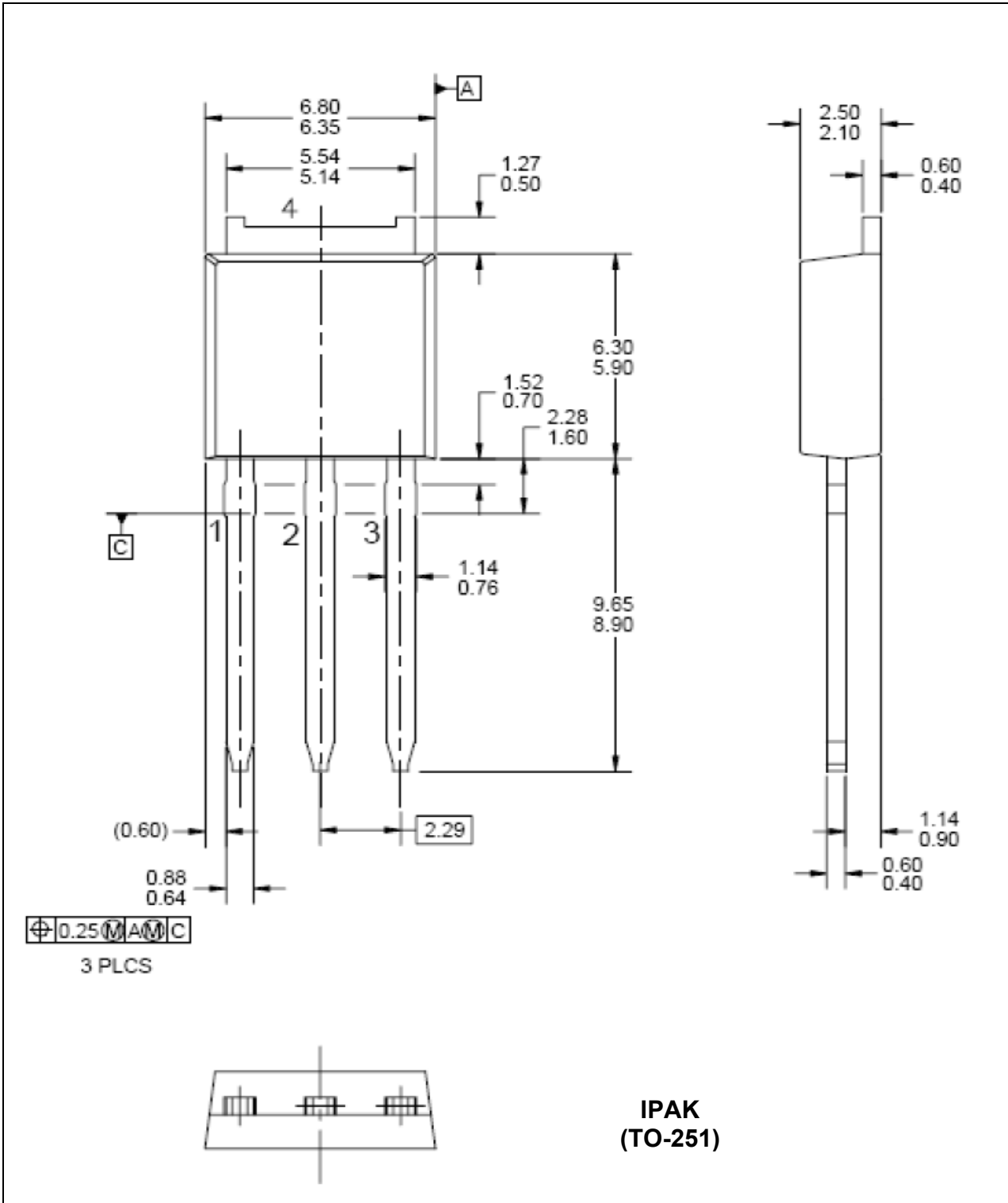
MSU2N60



DPAK
(TO-252)

600V/2.0A POWER MOSFET (N-Channel)

MSU2N60



600V/2.0A POWER MOSFET (N-Channel)

MSU2N60

How to contact us:

US HEADQUARTERS

28040 WEST HARRISON PARKWAY, VALENCIA, CA 91355-4162

Tel: (800) TAITRON (800) 824-8766 (661) 257-6060

Fax: (800) TAITFAX (800) 824-8329 (661) 257-6415

Email: taitron@taitroncomponents.com

Http://www.taitroncomponents.com

TAITRON COMPONENTS MEXICO, S.A .DE C.V.

BOULEVARD CENTRAL 5000 INTERIOR 5 PARQUE INDUSTRIAL ATITALAQUIA, HIDALGO C.P.
42970 MEXICO

Tel: +52-55-5560-1519

Fax: +52-55-5560-2190

TAITRON COMPONENTS INCORPORATED REPRESENTAÇÕES DO BRASIL LTDA

RUA DOMINGOS DE MORAIS, 2777, 2.ANDAR, SALA 24 SAÚDE - SÃO PAULO-SP 04035-001 BRAZIL

Tel: +55-11-5574-7949

Fax: +55-11-5572-0052

TAITRON COMPONENTS INCORPORATED, SHANGHAI REPRESENTATIVE OFFICE

METROBANK PLAZA, 1160 WEST YAN' AN ROAD, SUITE 1503, SHANGHAI, 200052, CHINA

Tel: +86-21-5424-9942

Fax: +86-21-5424-9931