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April 1<sup>st</sup>, 2010 Renesas Electronics Corporation

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# RJJ1011DPD

# P Channel Power MOS FET High Speed Switching

REJ03G1623-0300 Rev.3.00 Dec 19, 2008

### **Features**

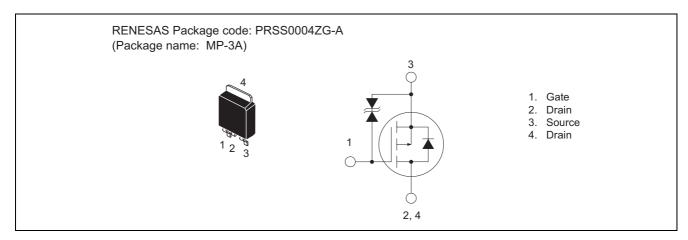
• V<sub>DSS</sub>: −100 V

•  $R_{DS(on)}$ : 0.30  $\Omega$  (Max)

•  $I_D$ : -6 A

• Surface mount package (MP-3A)

### **Outline**



### **Application**

• Motor control, Solenoid control, DC-DC converter, etc.

### **Absolute Maximum Ratings**

 $(Tc = 25^{\circ}C)$ 

Item	Symbol	Ratings	Unit	Conditions
Drain to source voltage	V <sub>DSS</sub>	-100	V	V <sub>GS</sub> = 0 V
Gate to source voltage	V <sub>GSS</sub>	±20	V	V <sub>DS</sub> = 0 V
Drain current (DC)	I <sub>D</sub>	-6	Α	
Drain current (Pulsed)*1	I <sub>D(pulse)</sub>	-12	Α	
Avalanche current	I <sub>AP</sub>	-6	Α	L = 100 μH
Channel dissipation	P <sub>ch</sub>	30	W	
Channel to case thermal impedance	θch-c	4.17	°C/W	
Channel temperature	Tch	-55 to +150	°C	
Storage temperature	Tstg	−55 to +150	°C	

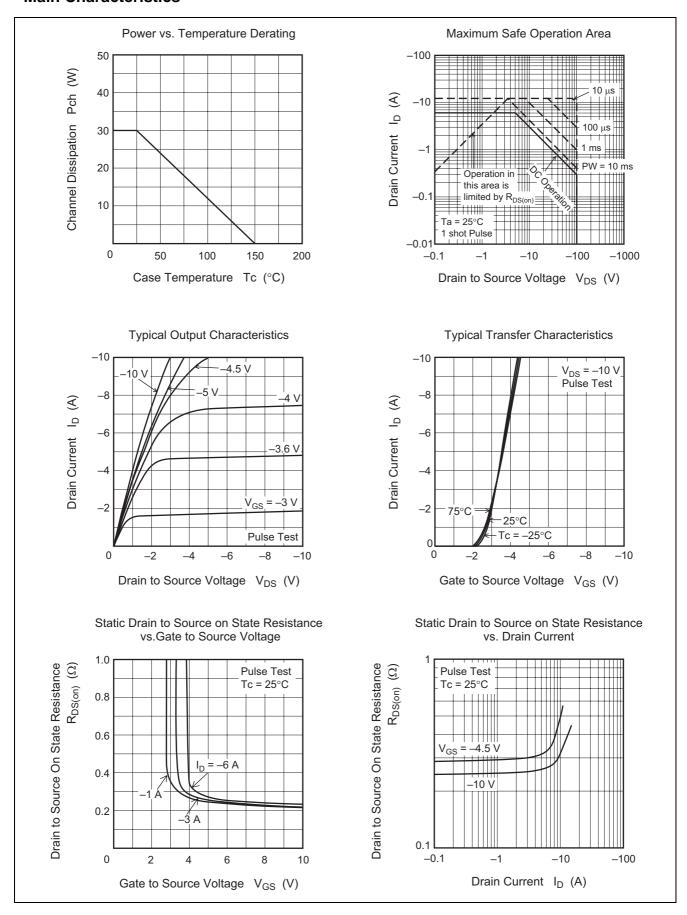
Note: 1. Pulse width limited by safe operating area.

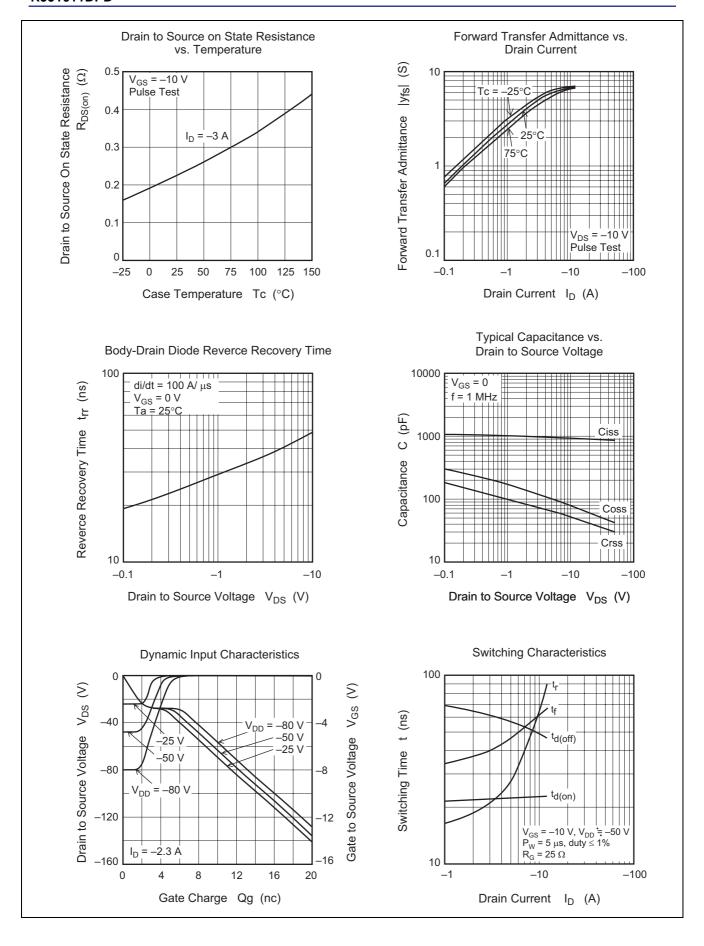
### **Electrical Characteristics**

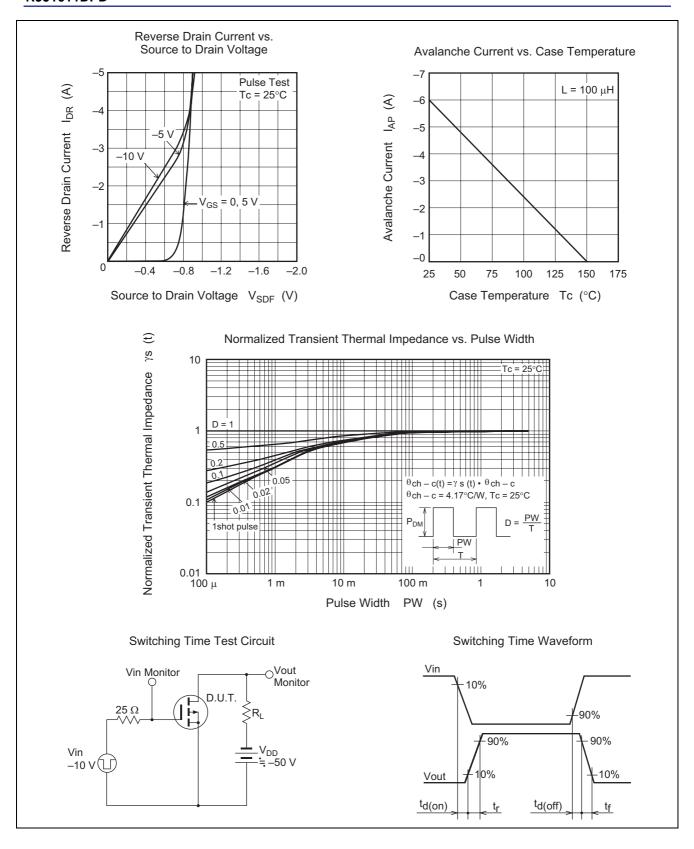
 $(Tc = 25^{\circ}C)$ 

Item	Symbol	Min.	Тур.	Max.	Unit	Conditions
Drain to source breakdown voltage	V <sub>(BR)DSS</sub>	-100	_	_	V	$I_D = -1 \text{ mA}, V_{GS} = 0 \text{ V}$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±20	_	_	V	$I_G = \pm 100 \ \mu A, \ V_{DS} = 0 \ V$
Drain to source leakage current	I <sub>DSS</sub>	_	_	-1	mA	$V_{DS} = -100 \text{ V}, V_{GS} = 0 \text{ V}$
Gate to source leak current	I <sub>GSS</sub>	_	_	±10	μΑ	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$
Gate-source cutoff voltage	V <sub>GS(off)</sub>	-1.0	-1.9	-2.5	V	$I_D = -1 \text{ mA}, V_{DS} = -10 \text{ V}$
Static drain to source on state	R <sub>DS(on)</sub>	_	0.26	0.30	Ω	$I_D = -3 \text{ A}, V_{GS} = -10 \text{ V}$
resistance		_	0.30	0.50	Ω	$I_D = -3 \text{ A}, V_{GS} = -4.5 \text{ V}$
Input capacitance	Ciss	_	930	_	pF	$V_{DS} = -10 \text{ V}$
Output capacitance	Coss	_	80	_	pF	$V_{GS} = 0 V$
Reverse transfer capacitance	Crss	_	50	_	pF	f = 1 MHz
Turn-on delay time	t <sub>d(on)</sub>	_	10	_	ns	$V_{DD} = -50 \text{ V}$
Rise time	t <sub>r</sub>	_	15	_	ns	$I_D = -3 A$
Turn-off delay time	t <sub>d(off)</sub>	_	65	_	ns	$V_{GS} = -10 \text{ V}$
Fall time	t <sub>f</sub>	_	35	_	ns	$R_G = 25 \Omega$
Source-drain voltage	$V_{SD}$	_	-0.85	-1.2	V	$I_S = -3 \text{ A}, V_{GS} = 0 \text{ V}$

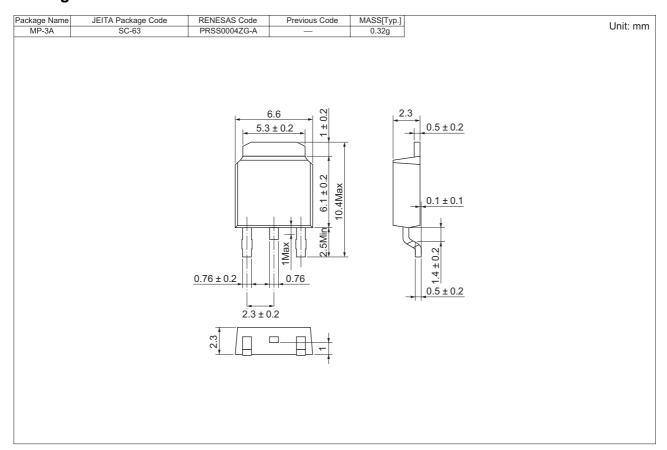
### **Main Characteristics**







### **Package Dimensions**



### **Ordering Information**

Part No.	Quantity	Shipping Container
RJJ1011DPD-00-J2	3000 pcs	Taping

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