



IXTP7N45, IXTP7N50, IXTM7N45, IXTM7N50

7 AMPS, 450-500 V, 0.85Ω/1.10Ω

T-39-13

MAXIMUM RATINGS

Parameter	Sym.	IXTP7N45 IXTM7N45	IXTP7N50 IXTM7N50	Unit
Drain-Source Voltage (1)	V_{DSS}	450	500	V_{dc}
Drain-Gate Voltage ($R_{GS}=1.0\text{ M}\Omega$) (1)	V_{DGR}	450	500	V_{dc}
Gate-Source Voltage Continuous	V_{GS}		± 20	V_{dc}
Gate-Source Voltage Transient	V_{GSM}		± 30	V
Drain Current Continuous ($T_C=25^\circ\text{C}$)	I_D		7	A_{dc}
Drain Current Pulsed (3)	I_{DM}		28	A
Total Power Dissipation	P_D		125	W
Power Dissipation Derating $>25^\circ\text{C}$			1.0	W/ $^\circ\text{C}$
Operating and Storage Temperature	T_J & T_{stg}		-65 to +150	$^\circ\text{C}$
Max. Lead Temp. for Soldering	T_L		300 (1.6mm from case for 10 sec.)	$^\circ\text{C}$


ELECTRICAL CHARACTERISTICS $T_C=25^\circ\text{C}$ unless otherwise specified

Parameter	Type	Min.	Typ.	Max.	Units	Test Conditions
BV_{DSS} Drain-Source Breakdown Voltage	7N45, 45A	450	-	-	V	$V_{GS}=0V$
	7N50, 50A	500	-	-	V	$I_D=250\mu A$
$V_{GS(th)}$ Gate Threshold Voltage	ALL	2.0	-	4.0	V	$V_{DS}=V_{GS}$, $I_D=250\mu A$
I_{GSS} Gate-Source Leakage Forward	ALL	-	-	100	nA	$V_{GS}=20V$
I_{GSS} Gate-Source Leakage Reverse	ALL	-	-	100	nA	$V_{GS}=-20V$
I_{DSS} Zero Gate Voltage Drain Current	ALL	-	-	200	μA	$V_{DS}=\text{Max. Rating}\times 0.8$, $V_{GS}=0V$
		-	-	1000	μA	$V_{DS}=\text{Max. Rating}\times 0.8$, $V_{GS}=0V$, $T_C=125^\circ\text{C}$
$R_{DS(on)}$ Static Drain-Source On-State Resistance (2)	7N45A, 50A	-	-	0.85	Ω	$V_{GS}=10V$, $I_D=3.5A$
	7N45, 50	-	-	1.10	Ω	
G_{fs} Forward Transconductance (2)	ALL	4.5	6.0	-	S	$V_{DS}\geq 15V$, $I_D=3.5A$
C_{iss} Input Capacitance	ALL	-	1570	-	pF	$V_{GS}=0V$, $V_{DS}=25V$, $f=1.0\text{ MHz}$
C_{oss} Output Capacitance	ALL	-	155	-	pF	
C_{rss} Reverse Transfer Capacitance	ALL	-	60	-	pF	
$t_{d(on)}$ Turn-On Delay Time	ALL	-	15	21	ns	$V_{DS}=0.5 BV_{DSS}$, $I_D=3.5A$, $Z_\theta=10\Omega$ (MOSFET switching times are essentially independent of operating temperature. See Fig. 3, page 22 for test circuit.)
t_r Rise Time	ALL	-	20	30	ns	
$t_{d(off)}$ Turn-Off Delay Time	ALL	-	50	74	ns	
t_f Fall Time	ALL	-	20	30	ns	
Q_g Total Gate Charge	ALL	-	-	80	nC	
Q_{gs} Gate-Source Charge	ALL	-	-	25	nC	$V_{GS}=10V$, $I_D=7.0A$, $V_{DS}=0.8\text{ Max. Rating}$. (Gate charge is essentially independent of operating temperature. See Fig. 4, page 22 for test circuit.)
Q_{gd} Gate-Drain ("Miller") Charge	ALL	-	-	30	nC	
W_{DSR} Unclamped Drain-to-Source Avalanche Energy	7N45R, 45AR 7N50R, 50AR	500	-	-	mJ	See Fig. 5, page 22 for test circuit.

THERMAL RESISTANCE

Parameter	Type	Min.	Typ.	Max.	Units	Test Conditions
R_{thJC} Junction-to-Case	ALL	-	-	1.0	$^\circ\text{C}/\text{W}$	
R_{thJA} Junction-to-Ambient TO-204	IXTM	-	-	30.0	$^\circ\text{C}/\text{W}$	Free Air Operation
R_{thJA} Junction-to-Ambient TO-220	IXTP	-	-	80.0	$^\circ\text{C}/\text{W}$	Free Air Operation

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS

Parameter	Type	Min.	Typ.	Max.	Units	Test Conditions
I_S Continuous Source Current (Body Diode)	ALL	-	-	7.0	A	Modified MOSFET symbol showing the integral reverse P-N junction rectifier. 
I_{SM} Pulse Source Current (Body Diode) (1)	ALL	-	-	28.0	A	
V_{SD} Diode Forward Voltage (2)	ALL	-	-	1.5	V	$T_C=25^\circ\text{C}$, $I_f=7.0A$, $V_{GS}=0V$
t_{rr} Reverse Recovery Time	ALL	-	400	-	ns	$I_f=7.0A$, $di/dt=100A/\mu s$

(1) $T_J=25^\circ\text{C}$ to 150°C (2) Pulse test: Pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$

(3) Repetitive rating: Pulse width limited by max. junction temperature.