



IRF830B/IRFS830B

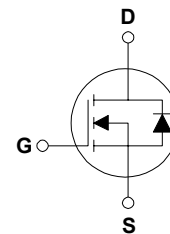
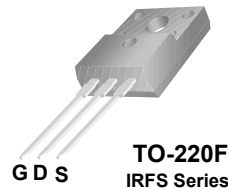
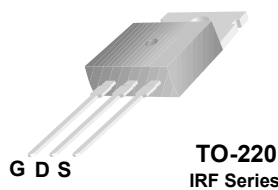
500V N-Channel MOSFET

General Description

These N-Channel enhancement mode power field effect transistors are produced using Mos-Tech's proprietary, planar, DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency switch mode power supplies, power factor correction and electronic lamp ballasts based on half bridge.

Features

- 4.5A, 500V, $R_{DS(on)} = 1.35\Omega @ V_{GS} = 10V$
- Low gate charge (typical 41nC)
- Low Crss (typical 56pF)
- Fast switching
- 100% avalanche tested
- Improved dv/dt capability



Absolute Maximum Ratings T_C = 25°C unless otherwise noted

Symbol	Parameter	IRF830B	IRFS830B	Units
V _{DSS}	Drain-Source Voltage	500		V
I _D	Drain Current - Continuous (T _C = 25°C) - Continuous (T _C = 100°C)	4.5	4.0	A
		2.9	2.3	A
I _{DM}	Drain Current - Pulsed (Note 1)	32	32	A
V _{GSS}	Gate-Source Voltage	± 30		V
E _{AS}	Single Pulsed Avalanche Energy (Note 2)	2.0		mJ
I _{AR}	Avalanche Current (Note 1)	1.0		A
E _{AR}	Repetitive Avalanche Energy (Note 1)	13.4		mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)	3.5		V/ns
P _D	Power Dissipation (T _C = 25°C)	1.0	1.0	W
	- Derate above 25°C	0.02	0.02	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range	-55 to +150		°C
T _L	Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	300		°C

* Drain current limited by maximum junction temperature.

Thermal Characteristics

Symbol	Parameter	IRF830B	IRFS830B	Units
R _{θJC}	Thermal Resistance, Junction-to-Case Max.	1.0	1.0	°C/W
R _{θCS}	Thermal Resistance, Case-to-Sink Typ.	0.5	--	°C/W
R _{θJA}	Thermal Resistance, Junction-to-Ambient Max.	62.5	62.5	°C/W

Electrical Characteristics

$T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
Off Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0\text{ V}, I_D = 250\ \mu\text{A}$	500	--	--	V
$\Delta BV_{DSS} / \Delta T_J$	Breakdown Voltage Temperature Coefficient	$I_D = 250\ \mu\text{A}$, Referenced to 25°C	--	0.55	--	$\text{V}/^\circ\text{C}$
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 500\text{ V}, V_{GS} = 0\text{ V}$	--	--	10	μA
		$V_{DS} = 400\text{ V}, T_C = 125^\circ\text{C}$	--	--	100	μA
I_{GSSF}	Gate-Body Leakage Current, Forward	$V_{GS} = 30\text{ V}, V_{DS} = 0\text{ V}$	--	--	100	nA
I_{GSSR}	Gate-Body Leakage Current, Reverse	$V_{GS} = -30\text{ V}, V_{DS} = 0\text{ V}$	--	--	-100	nA

On Characteristics

$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\ \mu\text{A}$	2.0	--	4.0	V
$R_{DS(on)}$	Static Drain-Source On-Resistance	$V_{GS} = 10\text{ V}, I_D = 10\text{ A}$	--	1.5	1.1	Ω
g_{FS}	Forward Transconductance	$V_{DS} = 40\text{ V}, I_D = 10\text{ A}$ (Note 4)	--	7.3	--	S

Dynamic Characteristics

C_{iss}	Input Capacitance	$V_{DS} = 25\text{ V}, V_{GS} = 0\text{ V},$ $f = 1.0\text{ MHz}$	--	10	10	pF
C_{oss}	Output Capacitance		--	10	190	pF
C_{rss}	Reverse Transfer Capacitance		--	10	10	pF

Switching Characteristics

$t_{d(on)}$	Turn-On Delay Time	$V_{DD} = 250\text{ V}, I_D = 1.0\text{ A},$ $R_G = 25\ \Omega$	--	5	5	ns	
t_r	Turn-On Rise Time		--	5	5	ns	
$t_{d(off)}$	Turn-Off Delay Time		(Note 4, 5)	--	5	5	ns
t_f	Turn-Off Fall Time		(Note 4, 5)	--	5	5	ns
Q_g	Total Gate Charge	$V_{DS} = 400\text{ V}, I_D = 1.0\text{ A},$ $V_{GS} = 10\text{ V}$	--	10	10	nC	
Q_{gs}	Gate-Source Charge		(Note 4, 5)	--	10	10	nC
Q_{gd}	Gate-Drain Charge		(Note 4, 5)	--	10	10	nC

Drain-Source Diode Characteristics and Maximum Ratings

I_S	Maximum Continuous Drain-Source Diode Forward Current	--	--	5	A	
I_{SM}	Maximum Pulsed Drain-Source Diode Forward Current	--	--	5	A	
V_{SD}	Drain-Source Diode Forward Voltage	$V_{GS} = 0\text{ V}, I_S = 1.0\text{ A}$	--	--	1.1	V
t_{rr}	Reverse Recovery Time	$V_{GS} = 0\text{ V}, I_S = 1.0\text{ A},$ $di_F / dt = 100\text{ A}/\mu\text{s}$ (Note 4)	--	10	10	ns
Q_{rr}	Reverse Recovery Charge		--	10	10	μC

Notes:

1. Repetitive Rating : Pulse width limited by maximum junction temperature
2. $L = 9.0\text{mH}, I_{AS} = 1.0\text{A}, V_{DD} = 50\text{V}, R_G = 25\ \Omega$, Starting $T_J = 25^\circ\text{C}$
3. $I_{SD} \leq 1.0\text{A}, di/dt \leq 200\text{A}/\mu\text{s}, V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^\circ\text{C}$
4. Pulse Test : Pulse width $\leq 300\ \mu\text{s}$, Duty cycle $\leq 2\%$
5. Essentially independent of operating temperature

Typical Characteristics

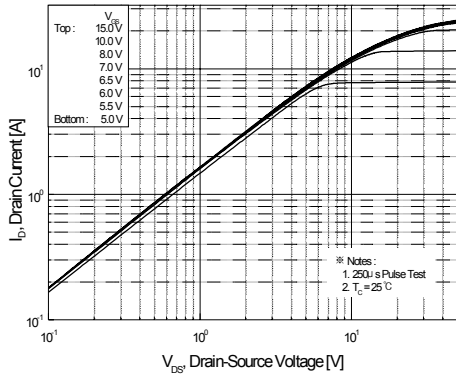


Figure 1. On-Region Characteristics

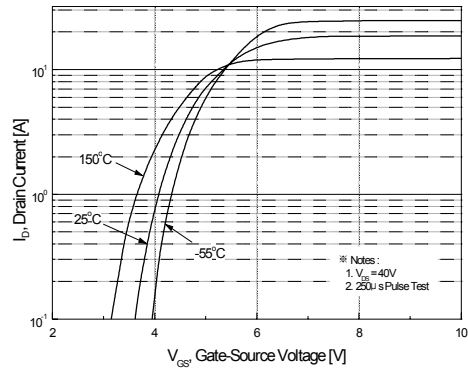


Figure 2. Transfer Characteristics

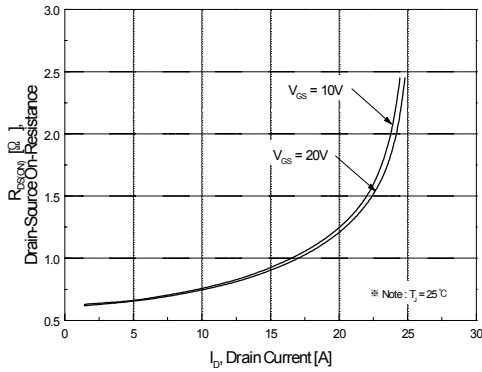


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

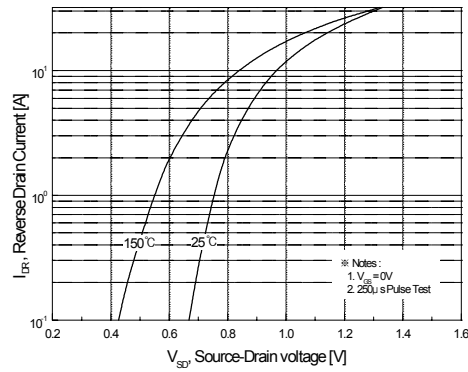


Figure 4. Body Diode Forward Voltage Variation with Source Current and Temperature

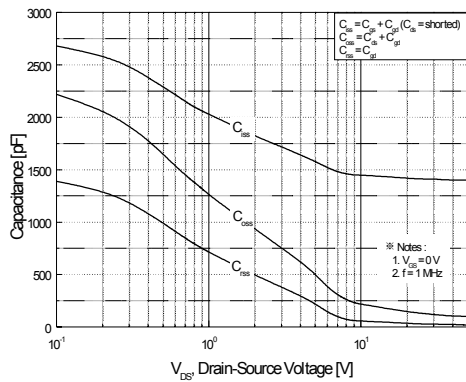


Figure 5. Capacitance Characteristics

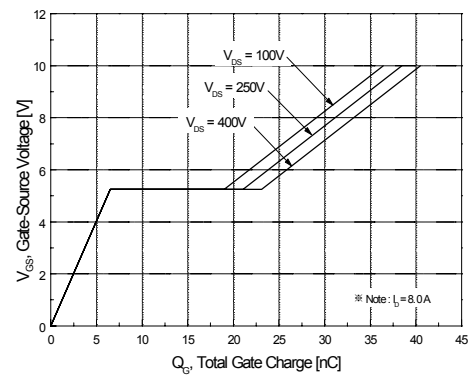


Figure 6. Gate Charge Characteristics

Typical Characteristics (Continued)

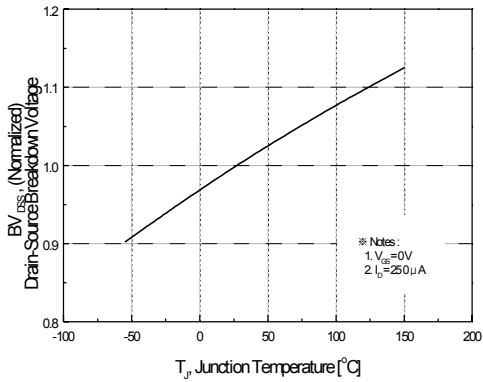


Figure 7. Breakdown Voltage Variation vs Temperature

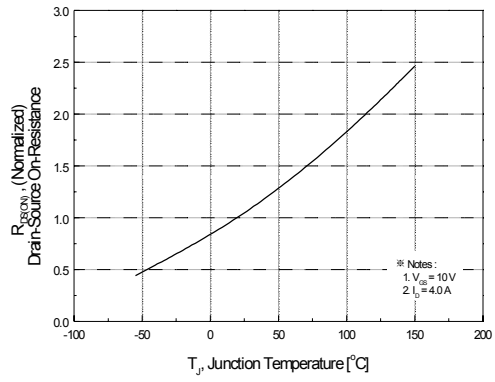


Figure 8. On-Resistance Variation vs Temperature

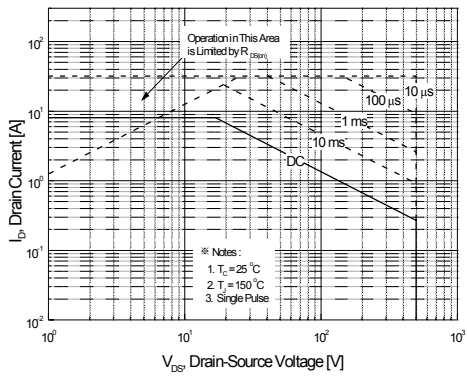


Figure 9-1. Maximum Safe Operating Area for IRF830B

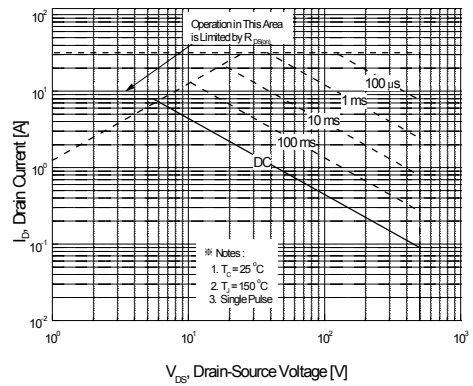


Figure 9-2. Maximum Safe Operating Area for IRFS830B

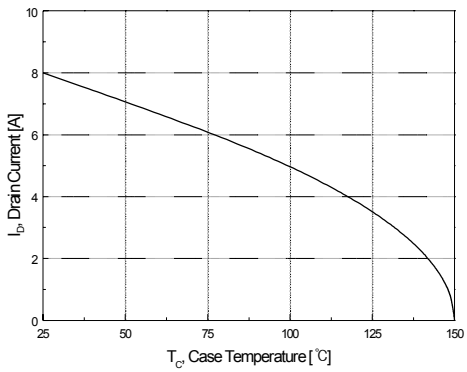


Figure 10. Maximum Drain Current vs Case Temperature

Typical Characteristics (Continued)

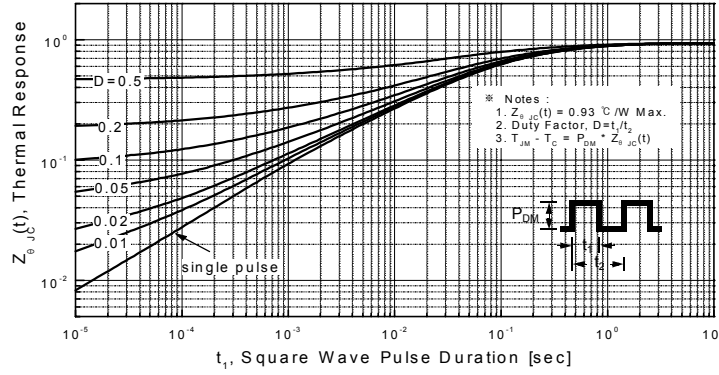


Figure 11-1. Transient Thermal Response Curve for IRF830B

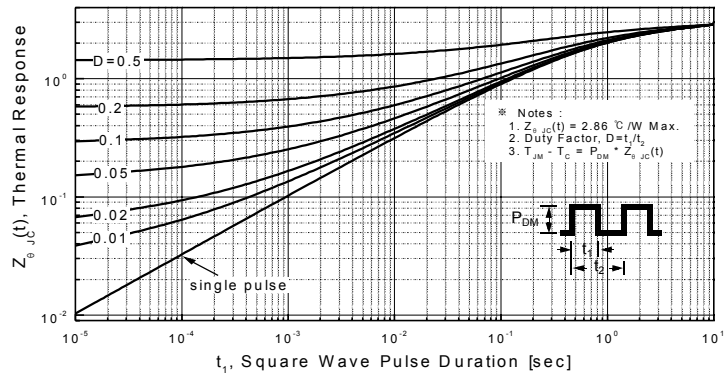
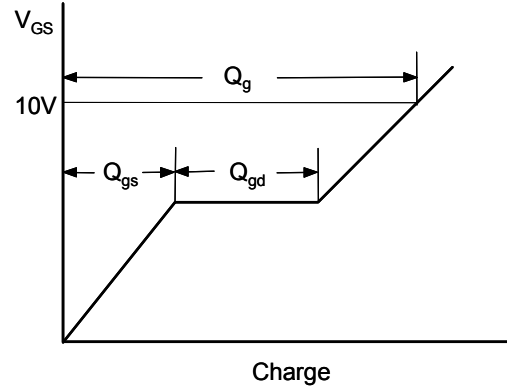
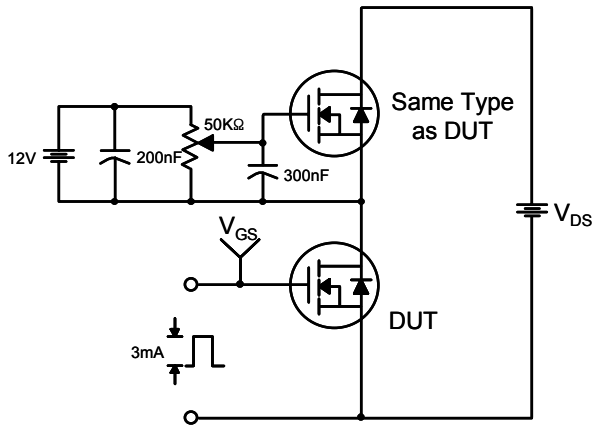
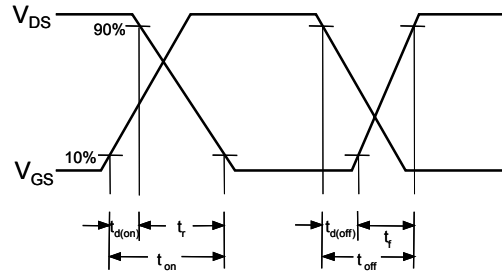
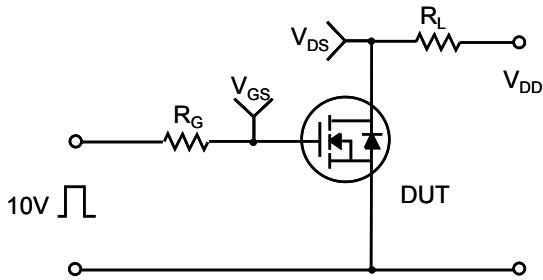


Figure 11-2. Transient Thermal Response Curve for IRFS830B

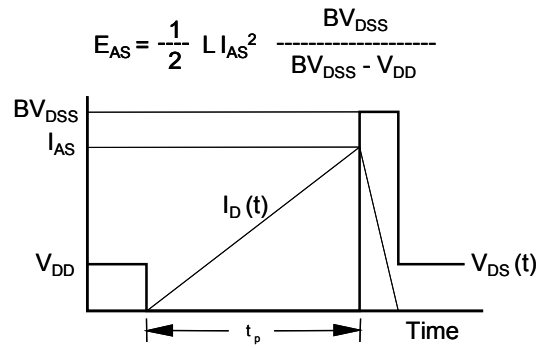
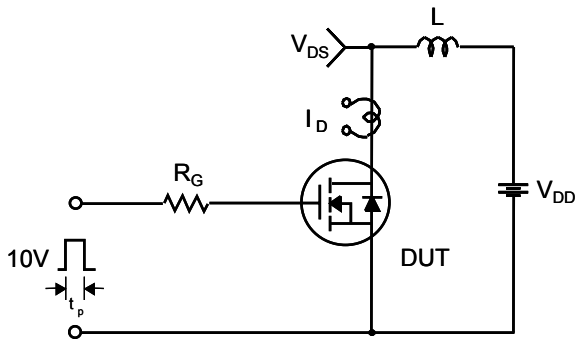
Gate Charge Test Circuit & Waveform

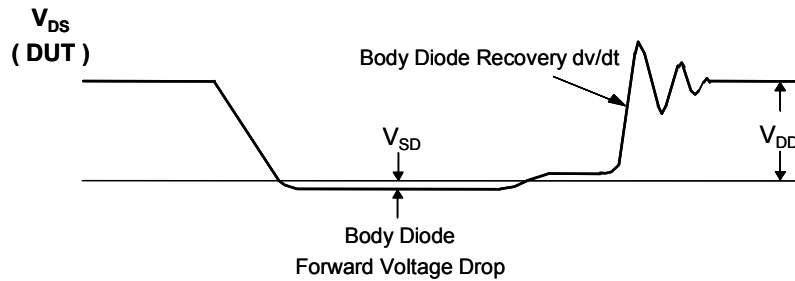
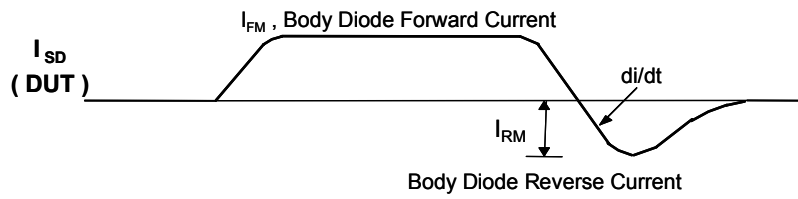
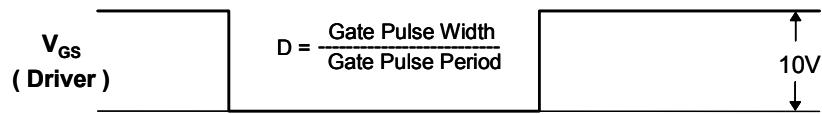
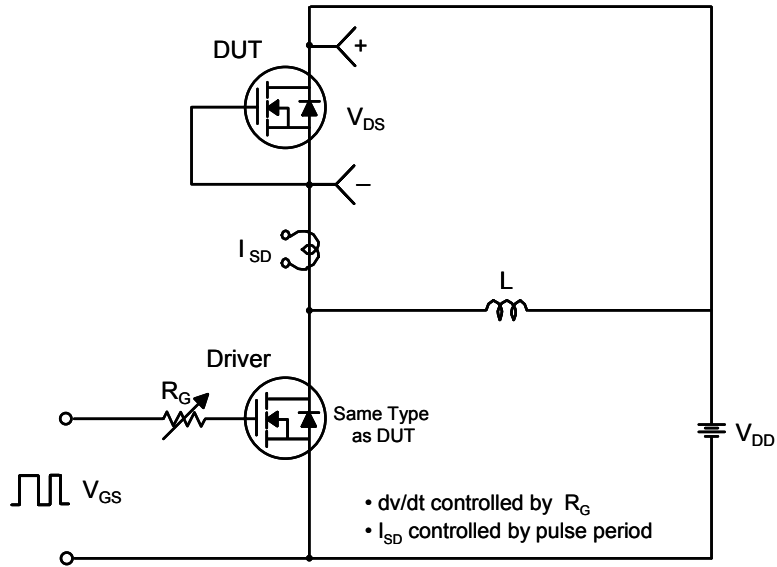


Resistive Switching Test Circuit & Waveforms

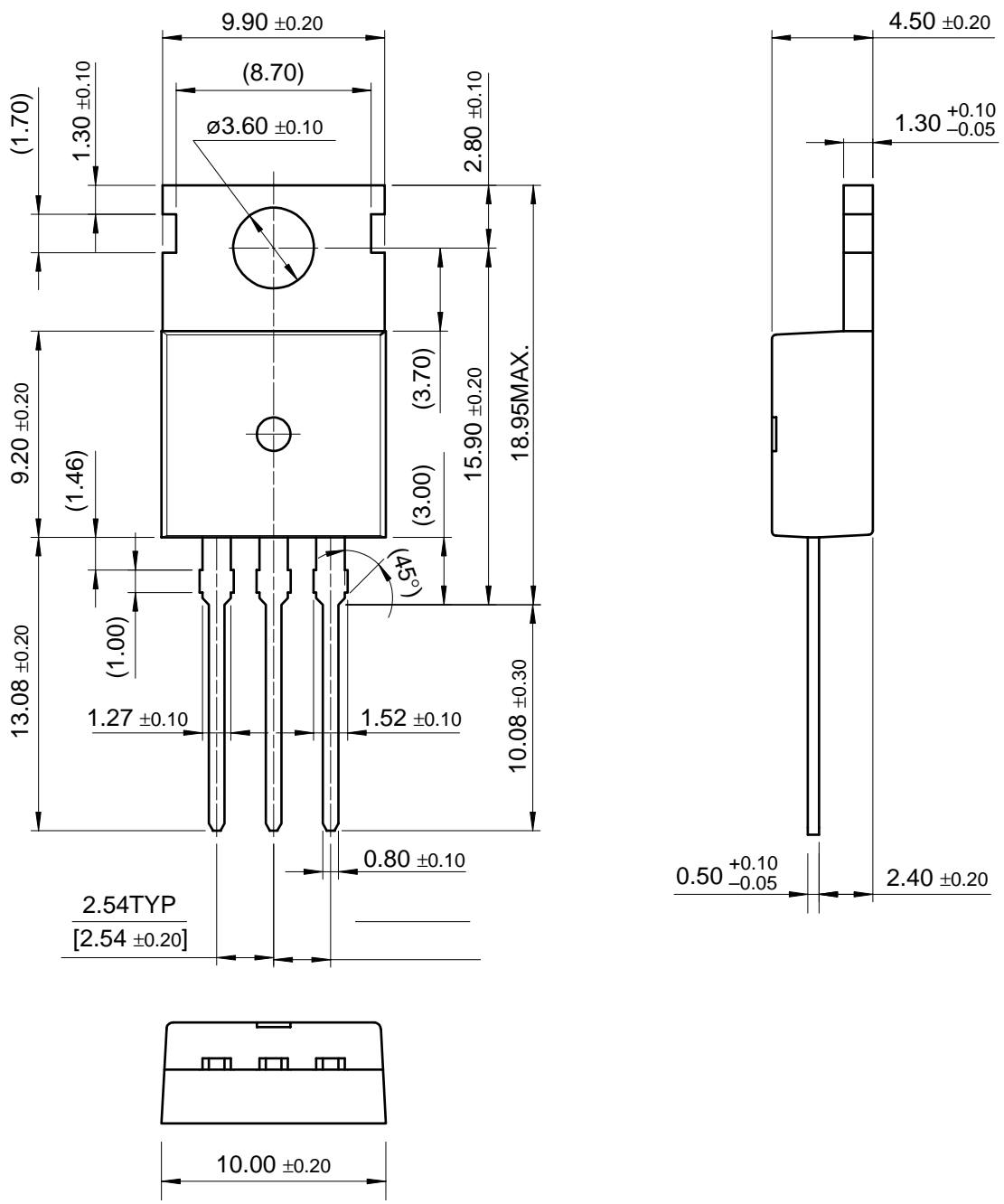


Unclamped Inductive Switching Test Circuit & Waveforms

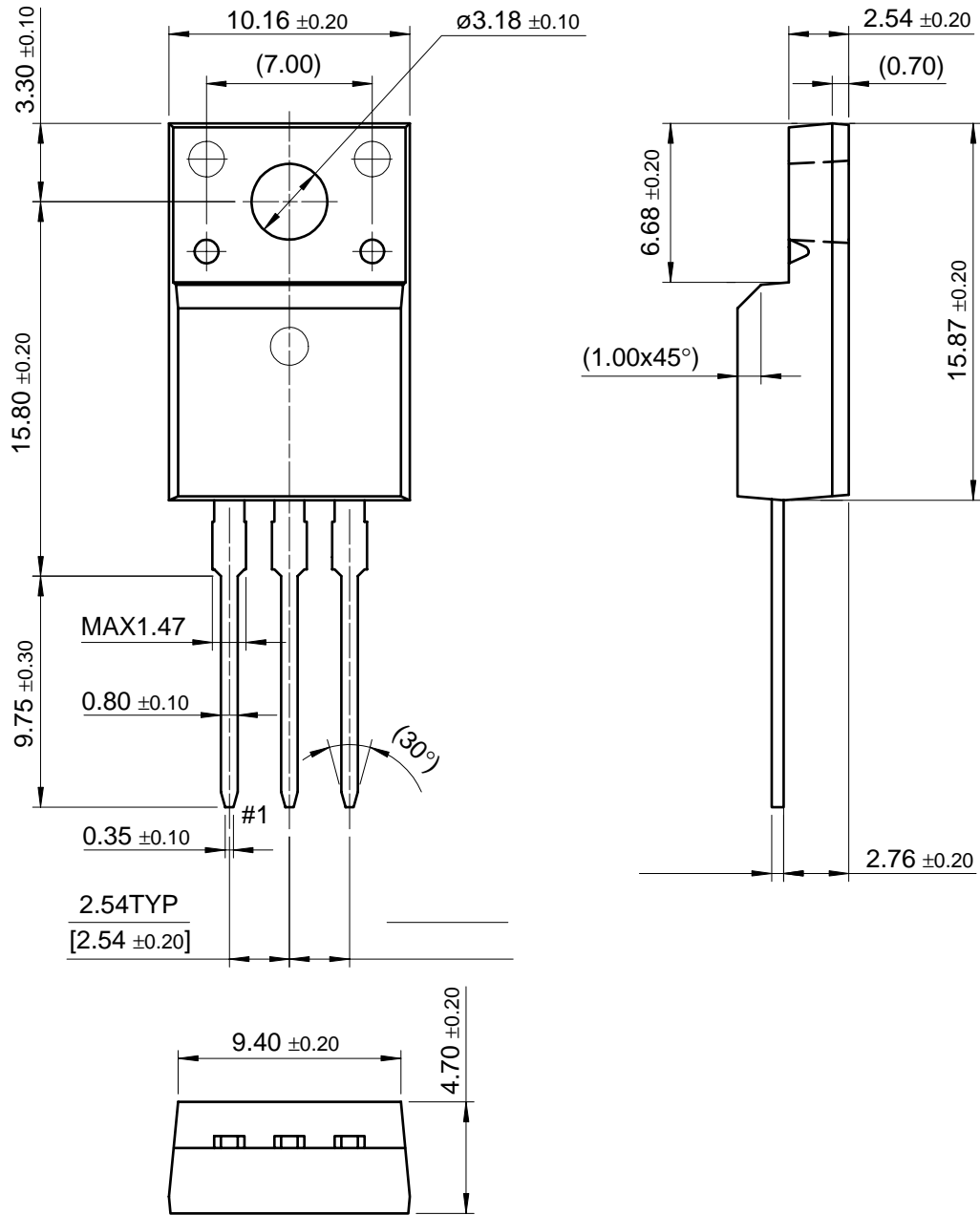




Peak Diode Recovery dv/dt Test Circuit & Waveforms

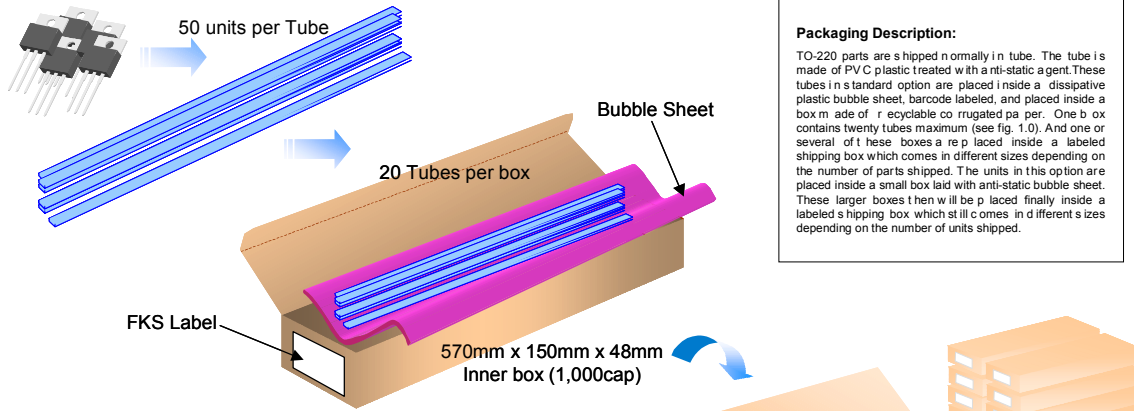


Peak Diode Recovery dv/dt Test Circuit & Waveforms



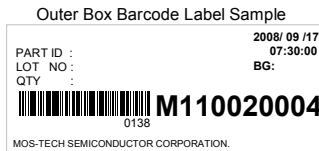
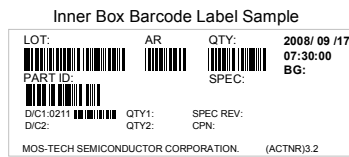
TO-220 Short Lead Tube Packing Data

TO-220 Short Lead Tube Packing Configuration: Figure 1.0



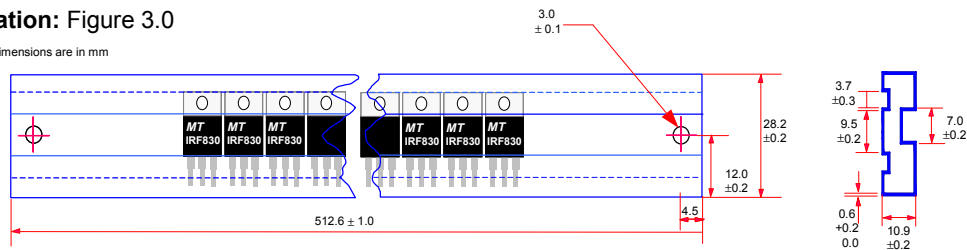
TO-220 Short Lead Packaging Information: Figure 2.0

TO-220 Packaging Information	
Packaging Option	Standard (no flow code)
Packaging type	Reel/Tube
Qty per Tube/ Inner Box	50
Inner Box Dimension (mm)	570x150x48
Max qty per Box	1,000
Outer Box Dimension (mm)	590x330x245
Max qty per Box	8,000
Weight per unit (gm)	1.9588
Note/Comments	



TO-220 Short Lead Tube Configuration: Figure 3.0

Note: All dimensions are in mm





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Keep safety first in your circuit designs!

1. MOS-TECH Semiconductor Corp. puts the maximum effort into making semiconductor products better and more reliable, but there is always the possibility that trouble may occur with them. Trouble with semiconductors may lead to personal injury, fire or property damage. Remember to give due consideration to safety when making your circuit designs, with appropriate measures such as (i) placement of substitutive, auxiliary circuits, (ii) use of nonflammable material or (iii) prevention against any malfunction or mishap.