

HITJ0202MP

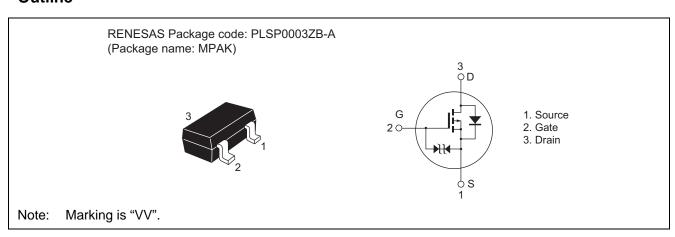
-20V, -2.7A, $105m\Omega$ max. Silicon P Channel MOS FET Power Switching

R07DS0474EJ0200 Rev.2.00 May 09, 2013

Features

- Low on-resistance $R_{DS(on)} = 83 \text{ m}\Omega \text{ typ } (V_{GS} = -4.5 \text{ V}, I_D = -1.4 \text{ A})$
- Low drive current
- High speed switching
- 2.5 V gate drive

Outline



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	-20	V
Gate to source voltage	V _{GSS}	+8 / -12	V
Drain current	I _D	-2.7	Α
Drain peak current	I _{D(pulse)} Note1	-8.0	Α
Body - drain diode reverse drain current	I _{DR}	-2.7	Α
Channel dissipation	Pch Note2	0.8	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

2. When using the glass epoxy board (FR-4: 40 x 40 x 1 mm)

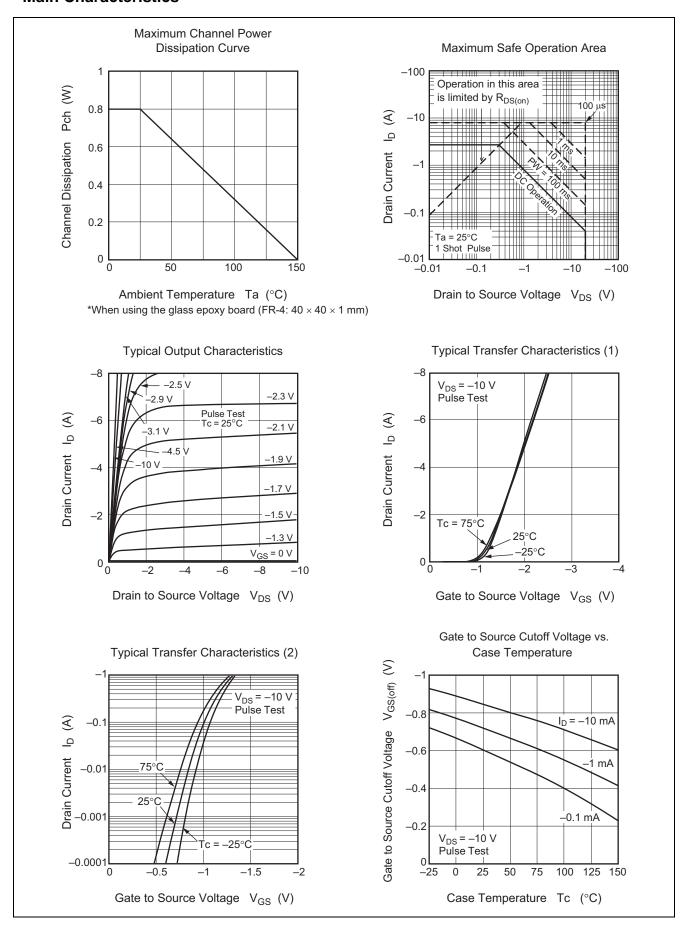
Electrical Characteristics

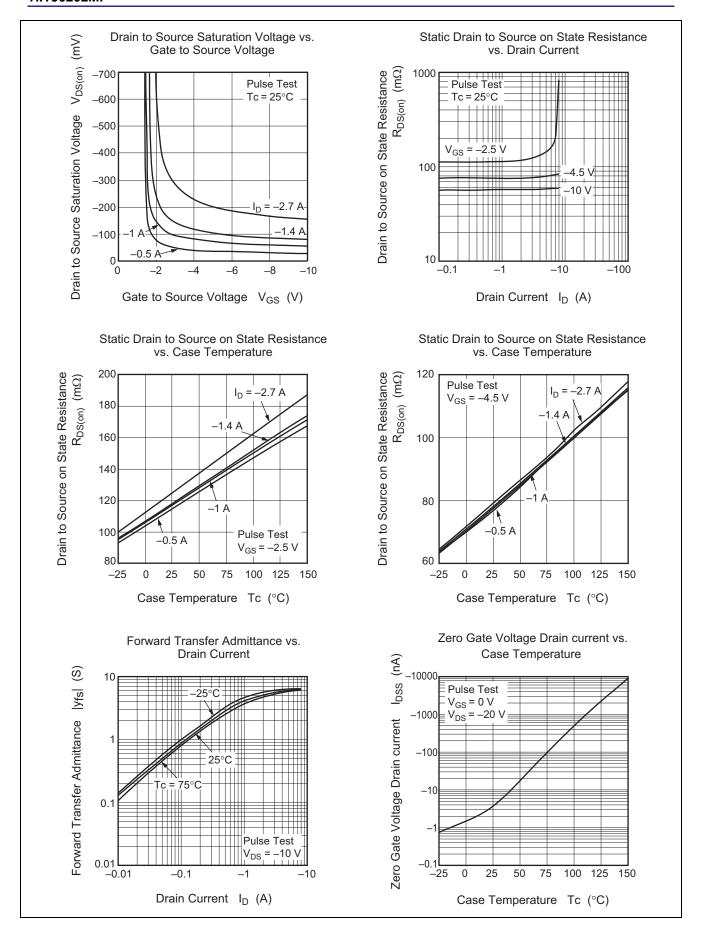
 $(Ta = 25^{\circ}C)$

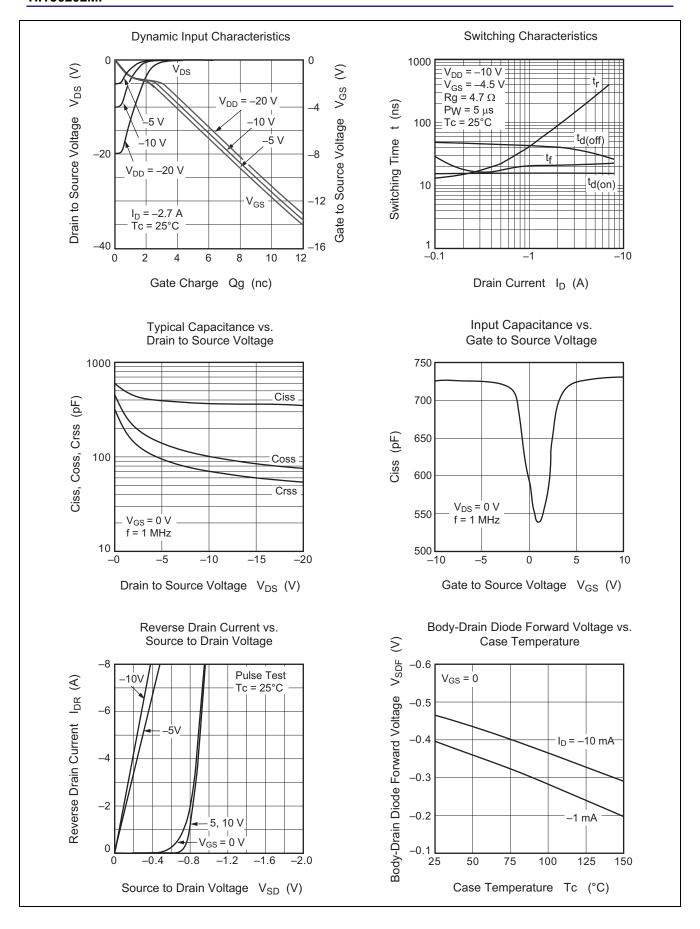
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	-20	_	_	V	$I_D = -10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	+8	_		V	$I_G = +100 \mu A, V_{DS} = 0$
	$V_{(BR)GSS}$	-12	_	1	V	$I_G = -100 \mu A, V_{DS} = 0$
Gate to source leak current	I _{GSS}	_	_	+10	μΑ	$V_{GS} = +6 \text{ V}, V_{DS} = 0$
	I _{GSS}	_	_	-10	μΑ	$V_{GS} = -10 \text{ V}, V_{DS} = 0$
Drain to source leak current	I _{DSS}	_	_	-1	μΑ	$V_{DS} = -20 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	-0.4	_	-1.4	V	$V_{DS} = -10 \text{ V}, I_{D} = -1 \text{ mA}$
Drain to source on state resistance	R _{DS(on)}	_	83	105	mΩ	$I_D = -1.4 \text{ A}, V_{GS} = -4.5 \text{ V}^{\text{Note3}}$
	R _{DS(on)}	_	124	170	mΩ	$I_D = -1.4 \text{ A}, V_{GS} = -2.5 \text{ V}^{\text{Note3}}$
Forward transfer admittance	y _{fs}	3	4.5		S	$I_D = -1.4 \text{ A}, V_{DS} = -10 \text{ V}^{\text{Note3}}$
Input capacitance	Ciss	_	365	_	pF	V _{DS} = -10 V
Output capacitance	Coss	_	102	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	70		pF	f = 1 MHz
Turn - on delay time	t _{d(on)}	_	15		ns	$I_D = -1.4 \text{ A}$
Rise time	t _r	_	57		ns	$V_{GS} = -4.5 \text{ V}$
Turn - off delay time	t _{d(off)}	_	40		ns	$R_L = 7.1 \Omega$
Fall time	t _f	_	21		ns	$Rg = 4.7 \Omega$
Total gate charge	Qg	_	4.3	_	nC	$V_{DD} = -10 \text{ V}$
Gate to source charge	Qgs	_	0.6	_	nC	$V_{GS} = -4.5 \text{ V}$
Gate to drain charge	Qgd	_	1.7		nC	$I_D = -2.7 \text{ A}$
Body - drain diode forward voltage	V_{DF}	_	-0.85	-1.1	V	$I_F = -2.7 \text{ A}, V_{GS} = 0^{\text{Note3}}$

Notes: 3. Pulse test

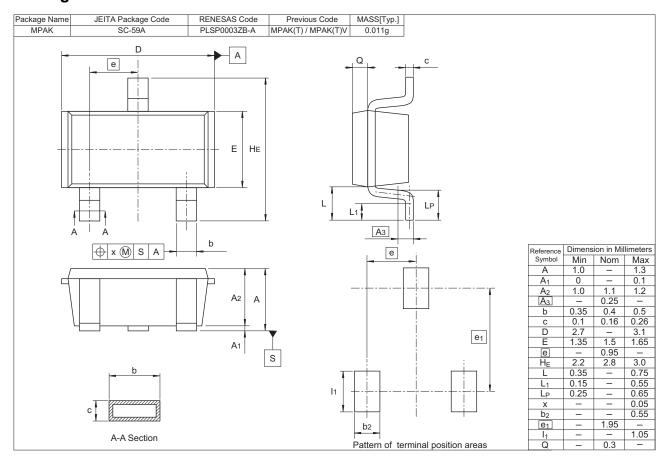
Main Characteristics







Package Dimensions



Ordering Information

Orderable Part Number	Quantity	Shipping Container
HITJ0202MPTL-HQ	3000 pcs.	φ178 mm reel, 8 mm Emboss taping

Note: This product is designed for consumer use and not for automotive.

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