

To our customers,

Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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H7N0602LD, H7N0602LS, H7N0602LM

Silicon N Channel MOS FET
High Speed Power Switching

REJ03G1130-0600

Rev.6.00

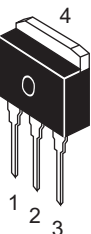
Oct 16, 2006

Features

- Low on-resistance
 $R_{DS(on)} = 4.1 \text{ m}\Omega$ typ.
- 4.5 V gate drive devices
- High Speed Switching

Outline

RENESAS Package code: PRSS0004AE-A
(Package name: LDKPAK (L))



H7N0602LD

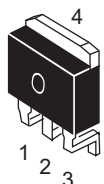
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(Package name: LDKPAK (S)-(1))



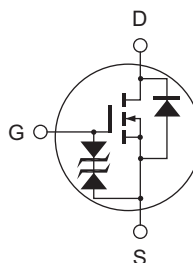
H7N0602LS

1. Gate
2. Drain
3. Source
4. Drain

RENESAS Package code: PRSS0004AE-C
(Package name: LDKPAK (S)-(2))



H7N0602LM



Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Drain to source voltage	V_{DSS}	60	V
Gate to source voltage	V_{GSS}	±20	V
Drain current	I_D	85	A
Drain peak current	$I_{D(pulse)}$ ^{Note 1}	340	A
Body to drain diode reverse drain current	I_{DR}	85	A
Avalanche current	I_{AP} ^{Note 3}	65	A
Avalanche energy	E_{AR} ^{Note 3}	362	mJ
Channel dissipation	P_{ch} ^{Note 2}	100	W
Channel temperature	T_{ch}	150	°C
Storage temperature	T_{stg}	-55 to +150	°C

Notes: 1. $PW \leq 10 \mu s$, duty cycle $\leq 1\%$
 2. Value at $T_c = 25^\circ C$
 3. Value at $T_{ch} = 25^\circ C$, $R_g \geq 50 \Omega$

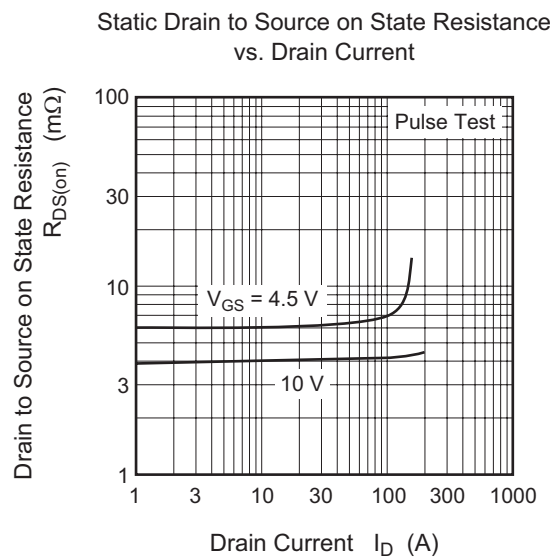
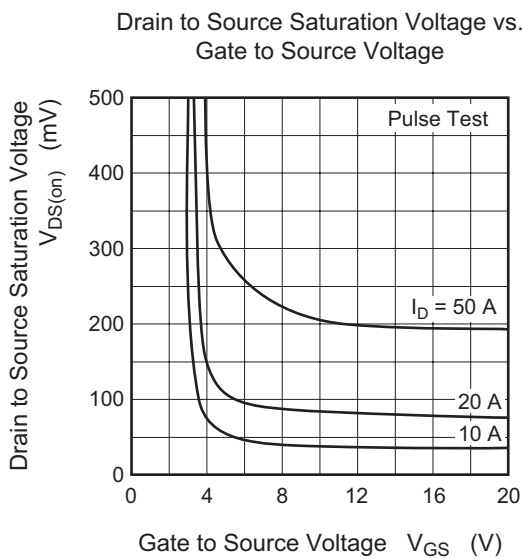
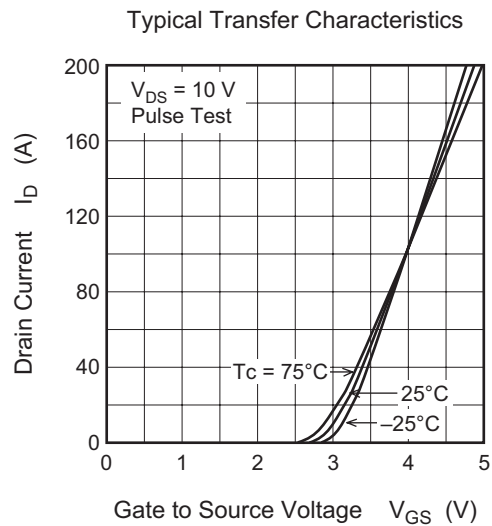
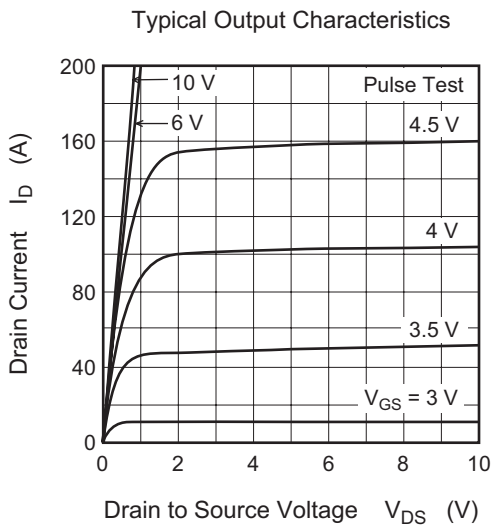
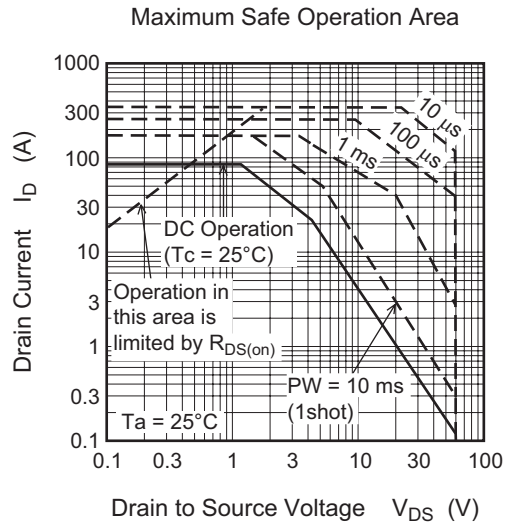
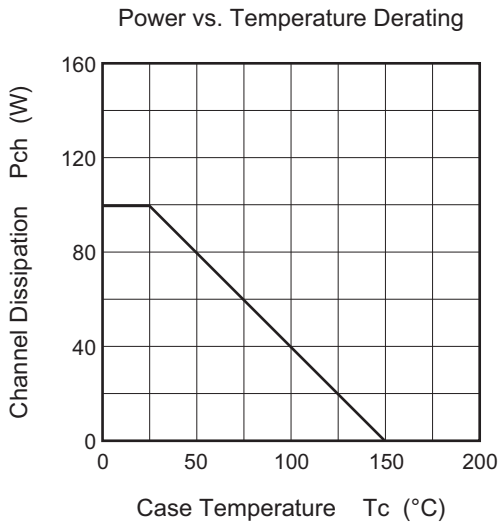
Electrical Characteristics

(Ta = 25°C)

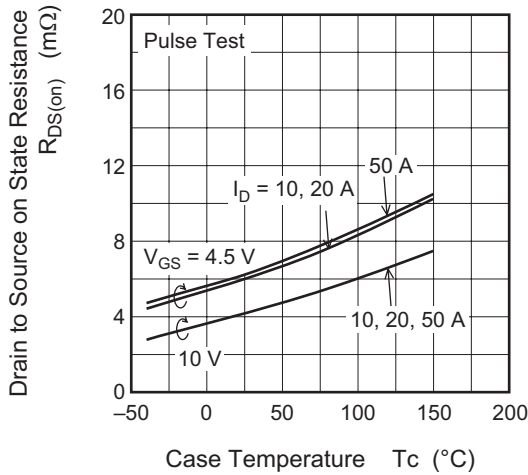
Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	60	—	—	V	$I_D = 10 \text{ mA}$, $V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±20	—	—	V	$I_G = \pm 100 \mu A$, $V_{DS} = 0$
Zero gate voltage drain current	I_{DSS}	—	—	10	μA	$V_{DS} = 60 \text{ V}$, $V_{GS} = 0$
Gate to source leak current	I_{GSS}	—	—	±10	μA	$V_{GS} = \pm 16 \text{ V}$, $V_{DS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	1.5	—	2.5	V	$V_{DS} = 10 \text{ V}$, $I_D = 1 \text{ mA}$ ^{Note 4}
Forward transfer admittance	$ y_{fs} $	70	120	—	S	$I_D = 45 \text{ A}$, $V_{DS} = 10 \text{ V}$ ^{Note 4}
Static drain to source on state resistance	$R_{DS(on)}$	—	4.1	5.2	m Ω	$I_D = 45 \text{ A}$, $V_{GS} = 10 \text{ V}$ ^{Note 4}
		—	6.2	9.0	m Ω	$I_D = 45 \text{ A}$, $V_{GS} = 4.5 \text{ V}$ ^{Note 4}
Input capacitance	C_{iss}	—	9000	—	pF	$V_{DS} = 10 \text{ V}$
Output capacitance	C_{oss}	—	1000	—	pF	$V_{GS} = 0$
Reverse transfer capacitance	C_{rss}	—	470	—	pF	$f = 1 \text{ MHz}$
Total gate charge	Q_g	—	140	—	nC	$V_{DD} = 25 \text{ V}$
Gate to source charge	Q_{gs}	—	30	—	nC	$V_{GS} = 10 \text{ V}$
Gate to drain charge	Q_{gd}	—	30	—	nC	$I_D = 85 \text{ A}$
Turn-on delay time	$t_{d(on)}$	—	55	—	ns	$V_{GS} = 10 \text{ V}$
Rise time	t_r	—	290	—	ns	$I_D = 45 \text{ A}$
Turn-off delay time	$t_{d(off)}$	—	140	—	ns	$R_L = 0.67 \Omega$
Fall time	t_f	—	50	—	ns	$R_g = 4.7 \Omega$
Body to drain diode forward voltage	V_{DF}	—	0.95	—	V	$I_F = 85 \text{ A}$, $V_{GS} = 0$
Body to drain diode reverse recovery time	t_{rr}	—	45	—	ns	$I_F = 85 \text{ A}$, $V_{GS} = 0$ $di_F/dt = 100 \text{ A}/\mu s$

Note: 4. Pulse test

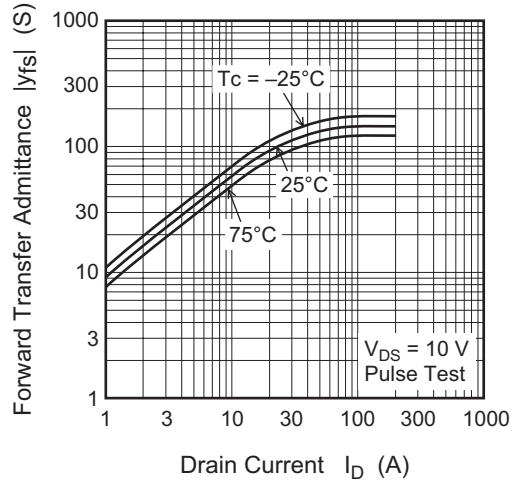
Main Characteristics



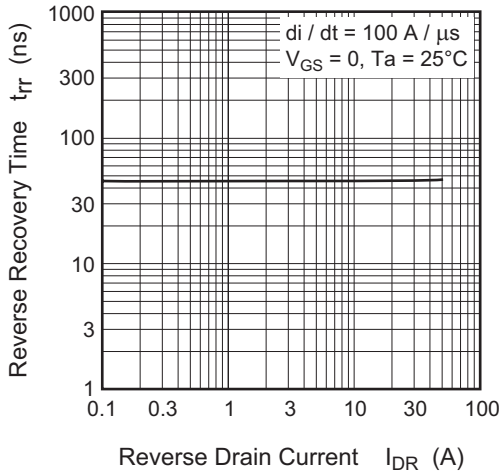
Static Drain to Source on State Resistance vs. Temperature



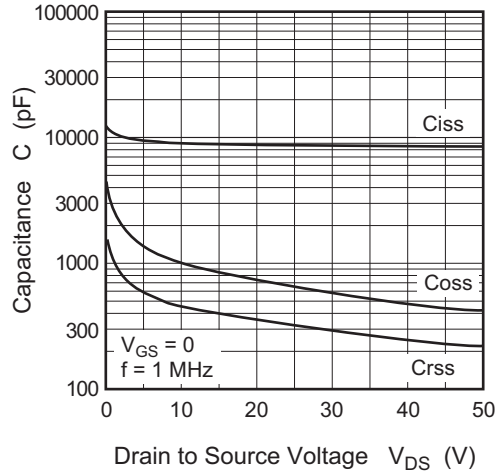
Forward Transfer Admittance vs. Drain Current



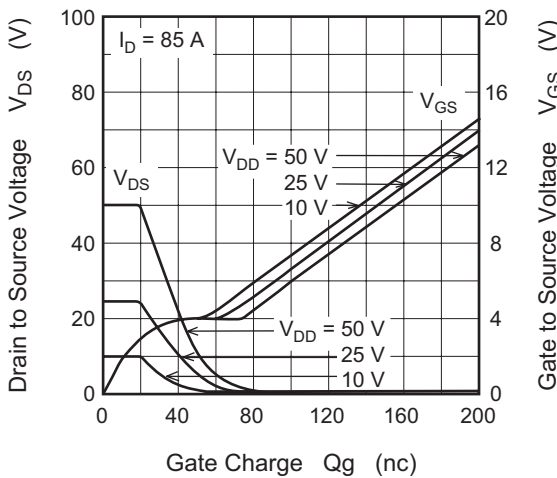
Body to Drain Diode Reverse Recovery Time



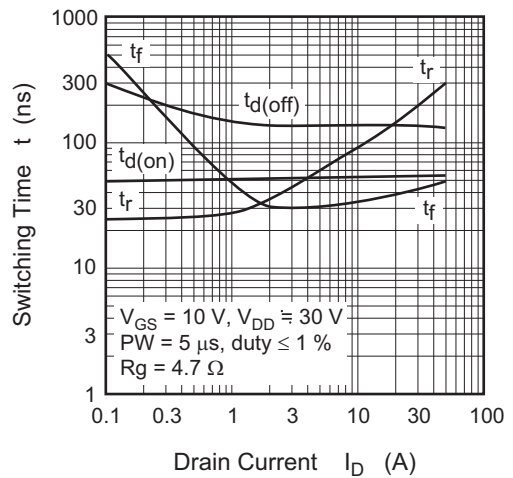
Typical Capacitance vs. Drain to Source Voltage

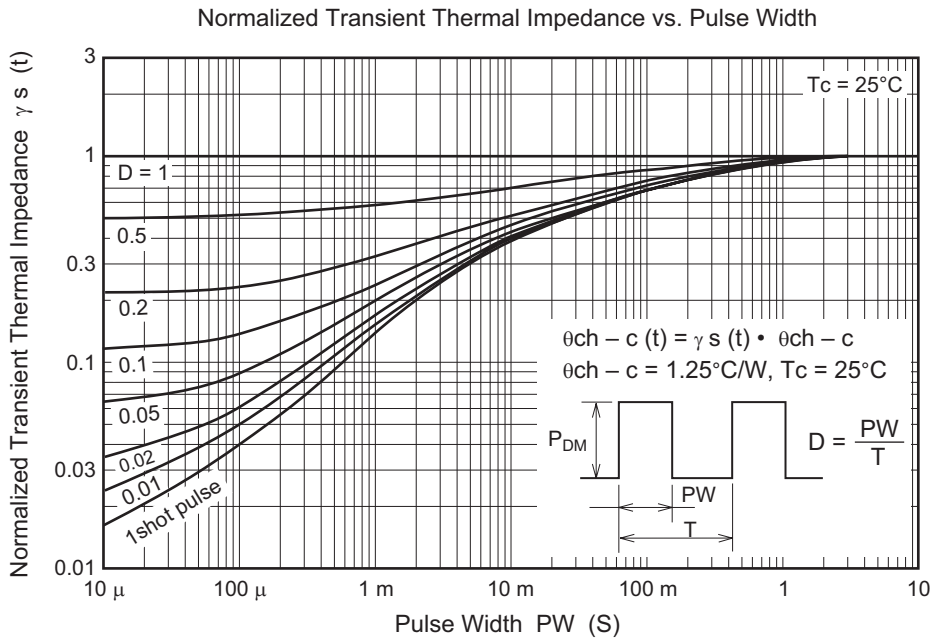
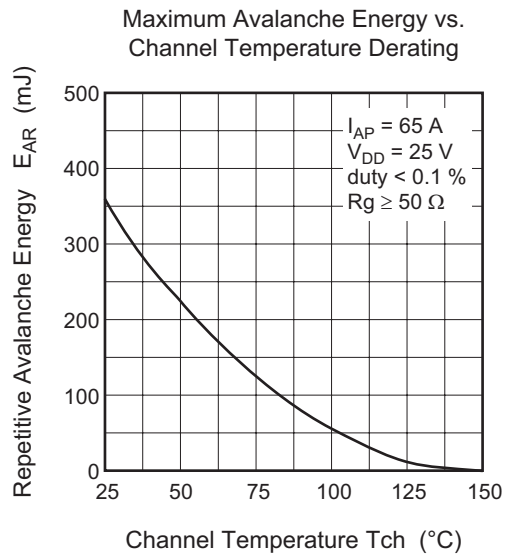
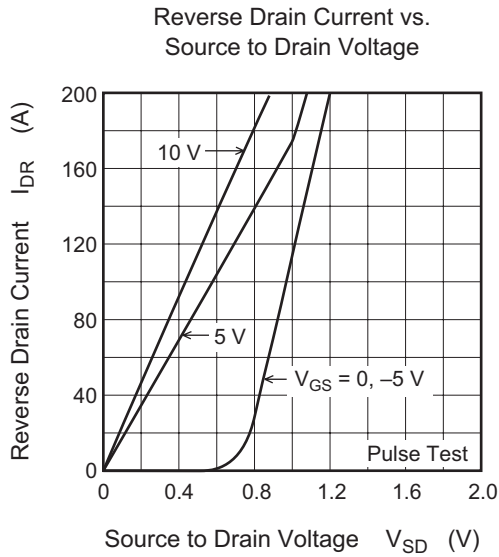


Dynamic Input Characteristics

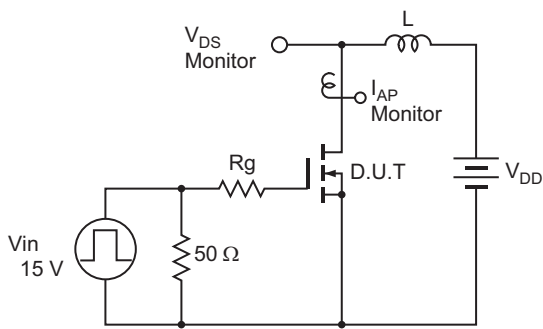


Switching Characteristics



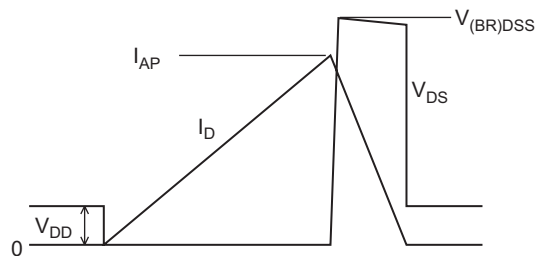


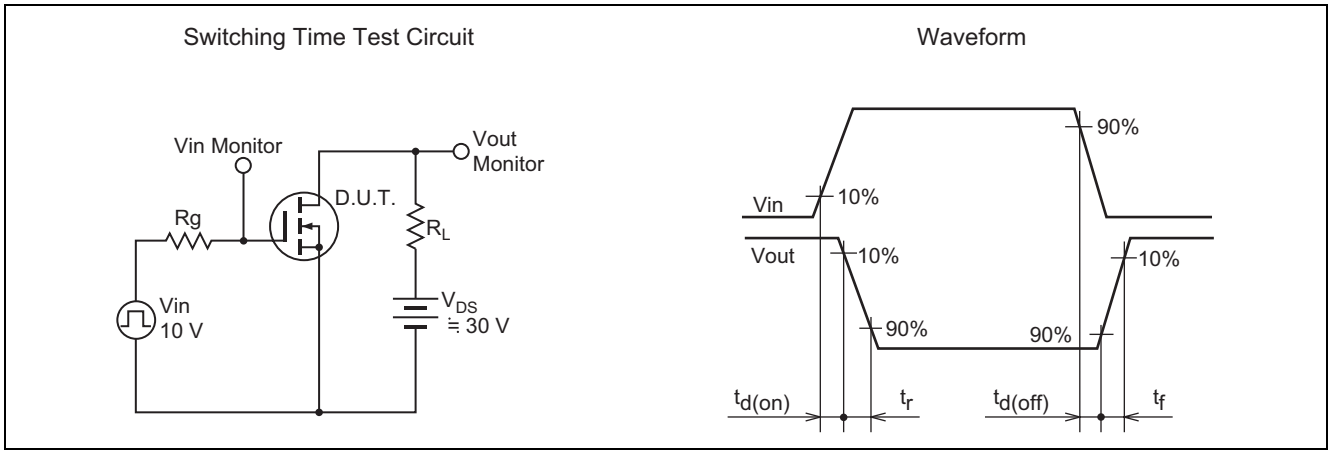
Avalanche Test Circuit



Avalanche Waveform

$$E_{AR} = \frac{1}{2} \cdot L \cdot I_{AP}^2 \cdot \frac{V_{DSS}}{V_{DSS} - V_{DD}}$$

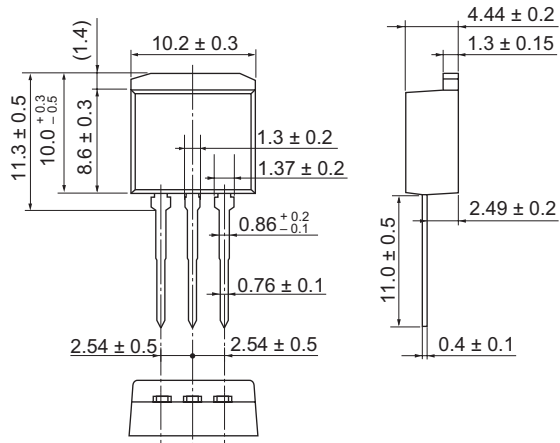




Package Dimensions

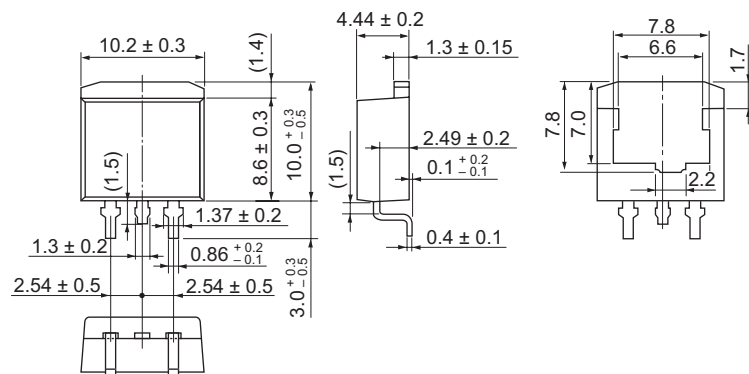
Package Name	JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
LDBAK(L)	—	PRSS0004AE-A	LDBAK(L) / LDBAK(L)V	1.40g

Unit: mm



Package Name	JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
LDBAK(S)-(1)	SC-83	PRSS0004AE-B	LDBAK(S)-(1) / LDBAK(S)-(1)V	1.30g

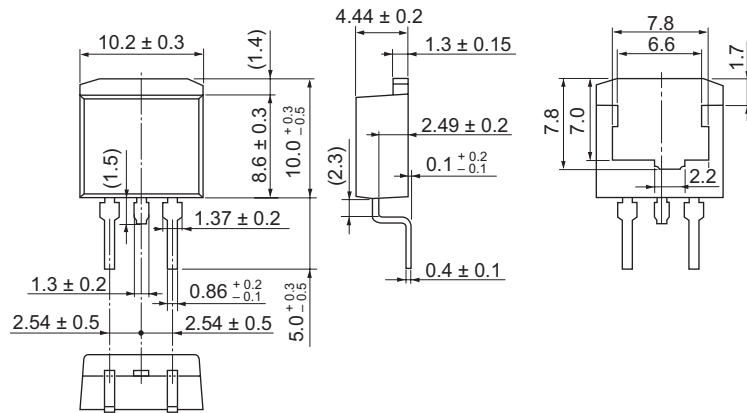
Unit: mm



H7N0602LD, H7N0602LS, H7N0602LM

Package Name	JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
LDBPAK(S)-(2)	—	PRSS0004AE-C	LDBPAK(S)-(2) / LDBPAK(S)-(2)V	1.35g

Unit: mm



Ordering Information

Part Name	Quantity	Shipping Container
H7N0602LD-E	500 pcs	Box (Conductive Sack)
H7N0602LSTL-E	1000 pcs	Taping
H7N0602LMTL-E	1000 pcs	Taping

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