# Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: http://www.renesas.com

April 1<sup>st</sup>, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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# RENESAS

# H5N5006DL, H5N5006DS

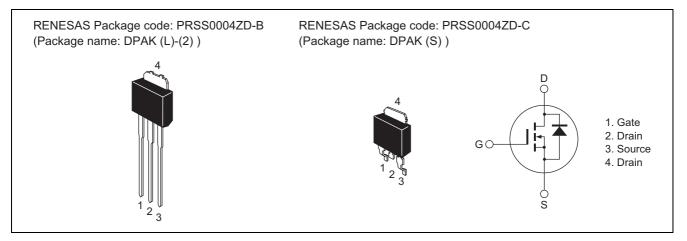
Silicon N Channel MOS FET High Speed Power Switching

> REJ03G0397-0100 Rev.1.00 May 30, 2006

### Features

- Low on-resistance:  $R_{DS(on)} = 2.5 \Omega$  typ.
- Low leakage current:  $I_{DSS} = 1 \ \mu A \ max.$  (at  $V_{DS} = 500 \ V$ )
- High speed switching:  $t_f = 15$  ns typ. (at  $V_{GS} = 10$  V,  $V_{DD} \cong 250$  V,  $I_D = 1.5$  A)
- Low gate charge: Qg = 14 nC typ. (at  $V_{DD} = 400 \text{ V}$ ,  $V_{GS} = 10 \text{ V}$ ,  $I_D = 3 \text{ A}$ )
- Avalanche ratings

### Outline



## **Absolute Maximum Ratings**

			$(Ta = 25^{\circ}C)$
Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	500	V
Gate to source voltage	V <sub>GSS</sub>	±30	V
Drain current	Ι <sub>D</sub>	3	A
Drain peak current	I <sub>D (pulse)</sub> Note1	12	A
Body-drain diode reverse drain current	I <sub>DR</sub>	3	A
Body-drain diode reverse drain peak current	I <sub>DR (pulse)</sub> Note1	12	A
Avalanche current	I <sub>AP</sub> <sup>Note3</sup>	3	A
Channel dissipation	Pch Note2	30	W
Channel to case thermal impedance	θch-c	4.17	°C/W
Channel temperature	Tch	150	٥C
Storage temperature	Tstg	-55 to +150	٥C

Notes: 1.  $PW \le 10 \ \mu s$ , duty cycle  $\le 1\%$ 

2. Value at Tc =  $25^{\circ}C$ 

3. STch =  $25^{\circ}$ C, Tch  $\leq 150^{\circ}$ C



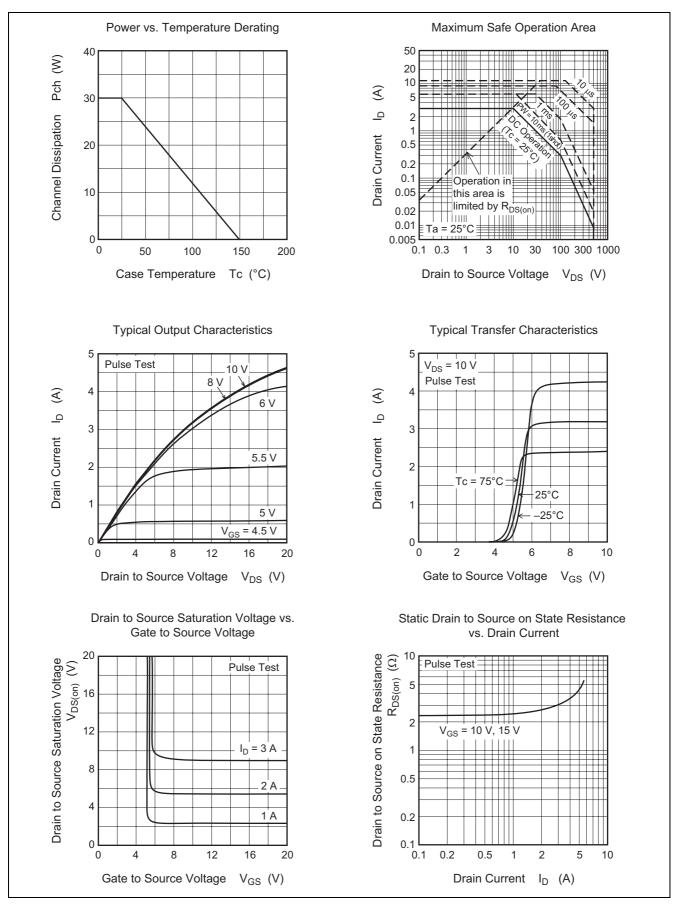
# **Electrical Characteristics**

Item	Symbol	Min	Тур	Мах	Unit	Test conditions
Drain to source breakdown voltage	V <sub>(BR)DSS</sub>	500	—	—	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>		—	1	μA	$V_{DS} = 500 \text{ V}, V_{GS} = 0$
Gate to source leak current	I <sub>GSS</sub>	_	—	±0.1	μΑ	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0$
Gate to source cutoff voltage	V <sub>GS(off)</sub>	3.0	—	4.5	V	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$
Forward transfer admittance	y <sub>fs</sub>	1.5	2.5	—	S	$I_D = 1.5 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note4}}$
Static drain to source on state resistance	$R_{\text{DS(on)}}$	_	2.5	3.0	Ω	$I_D = 1.5 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note4}}$
Input capacitance	Ciss	_	365	_	pF	$V_{DS} = 25 V$ $V_{GS} = 0$ $f = 1 MHz$
Output capacitance	Coss	_	35	_	pF	
Reverse transfer capacitance	Crss		8	—	pF	
Turn-on delay time	t <sub>d(on)</sub>		20	—	ns	$V_{DD}\cong 250~V,~I_D=1.5~A$
Rise time	tr		12	—	ns	V <sub>GS</sub> = 10 V
Turn-off delay time	t <sub>d(off)</sub>		48	—	ns	$R_L = 167 \Omega$
Fall time	t <sub>f</sub>		15	—	ns	Rg = 10 Ω
Total gate charge	Qg		14	—	nC	V <sub>DD</sub> = 400 V
Gate to source charge	Qgs		2	—	nC	V <sub>GS</sub> = 10 V
Gate to drain charge	Qgd	_	8	_	nC	I <sub>D</sub> = 3 A
Body-drain diode forward voltage	V <sub>DF</sub>	_	0.85	1.3	V	$I_F = 3 \text{ A}, V_{GS} = 0^{\text{Note4}}$
Body-drain diode reverse recovery time	t <sub>rr</sub>	—	270	—	ns	$I_F = 3 \text{ A}, V_{GS} = 0$ $di_F/dt = 100 \text{ A}/\mu\text{s}$
Body-drain diode reverse recovery charge	Qrr	—	0.8	—	μC	

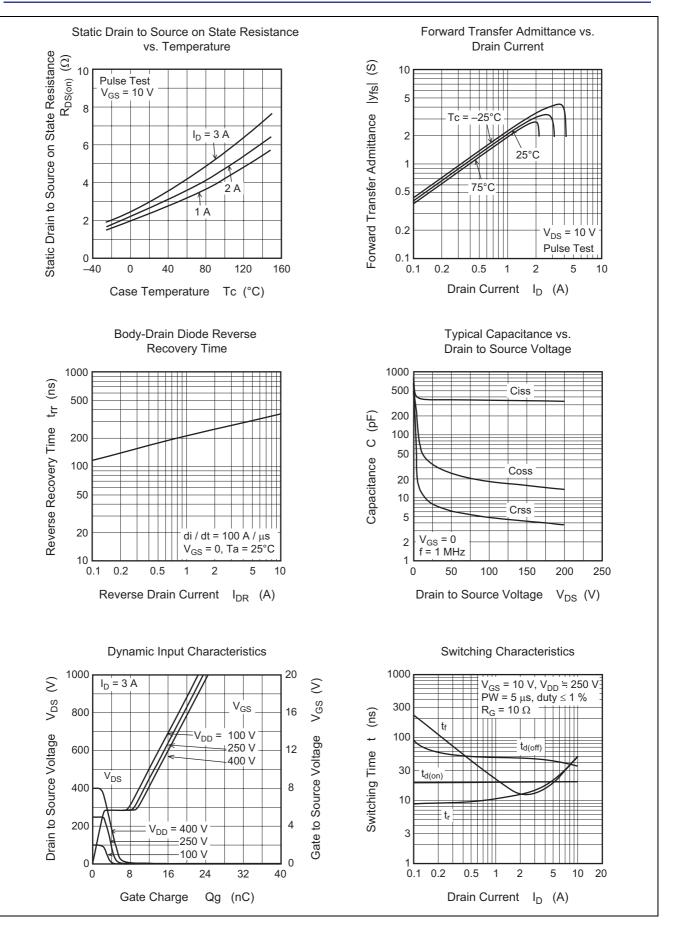
Notes: 4. Pulse test



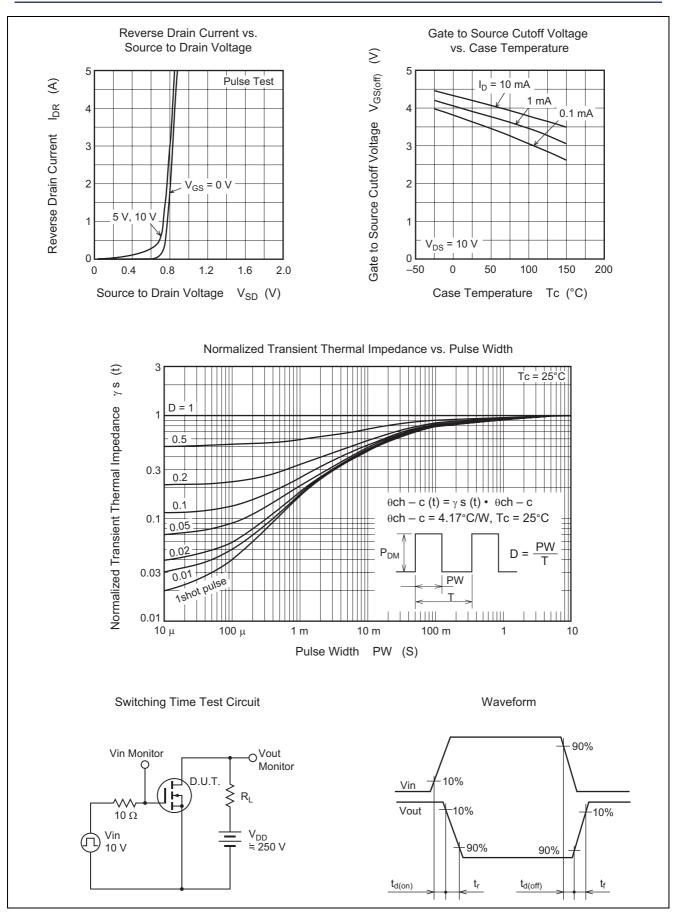
### **Main Characteristics**







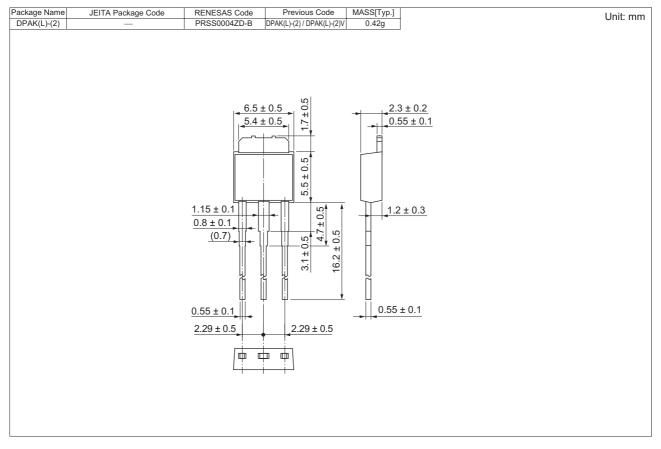




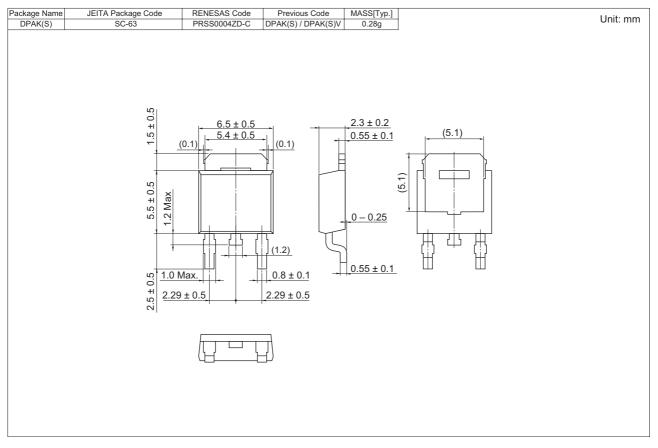


## **Package Dimensions**

### • H5N5006DL



### • H5N5006DS





# **Ordering Information**

Part Name	Quantity	Shipping Container		
H5N5006DL-E	3200 pcs	Box (Sack)		
H5N5006DSTL-E	3000 pcs	Taping		

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.



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