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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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H5N3008P

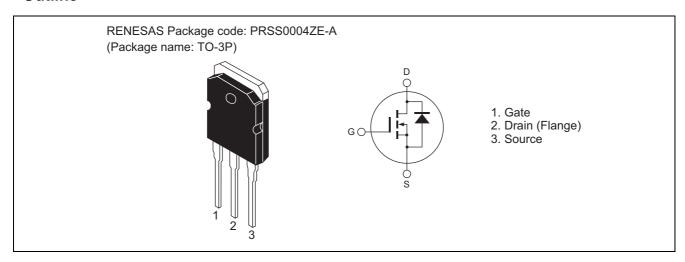
Silicon N Channel MOS FET High Speed Power Switching

REJ03G0539-0300 Rev.3.00 Oct 16, 2006

Features

- Low on-resistance
- Low leakage current
- High speed switching
- Built-in fast recovery diode

Outline



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Drain to Source voltage	V _{DSS}	300	V
Gate to Source voltage	V _{GSS}	±30	V
Drain current	I _D	40	А
Drain peak current	I _{D (pulse)} Note1	160	А
Body-Drain diode reverse Drain current	I _{DR}	40	А
Avalanche current	I _{AP} Note3	30	А
Channel dissipation	Pch Note2	150	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

2. Value at $Tc = 25^{\circ}C$

3. STch = 25° C, Tch $\leq 150^{\circ}$ C

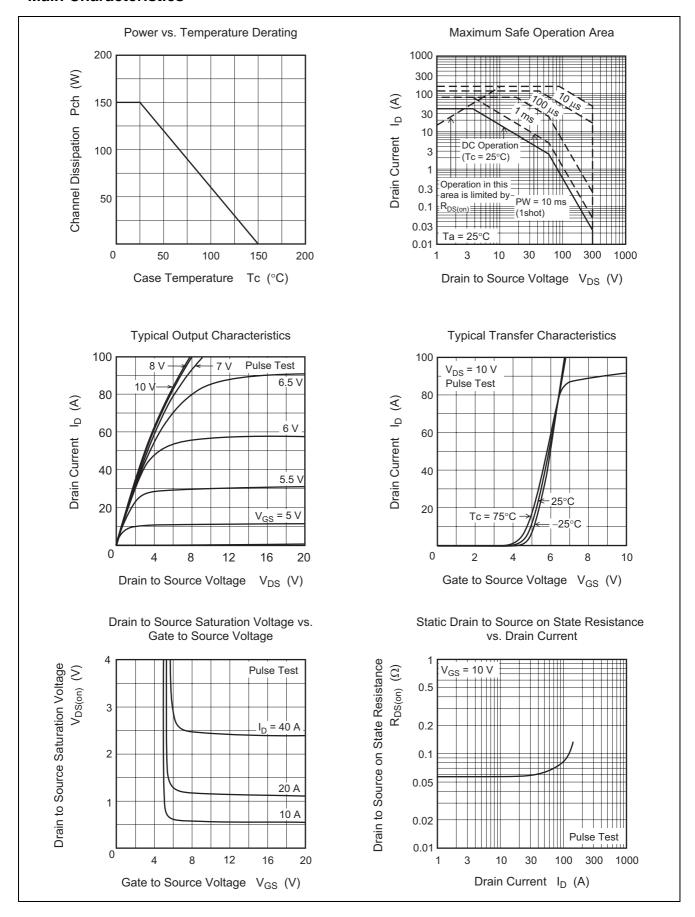
Electrical Characteristics

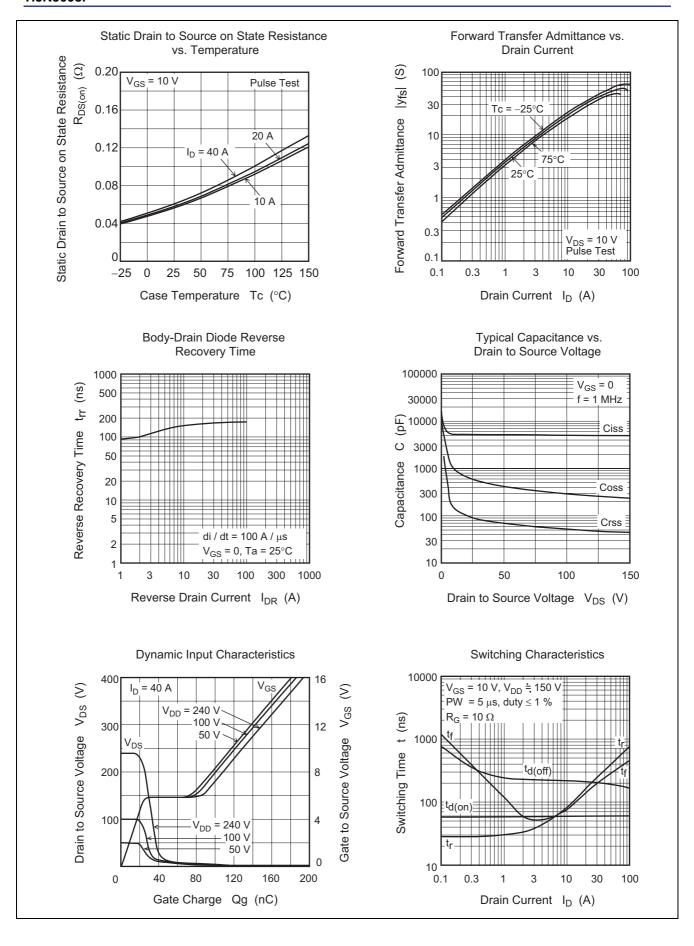
 $(Ta = 25^{\circ}C)$

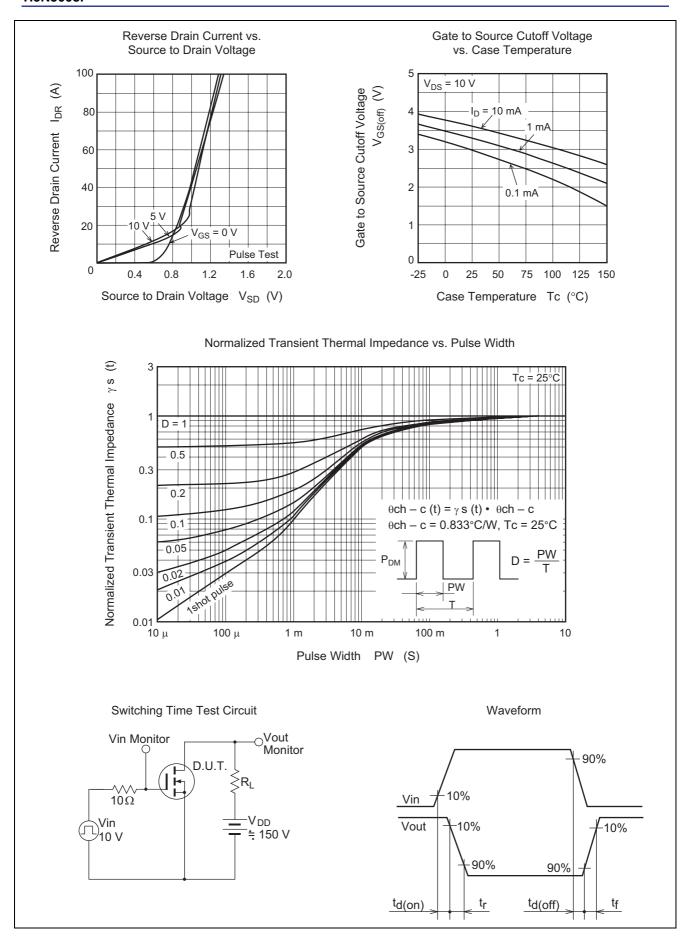
Item	Symbol	Min	Тур	Max	Unit	Test conditions	
Drain to Source breakdown voltage	$V_{(BR)DSS}$	300		_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$	
Zero Gate voltage Drain current	I _{DSS}	_	_	10	μΑ	$V_{DS} = 300 \text{ V}, V_{GS} = 0$	
Gate to Source leak current	I _{GSS}	_	_	±0.1	μΑ	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0$	
Gate to Source cutoff voltage	$V_{GS(off)}$	2.0	_	4.0	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$	
Forward transfer admittance	yfs	19	32	_	S	$I_D = 20 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note4}}$	
Static Drain to Source on state resistance	R _{DS(on)}	_	0.058	0.069	Ω	$I_D = 20 \text{ A}, V_{GS} = 10 \text{ V}^{Note4}$	
Input capacitance	Ciss	_	5150	_	pF	V _{DS} = 25 V	
Output capacitance	Coss	_	590	_	pF	$V_{GS} = 0$	
Reverse transfer capacitance	Crss	_	90	_	рF	f = 1 MHz	
Turn-on delay time	t _{d(on)}	_	60	_	ns	I _D = 20 A	
Rise time	t _r	_	170	_	ns	V _{GS} = 10 V	
Turn-off delay time	t _{d(off)}	_	210	_	ns	$R_L = 7.5 \Omega$	
Fall time	t _f	_	140	_	ns	$Rg = 10 \Omega$	
Total Gate charge	Qg	_	130	_	nC	V _{DD} = 240 V	
Gate to Source charge	Qgs	_	25	_	nC	V _{GS} = 10 V	
Gate to Drain charge	Qgd	_	60	_	nC	I _D = 40 A	
Body-Drain diode forward voltage	V_{DF}	_	1.0	1.5	V	$I_F = 40 \text{ A}, V_{GS} = 0^{\text{Note4}}$	
Body-Drain diode reverse recovery time	trr	_	170	_	ns	I _F = 40 A, V _{GS} = 0	
Body-Drain diode reverse recovery charge	Qrr	_	1.1	_	μC	diF/dt = 100 A/μs	

Notes: 4. Pulse test

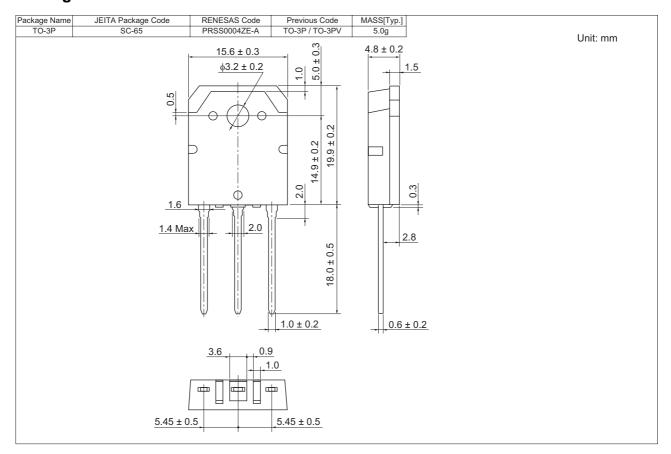
Main Characteristics







Package Dimensions



Ordering Information

Part Name	Quantity	Shipping Container
H5N3008P-E	360 pcs	Box (Tube)

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