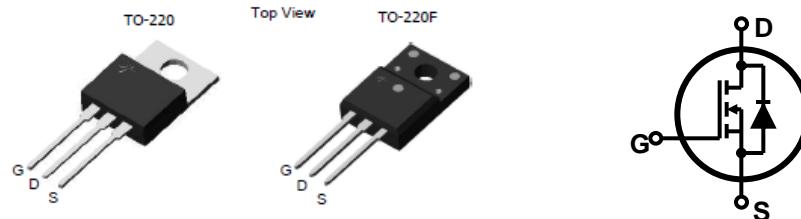


## Features

- Low gate charge
- 100% avalanche tested
- Improved dv/dt capability
- RoHS compliant
- Halogen free package
- JEDEC Qualification

$V_{DSS} = 715 \text{ V} @ T_{jmax}$   
 $I_D = 9.5 \text{ A}$   
 $R_{DS(on)} = 0.98 \Omega(\text{max}) @ V_{GS} = 10 \text{ V}$



Device	Package	Marking	Remark
TMP10N65 / TMPF10N65	TO-220 / TO-220F	TMP10N65 / TMPF10N65	RoHS
TMP10N65G / TMPF10N65G	TO-220 / TO-220F	TMP10N65G / TMPF10N65G	Halogen Free

## Absolute Maximum Ratings

Parameter	Symbol	TMP10N65(G)	TMPF10N65(G)	Unit
Drain-Source Voltage	$V_{DSS}$	650		V
Gate-Source Voltage	$V_{GS}$	$\pm 30$		V
Continuous Drain Current  $T_C = 25 \text{ }^\circ\text{C}$	$I_D$	9.5	9.5 *	A
		5.83	5.83 *	A
Pulsed Drain Current (Note 1)	$I_{DM}$	38	38*	A
Single Pulse Avalanche Energy (Note 2)	$E_{AS}$	293		mJ
Repetitive Avalanche Current (Note 1)	$I_{AR}$	9.5		A
Repetitive Avalanche Energy (Note 1)	$E_{AR}$	19.8		mJ
Power Dissipation  $T_C = 25 \text{ }^\circ\text{C}$	$P_D$	198	52	W
		1.58	0.41	W/ $^\circ\text{C}$
Peak Diode Recovery dv/dt (Note 3)	dv/dt	4.5		V/ns
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55~150		$^\circ\text{C}$
Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	$T_L$	300		$^\circ\text{C}$

\* Limited only by maximum junction temperature

## Thermal Characteristics

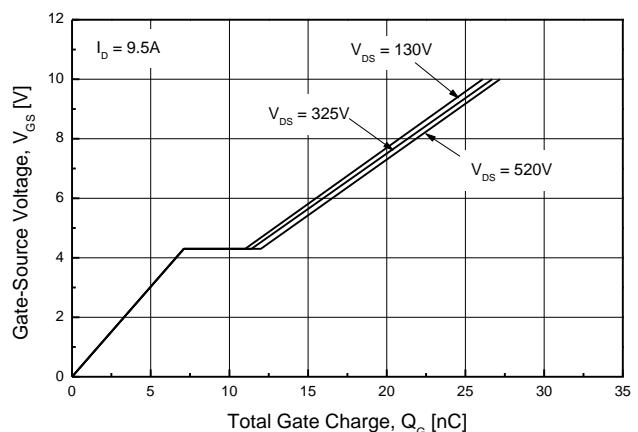
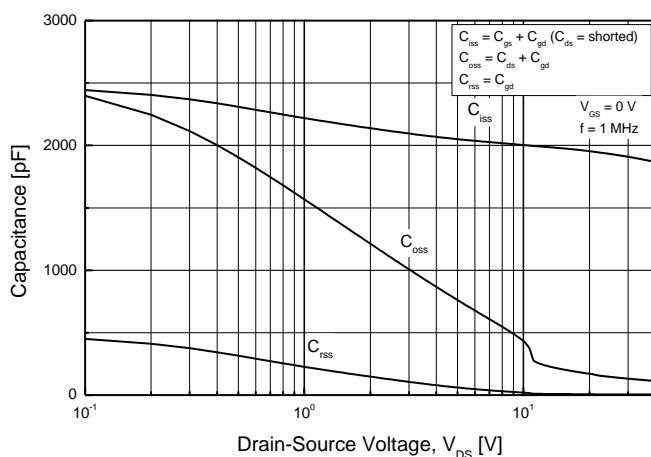
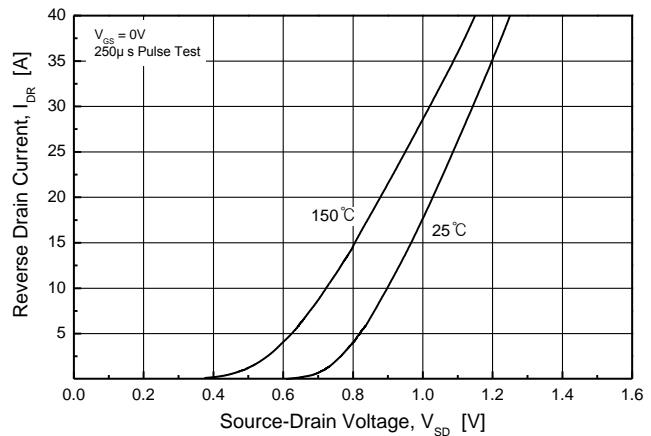
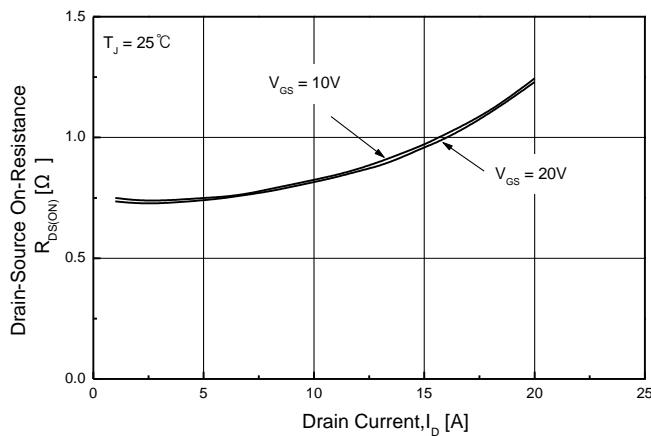
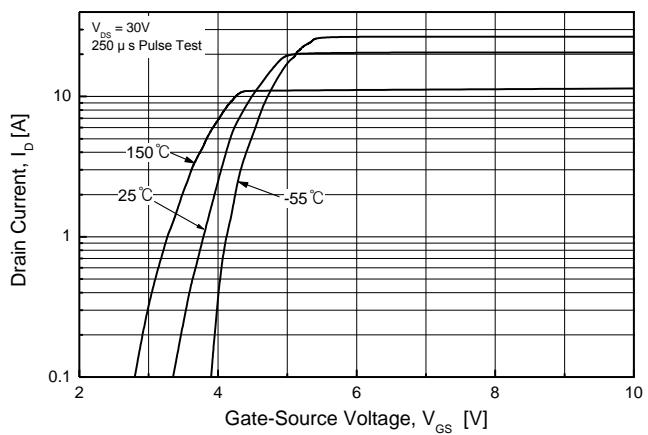
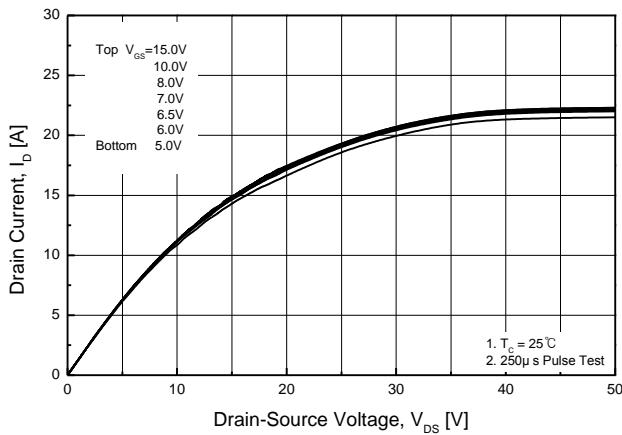
Parameter	Symbol	TMP10N65(G)	TMPF10N65(G)	Unit
Maximum Thermal resistance, Junction-to-Case	$R_{\theta JC}$	0.63	2.4	$^\circ\text{C/W}$
Maximum Thermal resistance, Junction-to-Ambient	$R_{\theta JA}$	62.5	62.5	$^\circ\text{C/W}$

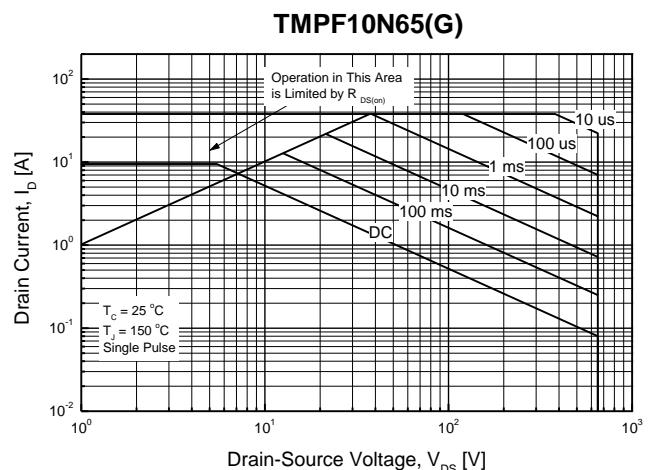
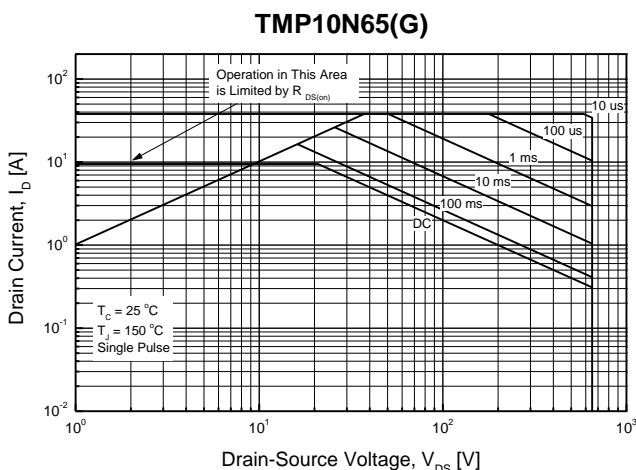
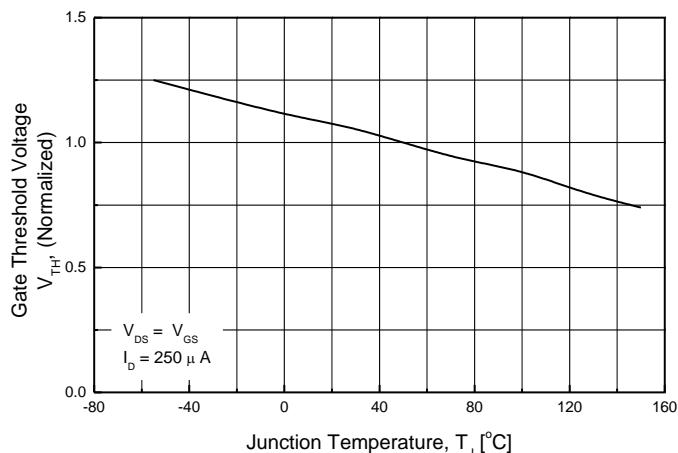
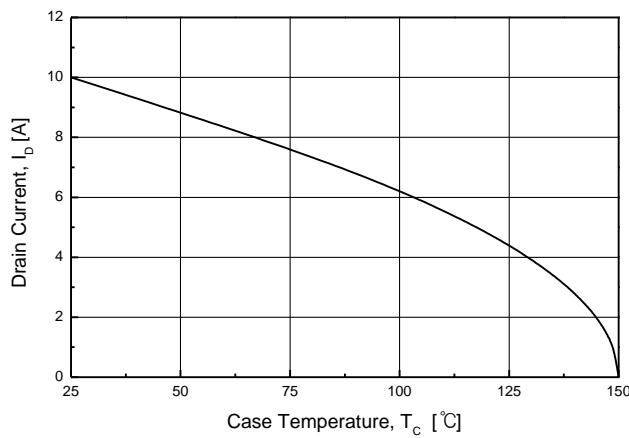
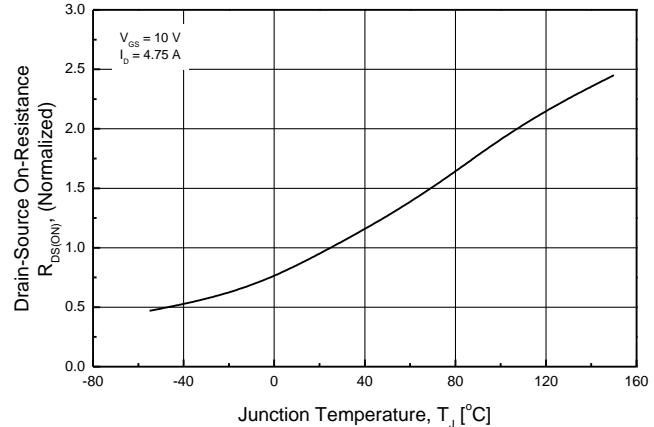
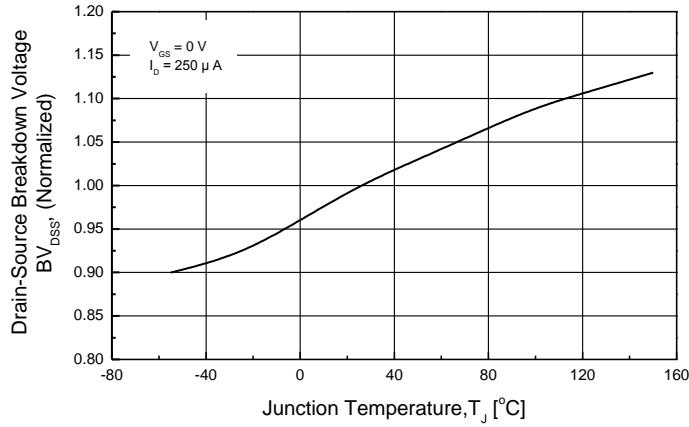
**Electrical Characteristics :  $T_c=25^\circ\text{C}$ , unless otherwise noted**

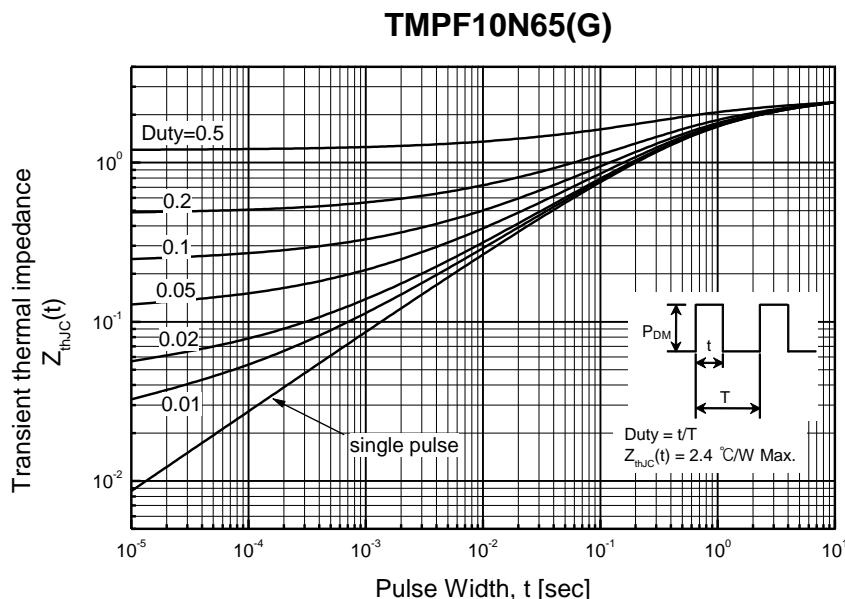
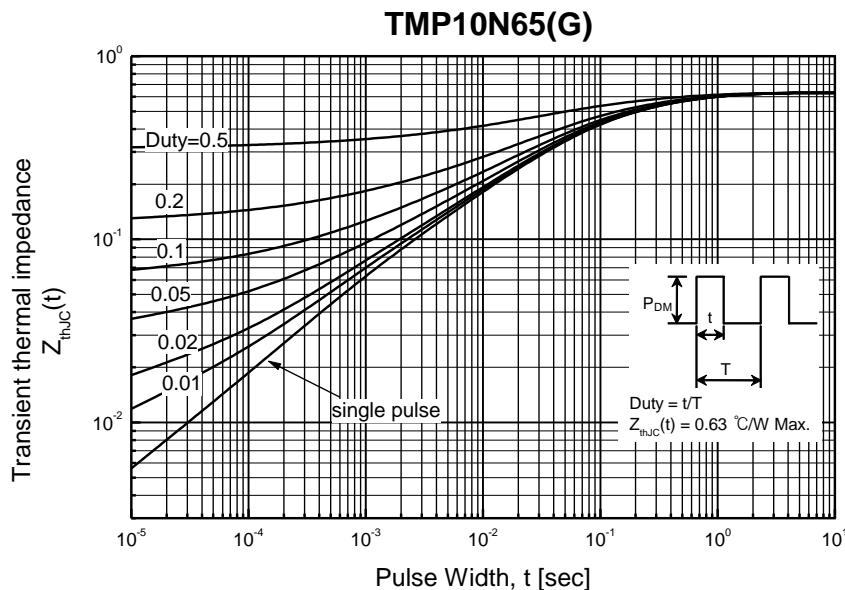
Parameter	Symbol	Test condition	Min	Typ	Max	Units
<b>OFF</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}} = 0 \text{ V}, I_{\text{D}} = 250 \mu\text{A}$	650	--	--	V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}} = 650 \text{ V}, V_{\text{GS}} = 0 \text{ V}$	--	--	1	$\mu\text{A}$
		$V_{\text{DS}} = 520 \text{ V}, T_c = 125^\circ\text{C}$	--	--	10	$\mu\text{A}$
Forward Gate-Source Leakage Current	$I_{\text{GSSF}}$	$V_{\text{GS}} = 30 \text{ V}, V_{\text{DS}} = 0 \text{ V}$	--	--	100	nA
Reverse Gate-Source Leakage Current	$I_{\text{GSSR}}$	$V_{\text{GS}} = -30 \text{ V}, V_{\text{DS}} = 0 \text{ V}$	--	--	-100	nA
<b>ON</b>						
Gate Threshold Voltage	$V_{\text{GS(th)}}$	$V_{\text{DS}} = V_{\text{GS}}, I_{\text{D}} = 250 \mu\text{A}$	2	--	4	V
Drain-Source On-Resistance	$R_{\text{DS(on)}}$	$V_{\text{GS}} = 10 \text{ V}, I_{\text{D}} = 4.75 \text{ A}$	--	0.76	0.98	$\Omega$
Forward Transconductance <sup>(Note 4)</sup>	$g_{\text{FS}}$	$V_{\text{DS}} = 30 \text{ V}, I_{\text{D}} = 4.75 \text{ A}$	--	13	--	S
<b>DYNAMIC</b>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{DS}} = 25 \text{ V}, V_{\text{GS}} = 0 \text{ V}, f = 1.0 \text{ MHz}$	--	2076	--	pF
Output Capacitance	$C_{\text{oss}}$		--	203	--	pF
Reverse Transfer Capacitance	$C_{\text{rss}}$		--	14	--	pF
<b>SWITCHING</b>						
Turn-On Delay Time <sup>(Note 4,5)</sup>	$t_{\text{d(on)}}$	$V_{\text{DD}} = 325 \text{ V}, I_{\text{D}} = 9.5 \text{ A}, R_{\text{G}} = 25 \Omega$	--	60	--	ns
Turn-On Rise Time <sup>(Note 4,5)</sup>	$t_r$		--	33	--	ns
Turn-Off Delay Time <sup>(Note 4,5)</sup>	$t_{\text{d(off)}}$		--	144	--	ns
Turn-Off Fall Time <sup>(Note 4,5)</sup>	$t_f$		--	50	--	ns
Total Gate Charge <sup>(Note 4,5)</sup>	$Q_g$	$V_{\text{DS}} = 520 \text{ V}, I_{\text{D}} = 9.5 \text{ A}, V_{\text{GS}} = 10 \text{ V}$	--	28	--	nC
Gate-Source Charge <sup>(Note 4,5)</sup>	$Q_{\text{gs}}$		--	7.1	--	nC
Gate-Drain Charge <sup>(Note 4,5)</sup>	$Q_{\text{gd}}$		--	4.6	--	nC
<b>SOURCE DRAIN DIODE</b>						
Maximum Continuous Drain-Source Diode Forward Current	$I_s$	----	--	--	9.5	A
Maximum Pulsed Drain-Source Diode Forward Current	$I_{\text{SM}}$	----	--	--	38	A
Drain-Source Diode Forward Voltage	$V_{\text{SD}}$	$V_{\text{GS}} = 0 \text{ V}, I_s = 9.5 \text{ A}$	--	--	1.5	V
Reverse Recovery Time <sup>(Note 4)</sup>	$t_{\text{rr}}$	$V_{\text{GS}} = 0 \text{ V}, I_s = 9.5 \text{ A}$ $dI_F / dt = 100 \text{ A}/\mu\text{s}$	--	306	--	ns
Reverse Recovery Charge <sup>(Note 4)</sup>	$Q_{\text{rr}}$		--	2.7	--	$\mu\text{C}$

Note :

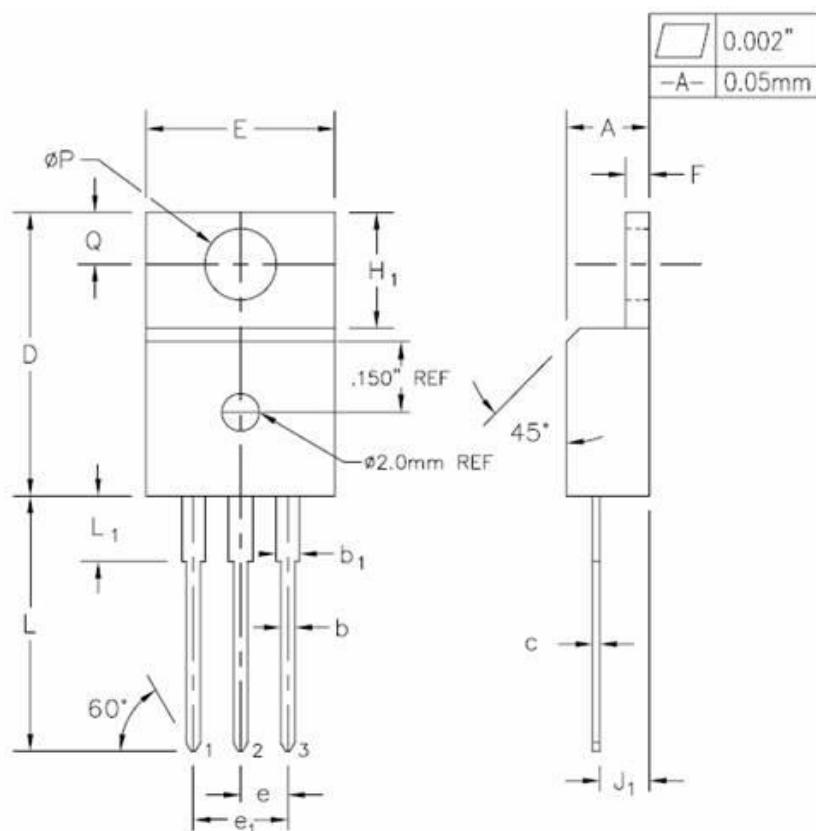
1. Repeated rating : Pulse width limited by safe operating area
2.  $L=6\text{mH}, I_{AS} = 9.5\text{A}, V_{DD} = 50\text{V}, R_G = 25\Omega$ , Starting  $T_j = 25^\circ\text{C}$
3.  $I_{SD} \leq 9.5\text{A}, dI/dt \leq 200\text{A}/\mu\text{s}, V_{DD} \leq \text{BV}_{\text{DS}}$ , Starting  $T_j = 25^\circ\text{C}$
4. Pulse Test : Pulse width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 2\%$
5. Essentially Independent of Operating Temperature Typical Characteristics





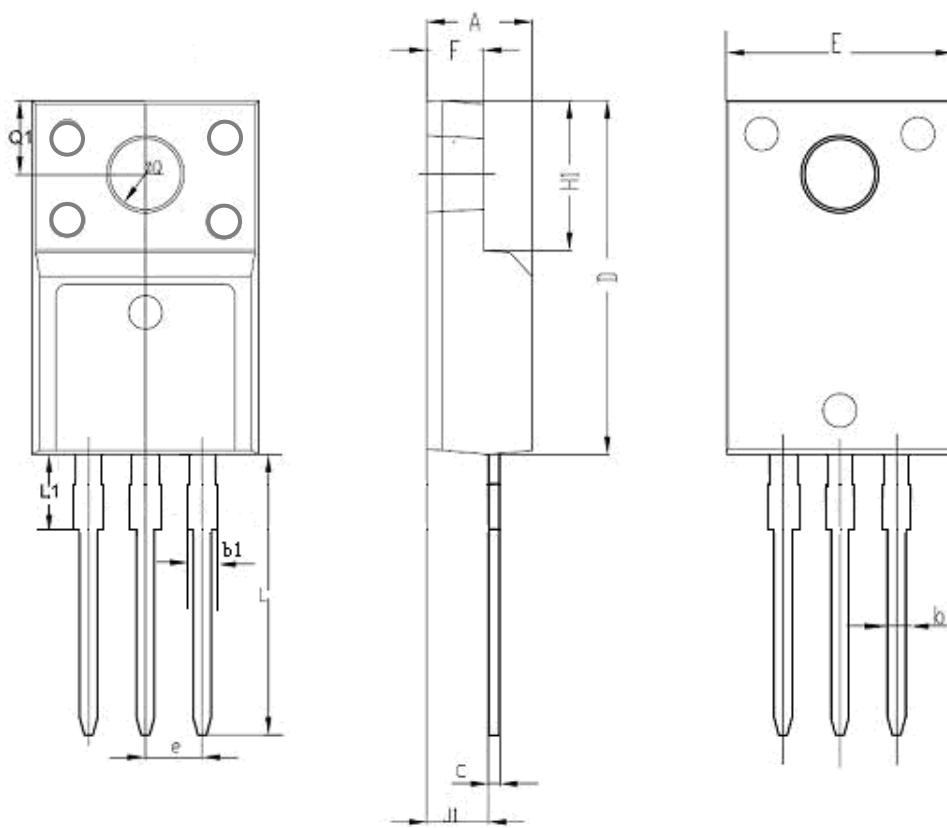


## TO-220AB-3L MECHANICAL DATA



SYMBOL	INCHES		MILLIMETERS		NOTES
	MIN.	MAX.	MIN.	MAX.	
A	0.170	0.180	4.32	4.57	
b	0.028	0.036	0.71	0.91	
b <sub>1</sub>	0.045	0.055	1.15	1.39	
c	0.014	0.021	0.36	0.53	
D	0.590	0.610	14.99	15.49	
E	0.395	0.410	10.04	10.41	
e	0.100	TYP.	2.54	TYP.	
e <sub>1</sub>	0.200	BSC	5.08	BSC	
F	0.048	0.054	1.22	1.37	
H <sub>1</sub>	0.235	0.255	5.97	6.47	
J <sub>1</sub>	0.100	0.110	2.54	2.79	
L	0.530	0.550	13.47	13.97	
L <sub>1</sub>	0.130	0.150	3.31	3.81	2
ØP	0.149	0.153	3.79	3.88	
Q	0.102	0.112	2.60	2.84	

**TO-220F-3L MECHANICAL DATA**



SYMBOL	INCHES		MILLIMETERS		NOTES
	MIN	MAX	MIN	MAX	
A	0.178	0.194	4.53	4.93	
b	0.028	0.036	0.71	0.91	
C	0.018	0.024	0.45	0.60	
D	0.617	0.633	15.67	16.07	
E	0.392	0.408	9.96	10.36	
e	0.100 TYP.		2.54TYP.		
H1	0.256	0.272	6.50	6.90	
J1	0.101	0.117	2.56	2.96	
L	0.503	0.519	12.78	13.18	
φQ	0.117	0.133	2.98	3.38	
b1	0.045	0.055	1.15	1.39	
L1	0.114	0.130	2.9	3.3	
Q1	0.122	0.138	3.10	3.50	
F	0.092	0.108	2.34	2.74	