TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSV)

# 2SK3417

#### **Switching Regulator Applications**

- Reverse-recovery time:  $t_{rr} = 60 \text{ ns (typ.)}$
- Built-in high-speed flywheel diode
- Low drain-source ON resistance:  $RDS(ON) = 1.6 \Omega \text{ (typ.)}$
- High forward transfer admittance:  $|Y_{fs}| = 4.0 \text{ S (typ.)}$
- Low leakage current:  $I_{DSS} = 100 \,\mu\text{A} \,(\text{max}) \,(V_{DS} = 500 \,\text{V})$
- Enhancement mode:  $V_{th}$  = 2.0 to 4.0 V ( $V_{DS}$  = 10 V,  $I_{D}$  = 1 mA)

### **Absolute Maximum Ratings (Ta = 25°C)**

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		$V_{DSS}$	500	V	
Drain-gate voltage ( $R_{GS} = 20 \text{ k}\Omega$ )		$V_{DGR}$	500	V	
Gate-source voltage		$V_{GSS}$	±30	V	
Drain current	DC (Note 1)	I <sub>D</sub>	5	^	
	Pulse (Note 1)	I <sub>DP</sub>	20	Α	
Drain power dissipation (Tc = 25°C)		P <sub>D</sub>	50	W	
Single pulse avalanche energy (Note 2)		E <sub>AR</sub>	180	mJ	
Avalanche current		I <sub>AR</sub>	5	Α	
Repetitive avalanche energy (Note 3)		E <sub>AR</sub>	5	mJ	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature range		T <sub>stg</sub>	-55 to 150	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

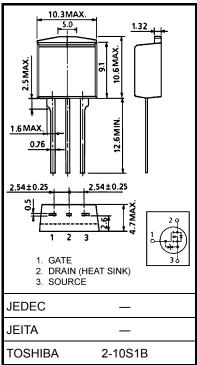
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	2.5	°C/W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	83.3	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

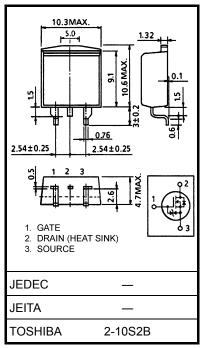
Note 2:  $V_{DD} = 90 \text{ V}$ ,  $T_{ch} = 25^{\circ}\text{C}$  (initial), L = 12.2 mH,  $R_G = 25 \Omega$ ,  $I_{AR} = 5 \text{ A}$ 

Note 3: Repetitive rating: pulse width limited by maximum channel temperature This transistor is an electrostatic-sensitive device. Please handle with caution.

Unit: mm



Weight: 1.5 g (typ.)



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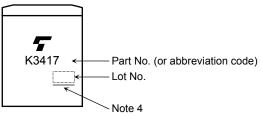
## **Electrical Characteristics (Ta = 25°C)**

Chara	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I <sub>GSS</sub>	$V_{GS} = \pm 25 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μА
Drain-source brea	akdown voltage	V (BR) GSS	$I_G = \pm 100 \ \mu A, \ V_{DS} = 0 \ V$	±30	_	_	V
Drain cut-OFF cu	rrent	I <sub>DSS</sub>	V <sub>DS</sub> = 500 V, V <sub>GS</sub> = 0 V	_	_	100	μА
Drain-source brea	akdown voltage	V (BR) DSS	$I_D = 10$ mA, $V_{GS} = 0$ V	500	_	_	V
Gate threshold vo	oltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.0	_	4.0	V
Drain-source ON	resistance	R <sub>DS</sub> (ON)	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 2.5 A	_	1.6	1.8	Ω
Forward transfer	admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 2.5 A	2.5	4.0	_	S
Input capacitance	9	C <sub>iss</sub>		_	780	_	
Reverse transfer capacitance		C <sub>rss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	60	_	pF
Output capacitance		C <sub>oss</sub>		_	200	_	
Switching time	Rise time	t <sub>r</sub>	$V_{GS}$ $V_{GS}$ $V_{DD}$	_	12	_	
	Turn-ON time	t <sub>on</sub>		_	25	_	- ns
	Fall time	t <sub>f</sub>		_	15	_	
	Turn-OFF time	t <sub>off</sub>		_	60	_	
Total gate charge (gate-source plus gate-drain)		Qg	$V_{DD} \simeq 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 5 \text{ A}$		17	_	nC
Gate-source charge		Q <sub>gs</sub>			11		
Gate-drain ("miller") charge		Q <sub>gd</sub>		_	6	_	

## **Source-Drain Ratings and Characteristics (Ta = 25°C)**

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	$I_{DR}$	_	_	_	5	Α
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_	_	20	Α
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 5 A, V <sub>GS</sub> = 0 V	_	_	-1.7	V
Reverse recovery time	t <sub>rr</sub>	$I_{DR} = 5 \text{ A}, V_{GS} = 0 \text{ V},$	_	60	_	ns
Reverse recovery charge	Q <sub>rr</sub>	dI <sub>DR</sub> /dt = 100 A/μs	_	0.1	_	μС

## Marking

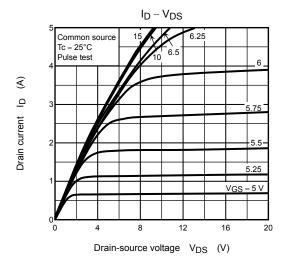


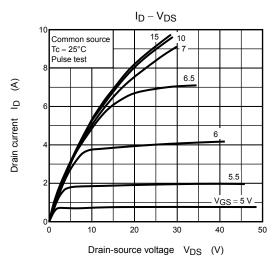
Note 4: A line under a Lot No. identifies the indication of product Labels.

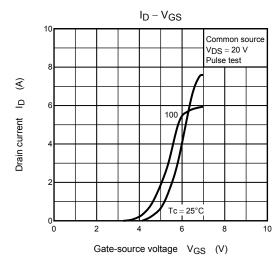
Not underlined: [[Pb]]/INCLUDES > MCV

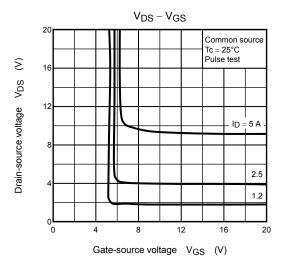
Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

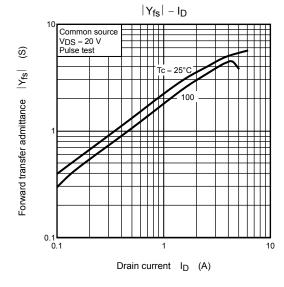
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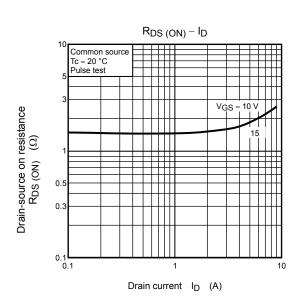


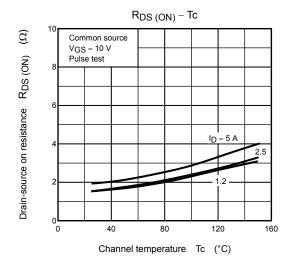


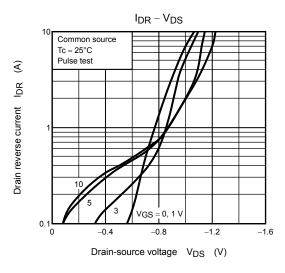


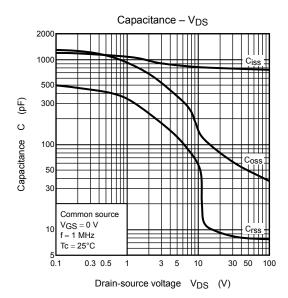


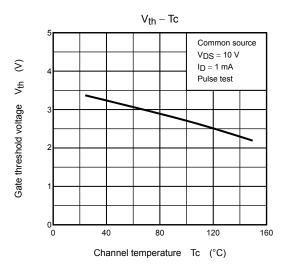


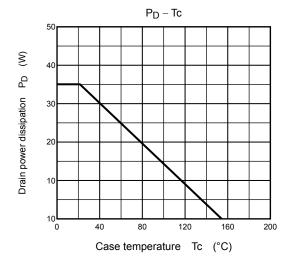


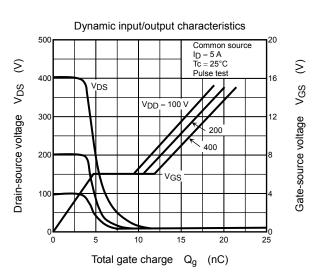




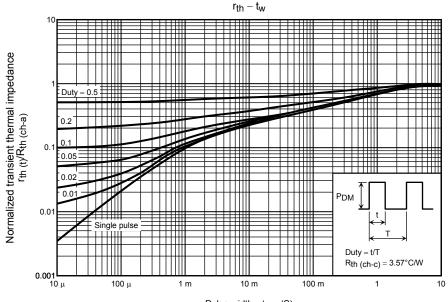




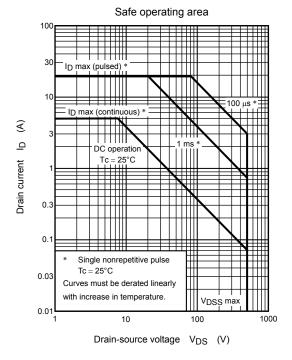


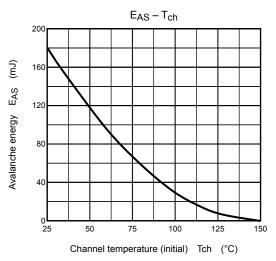


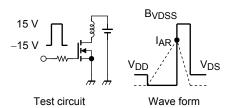
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$$R_G = 25 \Omega$$

$$V_{DD} = 90 \text{ V, L} = 12.2 \text{ mH}$$

$$E_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left( \frac{BVDSS}{BVDSS} - VDD \right)$$

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