TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π–MOSV)

# 2SK3126

#### **Switching Regulator Applications**

• Low drain-source ON resistance :  $R_{DS(ON)} = 0.48 \Omega$  (typ.)

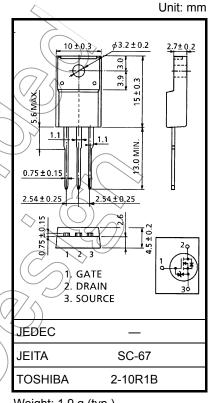
• High forward transfer admittance :  $|Y_{fs}| = 7.5 \text{ S (typ.)}$ 

Low leakage current :  $I_{DSS} = 100 \mu A (max) (V_{DS} = 450 V)$ 

Enhancement mode : V<sub>th</sub> = 2.4 to 3.4 V (V<sub>DS</sub> = 10 V, I<sub>D</sub> = 1 mA)

### **Absolute Maximum Ratings (Ta = 25°C)**

Characteris	stics	Symbol	Rating	Unit	
Drain-source voltage		$V_{DSS}$	450	$(\mathcal{N} \land$	
Drain-gate voltage (Ro	<sub>SS</sub> = 20 kΩ)	$V_{DGR}$	450	(V)	
Gate-source voltage		$V_{GSS}$	±30	V	
Drain current	DC (Note 1)	ΙD	10	A	
	Pulse (Note 1)	I <sub>DP</sub>	40	Α	
Drain power dissipation	n (Tc = 25°C)	$P_{D}$	40	W	
Single pulse avalanche	e energy (Note 2)	EAS	222	mJ	
Avalanche current		IAR	10	A	
Repetitive avalanche e	nergy (Note 3)	EAR	)) 4	mJ	
Channel temperature		Tch	150	∕ °C	
Storage temperature ra	ange	T <sub>stg</sub>	-55 to 150	7,¢	



Weight: 1.9 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

Characteristics	Symbol	Max	Unit
Thermal reverse, channel to case	Rth (ch-c)	3.125	°C / W
Thermal reverse, channel to ambient	Rth (ch-a)	62.5	°C / W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD} = 90 \text{ V}$ ,  $T_{ch} = 25^{\circ}\text{C}$  (initial), L = 3.7 mH,  $R_G = 25 \Omega$ ,  $I_{AR} = 10 \text{ A}$ 

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device.

Please handle with caution.

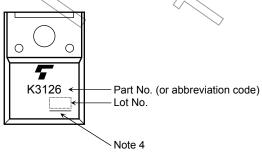
#### **Electrical Characteristics (Ta = 25°C)**

Charac	eteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	rrent	$I_{GSS}$	V <sub>GS</sub> = ±25 V, V <sub>DS</sub> = 0 V	_	_	±10	μΑ
Gate-source bre	eakdown voltage	V (BR) GSS	I <sub>G</sub> = ±10 μA, V <sub>DS</sub> = 0 V	±30	_	_	V
Drain cut-off cur	rrent	I <sub>DSS</sub>	V <sub>DS</sub> = 450 V, V <sub>GS</sub> = 0 V	\ <u></u>	_	100	μA
Drain-source br	eakdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	450		_	V
Gate threshold v	oltage	$V_{th}$	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.4	) >_	3.4	V
Drain-source OI	N resistance	R <sub>DS</sub> (ON)	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 5 A	<u> </u>	0.48	0.65	Ω
Forward transfer	admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 5 A	3.5	7.5	_	S
Input capacitano	e	C <sub>iss</sub>		)	1400	_	
Reverse transfer	r capacitance	C <sub>rss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz	^ —	240	_	pF
Output capacitar	nce	C <sub>oss</sub>		_	590		
Switching time Fa	Rise time	t <sub>r</sub>	$V_{GS} = \frac{10V}{0V}$ $V_{DD} = \frac{10V}{0}$ $V_{DD} = \frac{10V}{0}$	- (	35	/\rangle   \rangle   \rang	
	Turn-on time	t <sub>on</sub>			50	) –	20
	Fall time	t <sub>f</sub>		7	80	-	ns
	Turn-off time	t <sub>off</sub>	Duty $\leq 1\%$ , $t_{\rm W} = 10 \mu {\rm s}$		260	1	
Total gate charg plus gate-drain)		Qg			35	ı	
Gate-source cha	arge	Q <sub>gs</sub>	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, D = 10 \text{ A}$	_	19	_	nC
Gate-drain ("mil	ler") charge	Q <sub>gd</sub>			16	1	

## Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	lór		_	_	10	Α
Pulse drain reverse current (Note 1)	\ I <sub>DRP</sub>	_	_	_	40	Α
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 10 A, V <sub>GS</sub> = 0 V	_	_	-1.7	٧
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 10 A, V <sub>GS</sub> = 0 V	_	1400	_	ns
Reverse recovery charge	Qrr	dl <sub>DR</sub> / dt = 100 A / μs	-	14	_	μC





Note 4: A line under a Lot No. identifies the indication of product Labels.

Not underlined: [[Pb]]/INCLUDES > MCV Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

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