TOSHIBA Diode Silicon Epitaxial Schottky Barrier Type

1SS421

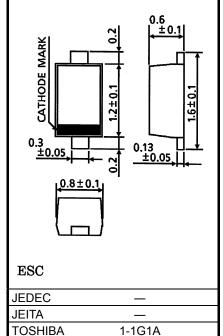
High-Speed Switching Application

Low forward voltage: $V_{F(3)} = 0.50V$ (max)

Abusolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Reverse voltage	VR	30	V
Maximum (peak) forward current	I _{FM}	300	mA
Average forward current	Ι _Ο	200	mA
Surge current (10ms)	I _{FSM}	1	А
Power dissipation	P *	150	mW
Junction temperature	Тј	125	°C
Storage temperature range	T _{stg}	-55 to 125	°C
Operating temperature range	T _{opr}	-40 to 100	°C

*: Mounted on a glass epoxy circuit board of 20 mm × 20 mm, Cu pad dimension of 4 mm × 4 mm.



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual

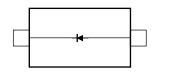
reliability data (i.e. reliability test report and estimated failure rate, etc).

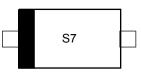
Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit	
Forward voltage	V _{F (1)}	_	I _F = 1 mA		0.18	—		
	V _{F (2)}	—	I _F = 5 mA		0.22	_	V	
	V _{F (3)}	_	I _F = 200 mA	-	0.44	0.5		
Reverse current	I _{R (1)}	_	V _R = 10 V	-		20	μA	
	I _{R (2)}	_	V _R = 30 V		-	30		
Total capacitance	CT	—	V _R = 0, f = 1 MHz		19		pF	

Equivalent Circuit (Top View)

Marking

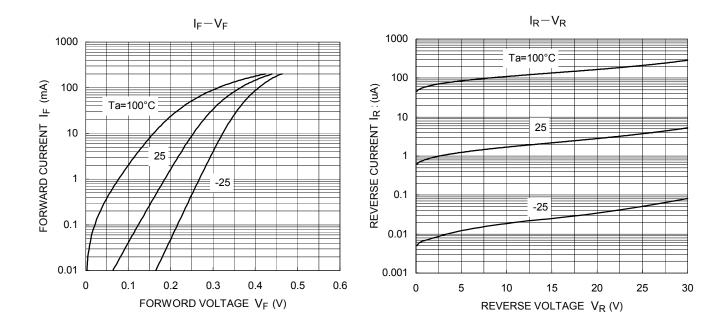




Unit: mm

Weight: 1.4 mg(typ.)

TOSHIBA



RESTRICTIONS ON PRODUCT USE

20070701-EN

• The information contained herein is subject to change without notice.

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In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the "Handling Guide for Semiconductor Devices," or "TOSHIBA Semiconductor Reliability Handbook" etc.

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