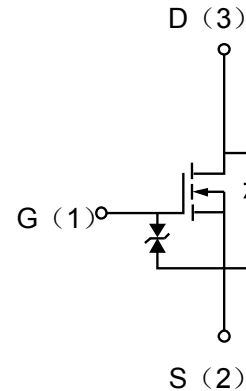


Description

PNM723T703E0-2 is designed for high speed switching applications
The enhancement mode MOS is extremely high density cell and low on-resistance.

MOSFET Product Summary			
$V_{DS}(V)$	$R_{DS(on)}(\Omega)$	$V_{GS(th)}(V)$	$I_D(A)$
40	7.5@ $V_{GS}=10V$	0.5 to 1.5	0.18



Electrical characteristics per line@25°C (unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	V_{DSS}	$I_D = 10\mu A, V_{GS} = 0V$	40	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 40V, V_{GS} = 0V$	-	-	0.5	μA
Gate-Body Leakage Current	I_{GSS}	$V_{DS} = 0V, V_{GS} = \pm 20V$	-	-	± 1	μA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	0.5	-	1.5	V
Static Drain-Source On-Resistance	$R_{DS(ON)}$	$V_{GS} = 5V, I_D = 0.05A$	-	-	7.5	Ω
		$V_{GS} = 10V, I_D = 0.5A$	-	-	7.5	Ω
DYNAMIC PARAMETERS						
Input Capacitance	C_{ISS}	$V_{GS} = 0V, V_{DS} = 25V,$ $f = 1MHz$	-	-	40	pF
Output Capacitance	C_{DSS}		-	-	20	pF
Reverse Transfer Capacitance	C_{RSS}		-	-	5	pF
SWITCHING PARAMETERS						
Turn-On Delay Time	$t_{d(on)}$	$V_{DS} = 30V, V_{GS} = 10V,$ $R_G = 25\Omega, R_L = 150\Omega$ $I_D = 0.2A$	-	-	20	ns
Turn-Off Delay Time	$t_{d(off)}$		-	-	20	ns

Absolute maximum rating@25°C

Rating		Symbol	Value	Units
Drain-Source Voltage		V_{DS}	40	V
Gate-Source Voltage		V_{GS}	± 20	V
Drain Current	Continuous	I_D	0.18	A
	Pulsed	I_D	0.36	A
Total Power Dissipation		P_D	150	mW

Typical Characteristics

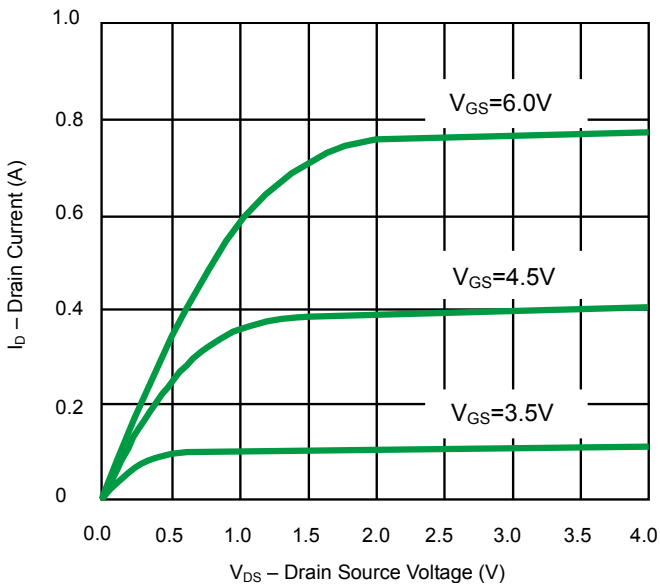


Fig 1. Output Characteristics

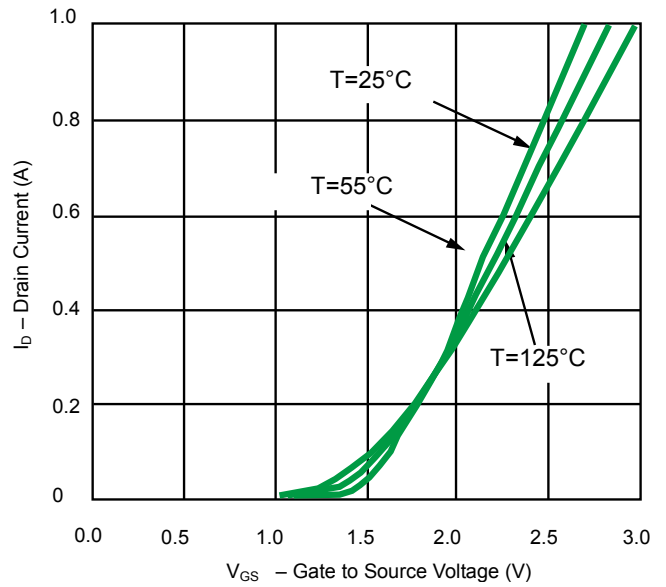


Fig 2. Transfer Characteristics

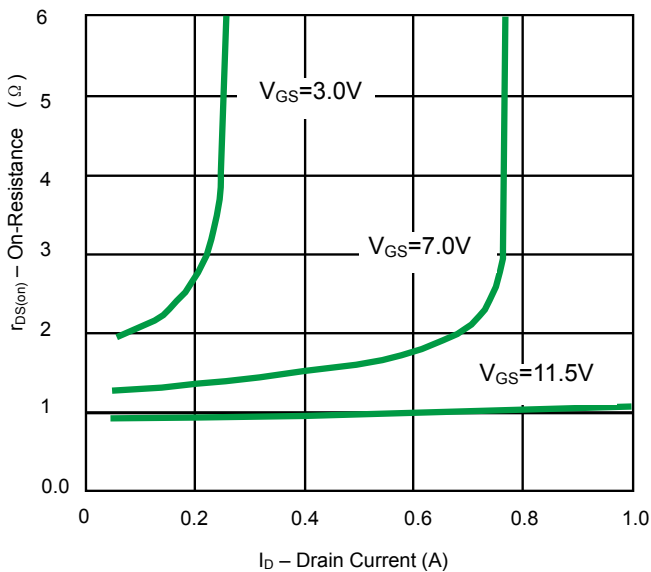


Fig 3. On-Resistance vs. Drain Current

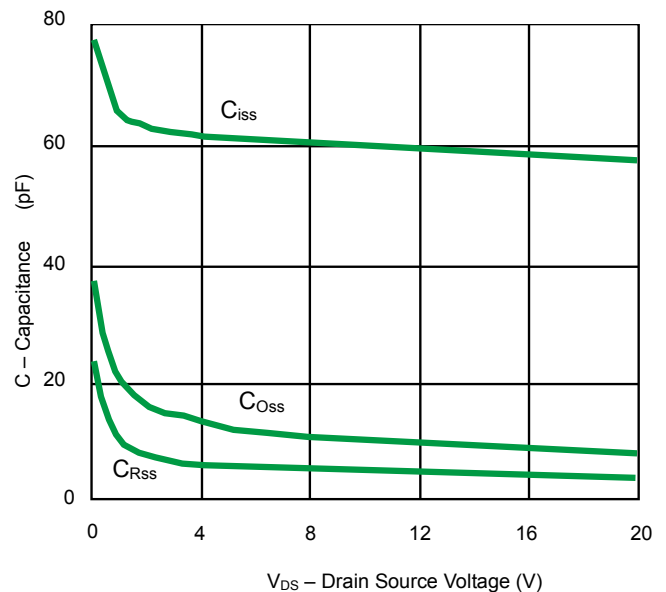
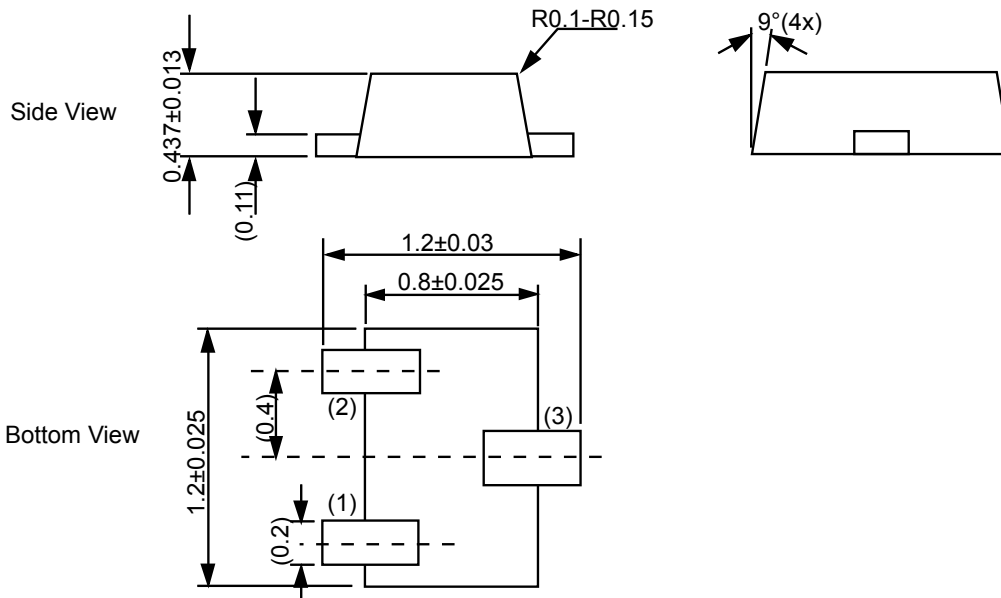
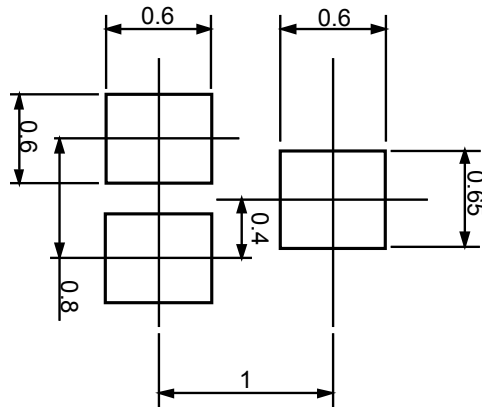


Fig 4. Capacitance

Product dimension (SOT-723)



Unit: mm




Unit: mm

Ordering information

Device	Package	Shipping
PNM723T703E0-2	SOT-723 (Pb-Free)	10000 / Tape & Reel


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