SUN0260F

New Generation N-Ch Power MOSFET

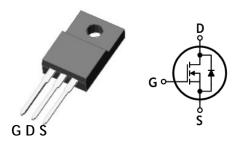
HIGH SPEED SWITCHING APPLICATION

Features

- Low drain-source On resistance: $R_{DS(on)}=3.9\Omega$ (Typ.)
- Low gate charge: Q_g=6.5nC (Typ.)
- Low reverse transfer capacitance: C_{rss}=4.4pF (Typ.)
- RoHS compliant device
- 100% avalanche tested

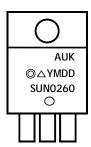
Ordering Information

Part Number	Marking	Package
SUN0260F	SUN0260	TO-220F-3L



TO-220F-3L

Marking Information



Column 1: Manufacturer

Column 2: Production Information

e.g.) ⊚△YMDD

-. ©: Option Code (H: Halogen Free)

-. △: Factory Management Code

-. YMDD: Date Code (Year, Month, Date)

Column 3: Device Code

Absolute maximum ratings (T_C=25°C unless otherwise noted)

Characteristic	Symbol		Rating	Unit	
Drain-source voltage	V _{DSS}		600	٧	
Gate-source voltage		V _{GSS} ±30		٧	
Drain current (DC)*	I _D	T _c =25°C	2	Α	
		T _c =100°C	1.26	А	
Drain current (Pulsed) *		I _{DM}	8	Α	
Single avalanche energy (Note 2)		E _{AS}	130	mJ	
Repetitive avalanche current (Note 1)		I _{AR}	2	Α	
Repetitive avalanche energy (Note 1)		E _{AR}	3.2	mJ	
Power dissipation		P _D	32	W	
Junction temperature		TJ	150	°C	
Storage temperature range		T_{stg}	-55~150	°C	

^{*} Limited only maximum junction temperature

Rev. date: 03-DEC-13 KSD-T00134-000 www.auk.co.kr

Thermal Characteristics

Characteristic	Symbol	Rating	Unit
Thermal resistance, junction to case	$R_{th(j-c)}$	Max. 3.9	°C/W
Thermal resistance, junction to ambient	$R_{th(j\text{-}a)}$	Max. 62.5	C/ W

Electrical Characteristics (T_C=25°C unless otherwise noted)

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Drain-source breakdown voltage	BV _{DSS}	I _D =250uA, V _{GS} =0	600	-	-	٧
Gate threshold voltage	$V_{GS(th)}$	I _D =250uA, V _{DS} =V _{GS}	3	-	5	٧
Drain-source cut-off current		V _{DS} =600V, V _{GS} =0V	-	-	1	uA
	I _{DSS}	V _{DS} =600V, T _c =150°C	-	-	100	uA
Gate leakage current	I _{GSS}	V_{DS} =0V, V_{GS} =±30V	-	-	±100	nA
Drain-source on-resistance	R _{DS(ON)}	V _{GS} =10V, I _D =1A	-	3.9	4.7	Ω
Forward transfer conductance (Note 3)	g _{fs}	V _{DS} =10V, I _D =1A	-	2.5	-	S
Input capacitance	C _{iss}	V _{DS} =25V, V _{GS} =0V, f=1.0MHz	-	417	-	pF
Output capacitance	C _{oss}		-	36	-	
Reverse transfer capacitance	C _{rss}		-	4.4	-	
Turn-on delay time (Note 3,4)	t _{d(on)}	V_{DS} =300V, I_{D} =2A, R_{G} =25 Ω	-	30	-	ns
Rise time (Note 3,4)	t _r		-	25	-	
Turn-off delay time (Note 3,4)	t _{d(off)}		-	42	-	
Fall time (Note 3,4)	t _f		-	19	-	
Total gate charge (Note 3,4)	Qg	V_{DS} =480V, V_{GS} =10V, I_{D} =2A	-	6.5	9	
Gate-source charge (Note 3,4)	Q_{gs}		-	3	-	nC
Gate-drain charge (Note 3,4)	Q_{gd}		-	1.5	-	

Source-Drain Diode Ratings and Characteristics (T_c=25°C unless otherwise noted)

oran de Brann Brode Mathige and endracter lettes (1, 25 c amoss entermise hetea)							
Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit	
Source current (DC)	Is	Integral reverse diode in the MOSFET	-	-	2	Α	
Source current (Pulsed)	I _{SM}		-	-	8	Α	
Forward voltage	V_{SD}	$V_{GS}=0V$, $I_{SD}=2A$	-	-	1.4	٧	
Reverse recovery time (Note 3,4)	t _{rr}	I _{SD} =2A, V _{GS} =0V dI _F /dt=100A/us	-	230	-	ns	
Reverse recovery charge (Note 3,4)	Q _{rr}		-	1	-	uC	

- 1. Repeated rating: Pulse width limited by safe operating area
- 2. L=59.5mH, I_{AS} =2A, V_{DD} =50V, R_G =25 Ω , Starting T_J =25°C 3. Pulse test: Pulse width \leq 300us, Duty cycle \leq 2%
- 4. Essentially independent of operating temperature typical characteristics

Typical Electrical Characteristics Curves

Fig. 1 Typical Output Characteristics

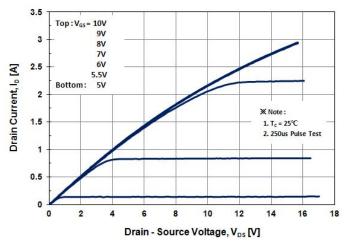


Fig. 2 Typical Output Characteristics

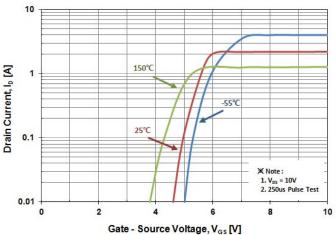


Fig.3 On-Resistance Variation with Drain Current and Gate Voltage

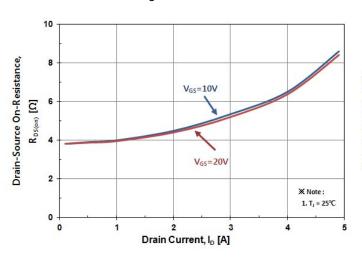


Fig. 4 Body Diode Forward Voltage Variation with Source Current

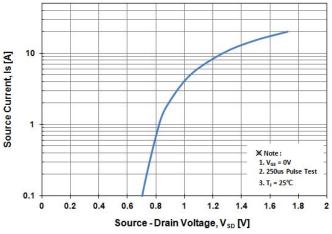


Fig. 5 Typical Capacitance Characteristics

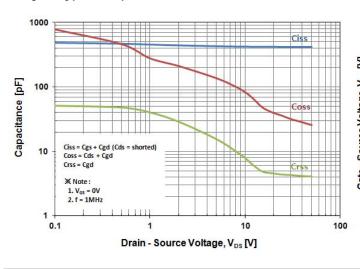
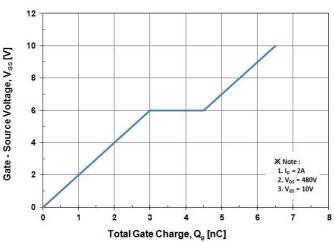


Fig. 6 Typical Total Gate Charge Characteristics



SUN0260F

Fig. 7 Breakdown Voltage Variation vs. Temperature

Fig. 8 On-Resistance Variation vs. Temperature

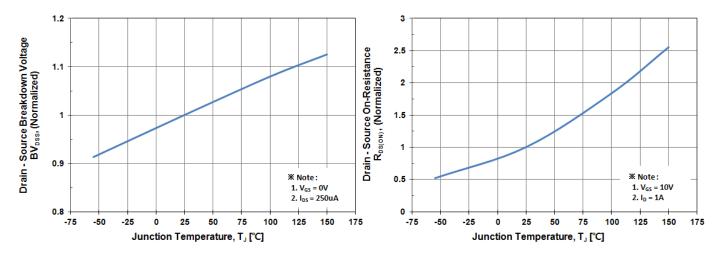


Fig. 9 Maximum Drain Current vs. Case Temperature

Fig. 10 Maximum Safe Operating Area

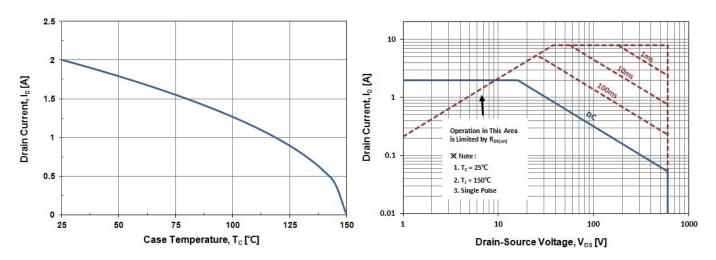
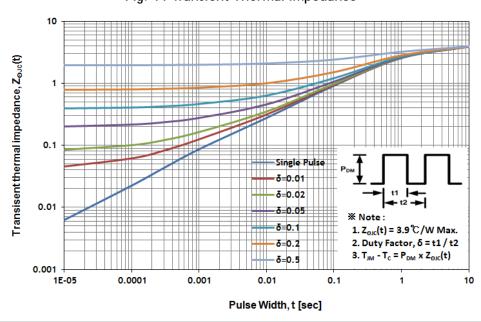
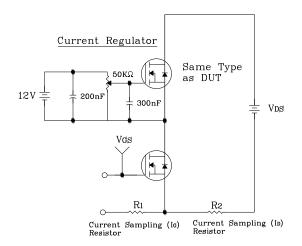


Fig. 11 Transient Thermal Impedance



Rev. date: 03-DEC-13 KSD-T00134-000 www.auk.co.kr

Fig. 12 Gate Charge Test Circuit & Waveform



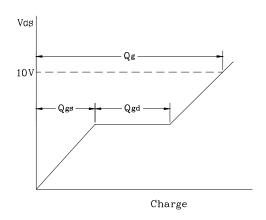
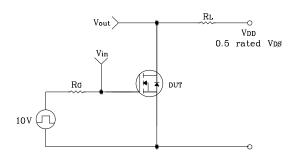


Fig. 13 Resistive Switching Test Circuit & Waveform



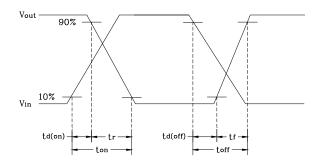
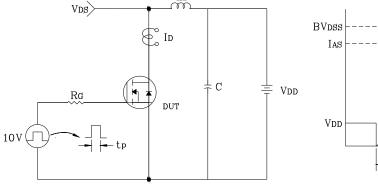


Fig. 14 E_{AS} Test Circuit & Waveform



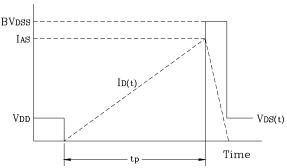
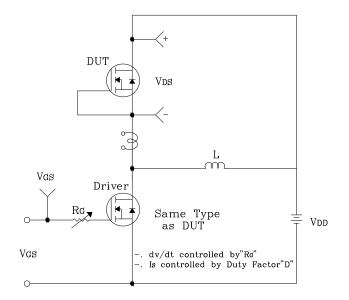
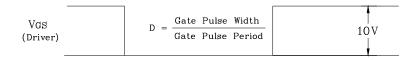
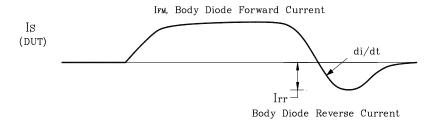
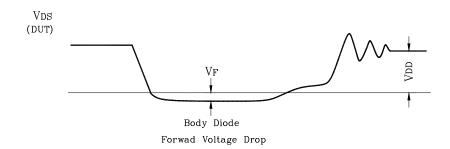


Fig. 15 Diode Reverse Recovery Time Test Circuit & Waveform

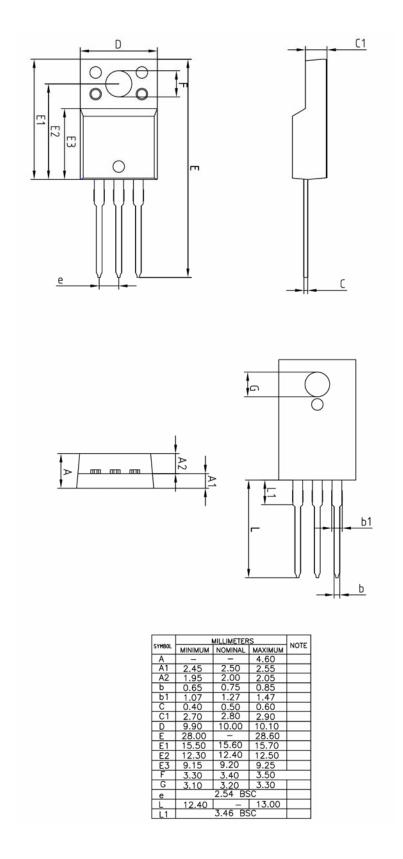








Package Outline Dimensions



SUN0260F

The AUK Corp. products are intended for the use as components in general electronic equipment (Office and communication equipment, measuring equipment, home appliance, etc.).

Please make sure that you consult with us before you use these AUK Corp. products in equipments which require high quality and / or reliability, and in equipments which could have major impact to the welfare of human life(atomic energy control, airplane, spaceship, transportation, combustion control, all types of safety device, etc.). AUK Corp. cannot accept liability to any damage which may occur in case these AUK Corp. products were used in the mentioned equipments without prior consultation with AUK Corp..

Specifications mentioned in this publication are subject to change without notice.