



Power MOSFET

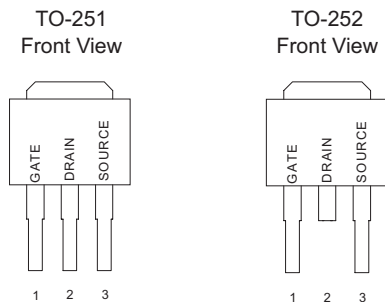
Features

- Robust High Voltage Termination
- Avalanche Energy Specified
- Source-to-Drain Diode Recovery Time Comparable to a Discrete Fast Recovery
- Diode is Characterized for Use in Bridge Circuits
- I_{BSS} and $V_{DS(on)}$ Specified at Elevated Temperature

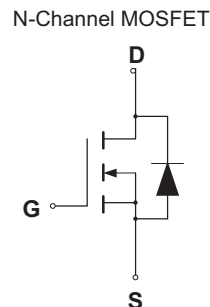
General Description

This high voltage MOSFET uses an advanced termination scheme to provide enhanced voltage-blocking capability without degrading performance over time. In addition, this advanced MOSFET is designed to withstand high energy in avalanche and commutation modes. The new energy efficient design also offers a drain-to-source diode with a fast recovery time. Designed for high voltage, high speed switching applications in power supplies, converters and PWM motor controls, these devices are particularly well suited for bridge circuits where diode speed and commutating safe operating areas are critical and offer additional and safety margin against unexpected voltage transients.

Pin Configuration



Symbol



Absolute Maximum Ratings

Rating	Symbol	Value	Unit
Drain Current – Continuous – Pulsed	I_D I_{DM}	1.0 9.0	A
Gate-to-Source Voltage – Continuous – Non-repetitive	V_{GS} V_{GSM}	± 30 ± 40	V
Total Power Dissipation TO-251/252	P_D	50	W
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to 150	°C
Single Pulse Drain-to-Source Avalanche Energy – $T_J = 25^\circ\text{C}$ ($V_{DD} = 100\text{V}, V_{GS} = 10\text{V}, I_L = 1\text{A}, L = 10\text{mH}, R_G = 25\Omega$)	EAS	20	mJ
Thermal Resistance – Junction to Case – Junction to Ambient	θ_{JC} θ_{JA}	1.0 62.5	°C/W
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds	T_L	260	°C



Ordering Information

Part Number	Package
CTM01N60N251	TO-251
CTM01N60N252	TO-252

Electrical Characteristics

Unless otherwise specified, $T_J = 25^\circ\text{C}$.

Characteristic	Symbol	CTM01N60			Units	
		Min	Typ	Max		
Drain-Source Breakdown Voltage ($V_{GS} = 0\text{ V}$, $I_D = 250\ \mu\text{A}$)	$V_{(BR)DSS}$	600			V	
Drain-Source Leakage Current ($V_{DS} = 600\text{ V}$, $V_{GS} = 0\text{ V}$) ($V_{DS} = 480\text{ V}$, $V_{GS} = 0\text{ V}$, $T_J = 125^\circ\text{C}$)	I_{DSS}			0.1 0.3	mA	
Gate-Source Leakage Current-Forward ($V_{GS} = 20\text{ V}$, $V_{DS} = 0\text{ V}$)	I_{GSSF}			100	nA	
Gate-Source Leakage Current-Reverse ($V_{GS} = -20\text{ V}$, $V_{DS} = 0\text{ V}$)	I_{GSSR}			-100	nA	
Gate Threshold Voltage ($V_{DS} = V_{GS}$, $I_D = 250\ \mu\text{A}$)	$V_{GS(th)}$	2.0		4.0	V	
Static Drain-Source On-Resistance ($V_{GS} = 10\text{ V}$, $I_D = 0.6\text{ A}$) *	$R_{DS(on)}$			8.0	Ω	
Forward Transconductance ($V_{DS} \geq 50\text{ V}$, $I_D = 0.5\text{ A}$) *	g_{FS}	0.5			S	
Input Capacitance	$(V_{DS} = 25\text{ V}$, $V_{GS} = 0\text{ V}$, $f = 1.0\text{ MHz}$)	C_{iss}		210	pF	
Output Capacitance		C_{oss}		28	pF	
Reverse Transfer Capacitance		C_{rss}		4.2	pF	
Turn-On Delay Time	$(V_{DD} = 300\text{ V}$, $I_D = 1.0\text{ A}$, $V_{GS} = 10\text{ V}$, $R_G = 18\ \Omega$) *	$t_{d(on)}$		8	ns	
Rise Time		t_r		21	ns	
Turn-Off Delay Time		$t_{d(off)}$		18	ns	
Fall Time		t_f		24	ns	
Total Gate Charge	$(V_{DS} = 400\text{ V}$, $I_D = 1.0\text{ A}$, $V_{GS} = 10\text{ V}$) *	Q_g		8.5	14	nC
Gate-Source Charge		Q_{gs}		1.8		nC
Gate-Drain Charge		Q_{gd}		4		nC
Internal Drain Inductance (Measured from the drain lead 0.25" from package to center of die)	L_D			4.5	nH	
Internal Drain Inductance (Measured from the source lead 0.25" from package to source bond pad)	L_S			7.5	nH	
SOURCE-DRAIN DIODE CHARACTERISTICS						
Forward On-Voltage	$(I_S = 1.0\text{ A}$, $V_{GS} = 0\text{ V}$, $d_iS/d_i = 100\text{ A}/\mu\text{s}$)	V_{SD}			1.5	V
Forward Turn-On Time		t_{on}		**		ns
Reverse Recovery Time		t_{rr}		350	500	ns

* Pulse Test: Pulse Width $\leq 300\ \mu\text{s}$, Duty Cycle $\leq 2\%$

** Negligible, Dominated by circuit inductance



Typical Electrical Characteristics

Figure 1. On-Region Characteristics

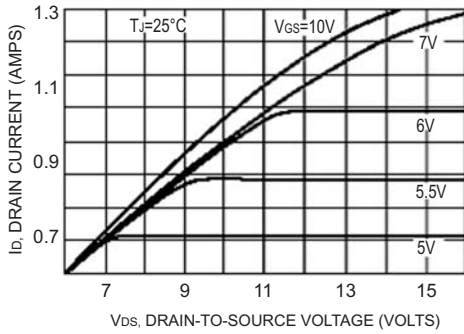


Figure 2. Transfer Characteristics

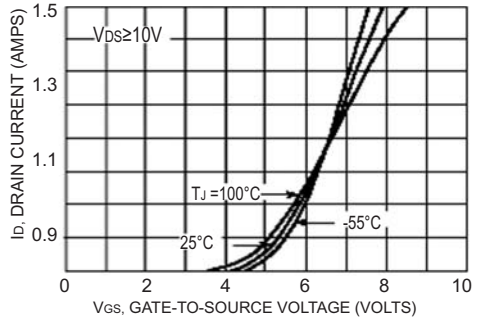


Figure 3. On-Resistance versus Drain Current and Temperature

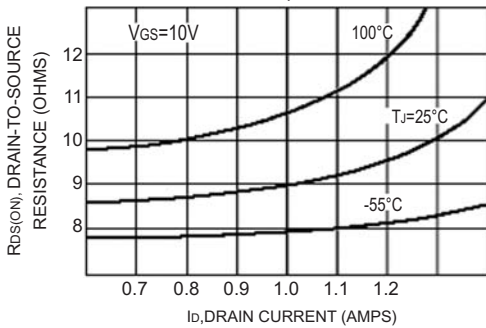


Figure 4. On-Resistance versus Drain Current and Gate Voltage

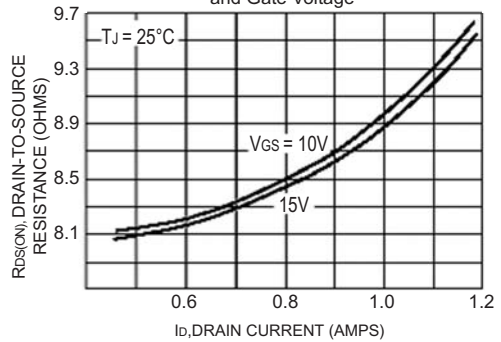




Figure 5. On-Resistance Variation with Temperature

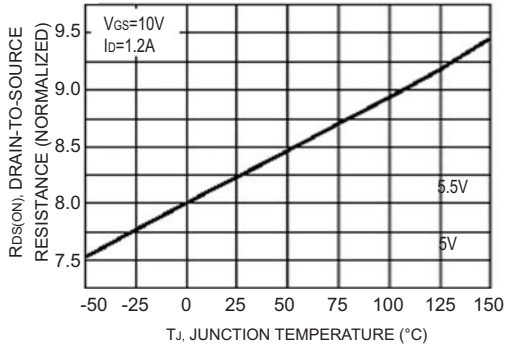
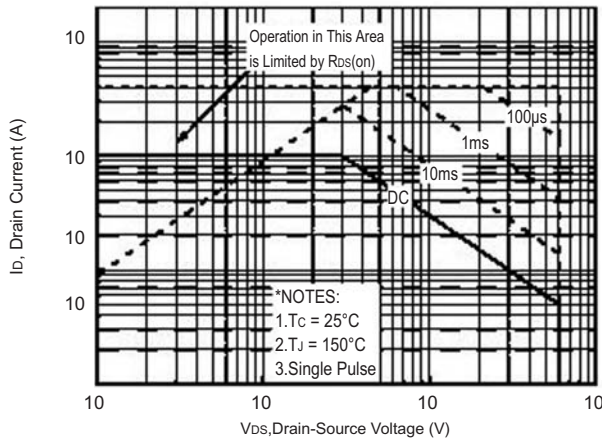
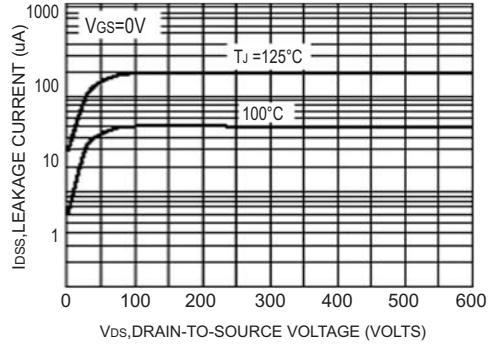


Figure 6. Drain-to-Source Leakage Current versus Voltage



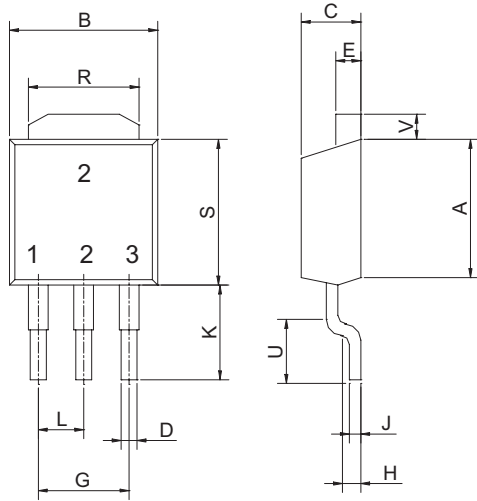
Maximum Safe Operatng Area

TO-251/TO-252



Package Dimension

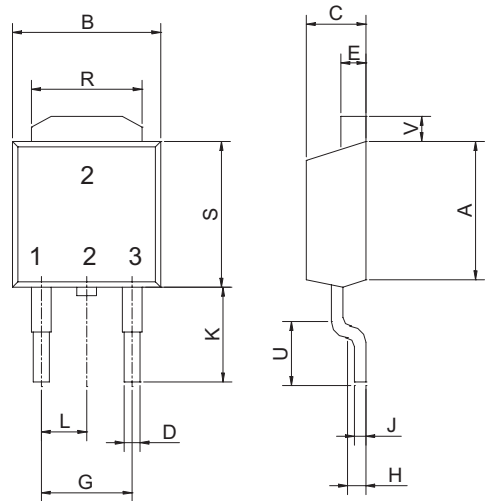
TO-251



PIN 1: GATE
 PIN 2: DRAIN
 PIN 3: SOURCE

SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHS		
	MIN	NOM	MAX	MIN	NOM	MAX
A	5.97	----	6.35	0.235	----	0.250
B	6.35	----	6.73	0.250	----	0.265
C	2.19	----	2.38	0.086	----	0.094
D	0.69	----	0.88	0.027	----	0.035
E	0.46	----	1.01	0.033	----	0.047
G	4.58BSC			0.180BSC		
H	0.87	----	1.01	0.034	----	0.040
J	0.46	----	0.58	0.018	----	0.023
K	2.60	----	2.89	0.102	----	0.114
L	2.29BSC			0.090BSC		
R	4.45	----	5.46	0.175	----	0.215
S	0.51	----	1.27	0.020	----	0.050
U	0.51	----		0.020	----	
V	0.77	----	1.27	0.030	----	0.050

TO-252



PIN 1: GATE
 PIN 2: DRAIN
 PIN 3: SOURCE

SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHS		
	MIN	NOM	MAX	MIN	NOM	MAX
A	5.97	----	6.35	0.235	----	0.250
B	6.35	----	6.73	0.250	----	0.265
C	2.19	----	2.38	0.086	----	0.094
D	0.69	----	0.88	0.027	----	0.035
E	0.46	----	1.01	0.033	----	0.047
G	4.58BSC			0.180BSC		
H	0.87	----	1.01	0.034	----	0.040
J	0.46	----	0.58	0.018	----	0.023
K	2.60	----	2.89	0.102	----	0.114
L	2.29BSC			0.090BSC		
R	4.45	----	5.46	0.175	----	0.215
S	0.51	----	1.27	0.020	----	0.050
U	0.51	----		0.020	----	
V	0.77	----	1.27	0.030	----	0.050