N-CHANNEL ENHANCEMENT MOS FET

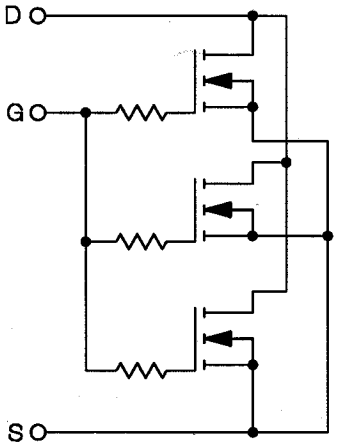
200V, 75A, 0.035Ω

SDF75NA20 GBN

FEATURES

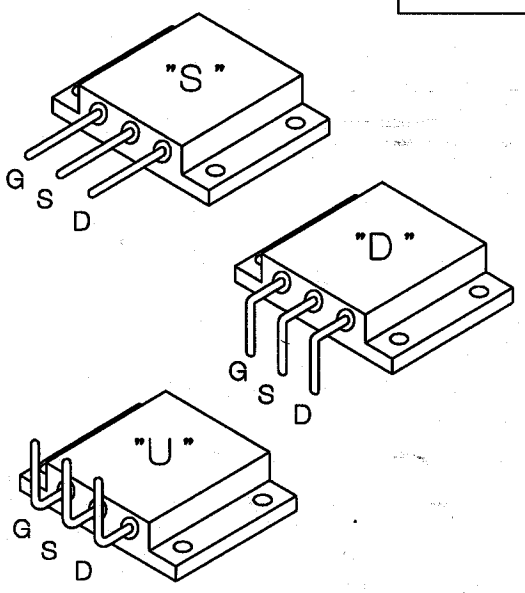
- RUGGED PACKAGE
- HI-REL CONSTRUCTION
- CERAMIC EYELETS
- LEAD BENDING OPTIONS
- COPPER CORED 52 ALLOY PINS
- LOW IR LOSSES
- LOW THERMAL RESISTANCE
- OPTIONAL MIL-STD-883 SCREENING

SCHEMATIC



(CUSTOM SCHEMATIC OPTIONS AVAILABLE)

STANDARD BEND CONFIGURATION **GBN**



(CUSTOM BEND OPTIONS AVAILABLE)

ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL		UNITS
Drain-source Volt.(1)	VDSS	200	Vdc
Drain-Gate Voltage (R_{GS}=1.0MΩ) (1)	VDGR	200	Vdc
Gate-Source Voltage Continuous	VGS	±20	Vdc
Drain Current Continuous (Tc = 25°C)	ID	75	Adc
Drain Current Pulsed(3)	IDM	360	A
Total Power Dissipation	PD	450	W
Power Dissipation Derating > 25°C		3.3	W/°C
Operating & Storage Temp.	TJ/Tsig	-55 TO +150	°C
Thermal Resistance	RthJc	0.3	°C/W
Max.Lead temperature	TL	300	°C

ELECTRICAL CHARACTERISTICS T_C= 25°C (UNLESS OTHERWISE SPECIFIED)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Drain-source Breakdown Volt.	V(BR)DSS	VGS=0V ID=250 µA	200	–	–	V
Gate Threshold Voltage	VGS(TH)	VDS=VGS ID=250 µA	2.0	–	4.0	V
Gate Source Leakage	IGSS	VGS=±20 V	–	–	300	nA
Zero Gate Voltage Drain Current	IDSS	VDS=MAX.RATING VGS=0	–	–	750	µA
		VDS=0.8 MAX.RATING VGS=0 TJ=125°C	–	–	3.0	mA
Static Drain-Source On-State Resistance(1)	RDS(ON)	VGS=10 V ID=48A	–	–	.035	Ω
Forward Trans-Conductance (2)	gfs	VDS ≥ 50 V IDS=48A	13	–	–	S(℧)
Input Capacitance	CISS	VGS=0V VDS=25 V f=1.0 MHz	–	7800	–	pF
Output Capacitance	COSS		–	1950	–	pF
Reverse Transfer Capacitance	CRSS		–	450	–	pF
Turn-On Delay	td(on)	VDD=100V RG=6.2Ω ID=75A RD=1.3Ω (MOSFET switching times are essentially independent of operating temp.)	–	–	30	ns
Rise Time	tr		–	–	180	ns
Turn-Off Delay	td(off)		–	–	100	ns
Fall Time	tf		–	–	120	ns
Total Gate Charge (Gate-Source Plus Gate-Drain)	Qg	VGS=10V, ID=75A VDS=0.8 MAX.RATING (Gate charge is essentially independent of the operating temperature)	–	–	345	nC
Gate-Source Charge	Qgs		–	–	63	nC
Gate-Drain ("Miller") Charge	Qgd		–	–	180	nC

SOURCE-DRAIN DIODE RATINGS & CHARACT. T_C= 25°C (UNLESS OTHERWISE SPECIFIED)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Continuous Source Current (Body Diode)	IS	Modified MOSFET symbol showing the integral reverse P-N junction rectifier (See schematic)	–	–	75	A
Pulse Source Current (Body Diode) (1)	ISM		–	–	300	A
Diode Forward Voltage (2)	VSD	IF=75A, VGS=0V Tc=+25°C	–	–	2.0	V
Reverse Recovery Time	trr	Tc=+25° C IF=75A di/dt=100A/µS	–	–	950	ns
Reverse Recovery Charge	Qrr		–	11.4	–	µC

(1) TJ = 25°C to 150°C.
(2) Pulse test: Pulse Width < 300µS, Duty Cycle < 2%.
(3) Repetitive Rating: Pulse Width limited By Max. Junction Temperature.