

Vishay Siliconix

Automotive Dual N-Channel 20 V (D-S) 175 °C MOSFET

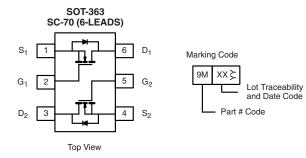
PRODUCT SUMMARY					
V _{DS} (V)	20				
$R_{DS(on)}(\Omega)$ at $V_{GS} = 4.5 \text{ V}$	0.280				
$R_{DS(on)}$ (Ω) at $V_{GS} = 2.5 \text{ V}$	0.360				
$R_{DS(on)}(\Omega)$ at $V_{GS} = 1.8 \text{ V}$	0.450				
I _D (A)	0.8				
Configuration	Dual				

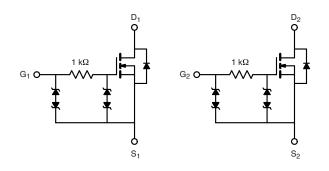
FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- AEC-Q101 Qualified^d
- 100 % R_q Tested
- Typical ESD Protection: 800 V
- Compliant to RoHS Directive 2002/95/EC









ORDERING INFORMATION	
Package	SC-70
Lead (Pb)-free and Halogen-free	SQ1912EEH-T1-GE3

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)						
PARAMETER	SYMBOL	LIMIT	UNIT			
Drain-Source Voltage		V_{DS}	20	.,		
Gate-Source Voltage		V_{GS}	± 12	V		
Continuous Drain Current ^a	T _C = 25 °C	I _D	0.8	A		
	T _C = 125 °C		0.8			
Continuous Source Current (Diode Conduction) ^a		Is	0.8	A		
Pulsed Drain Current ^b		I _{DM}	3			
Maximum Power Dissipation ^b	T _C = 25 °C	- P _D -	1.5	w		
	T _C = 125 °C		0.5			
Operating Junction and Storage Temperatur	T _J , T _{stg}	- 55 to + 175	°C			

THERMAL RESISTANCE RATINGS				
PARAMETER		SYMBOL	LIMIT	UNIT
Junction-to-Ambient	PCB Mount ^c	R_{thJA}	220	°C/W
Junction-to-Foot (Drain)		R_{thJF}	100	C/VV

Notes

- a. Package limited.
- b. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.
- c. When mounted on 1" square PCB (FR-4 material).
- d. Parametric verification ongoing.



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PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT	
Static								
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0$, $I_D = 250 \mu A$		20	-	-	V	
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} =	$V_{DS} = V_{GS}, I_D = 250 \mu A$		0.6	1.5	\ \ \	
Gate-Source Leakage	1	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 4.5 \text{ V}$		-	-	± 1	μΑ	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$		=	-	± 10	mA	
		$V_{GS} = 0 V$	V _{DS} = 20 V	1	-	1		
Zero Gate Voltage Drain Current	I _{DSS}	V _{GS} = 0 V	V _{DS} = 20 V, T _J = 125 °C	-	-	50	μΑ	
		V _{GS} = 0 V	V _{DS} = 20 V, T _J = 175 °C	=.	-	150		
On-State Drain Current ^a	I _{D(on)}	V _{GS} = 4.5 V	$V_{DS} \ge 5 V$	1.5	-	-	Α	
Drain-Source On-State Resistance ^a		V _{GS} = 4.5 V	I _D = 1.2 A	=	0.200	0.280		
		V _{GS} = 4.5 V	I _D = 1.2 A, T _J = 125 °C	-	-	0.423	1	
	R _{DS(on)}	V _{GS} = 4.5 V	I _D = 1.2 A, T _J = 175°C	=.	-	0.510	Ω	
		V _{GS} = 2.5 V	I _D = 1 A	=.	0.261	0.360		
		V _{GS} = 1.8 V	I _D = 0.2 A	=	0.320	0.45		
Forward Transconductance ^b	9 _{fs}	V _{DS} = 10 V, I _D = 1.2 A		-	2.6	-	S	
Dynamic ^b								
Input Capacitance	C _{iss}			=	68	85		
Output Capacitance	Coss	$V_{GS} = 0 V$	0 V V _{DS} = 10 V, f = 1 MHz		23	29	pF	
Reverse Transfer Capacitance	C _{rss}]		=	9	12	1	
Total Gate Charge ^c	Qg			=	1.5	2.3		
Gate-Source Charge ^c	Q_{gs}	$V_{GS} = 4.5 \text{ V}$	$V_{GS} = 4.5 \text{ V}$ $V_{DS} = 10 \text{ V}, I_D = 1.2 \text{ A}$		0.2	-	nC	
Gate-Drain Charge ^c	Q _{gd}			=.	0.3	-		
Gate Resistance	R _g	f = 1 MHz		0.6	1.1	1.6	Ω	
Turn-On Delay Time ^c	t _{d(on)}			=	34	50		
Rise Time ^c	t _r	$V_{DD} = 10 \text{ V}, \text{ R}_L = 20 \Omega$ $I_D \cong 0.5 \text{ A}, \text{ V}_{GEN} = 4.5 \text{ V}, \text{ R}_g = 1 \Omega$		-	51	75	ns	
Turn-Off Delay Time ^c	t _{d(off)}			=.	431	650		
Fall Time ^c	t _f			=	142	215		
Source-Drain Diode Ratings and Char	acteristics ^b							
Pulsed Current ^a	I _{SM}			_	_	3	Α	
T dioda Garront	-OIVI							

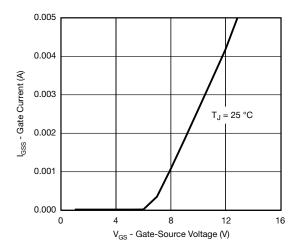
Notes

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

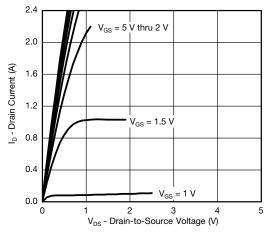
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



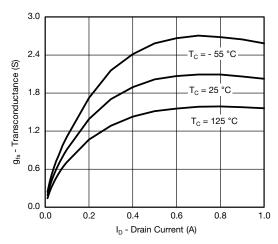
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



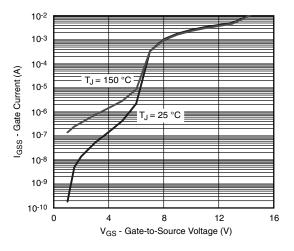
Gate Current vs. Gate-Source Voltage



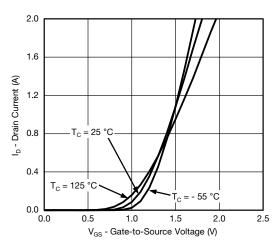
Output Characteristics



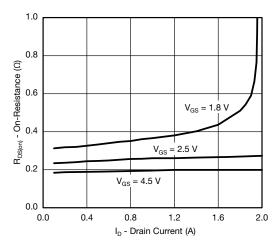
Transconductance



Gate Current vs. Gate-Source Voltage



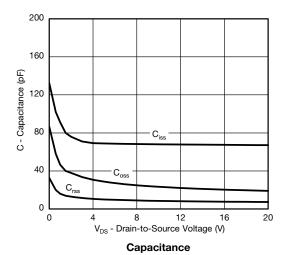
Transfer Characteristics

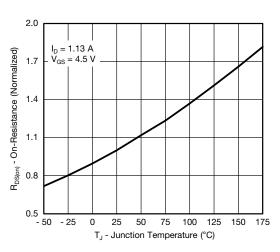


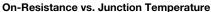
On-Resistance vs. Drain Current

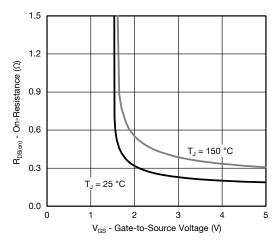


TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

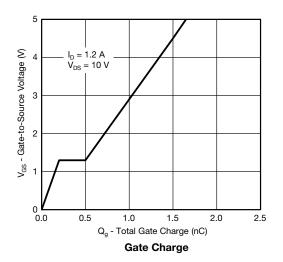


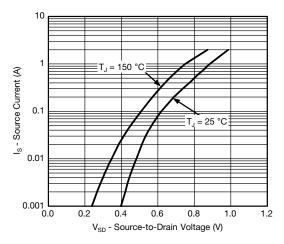




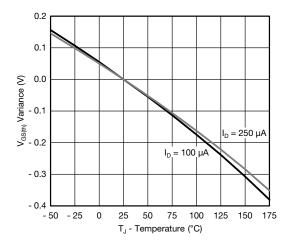


On-Resistance vs. Gate-to-Source Voltage





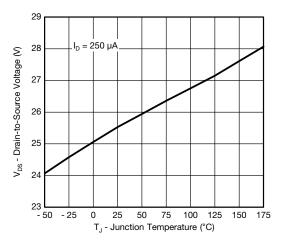
Source Drain Diode Forward Voltage



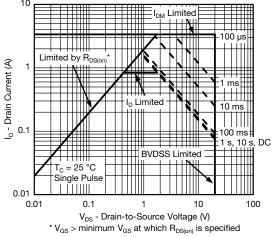
Threshold Voltage

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TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



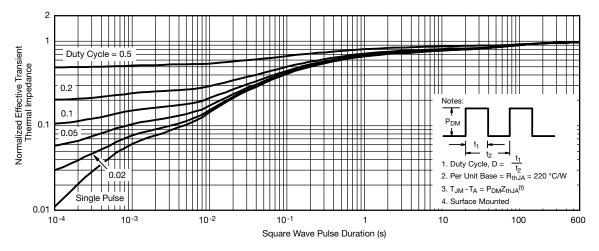
Drain Source Breakdown vs. Junction Temperature



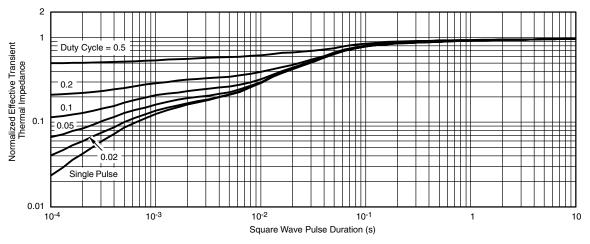
Safe Operating Area



THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

Note

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
 - Normalized Transient Thermal Impedance Junction-to-Foot (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

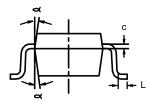
Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg265531.





SC-70: 6-LEADS



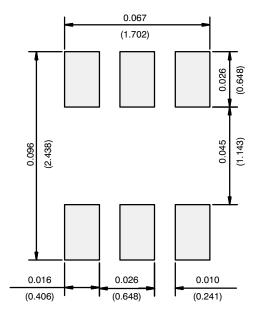


	MILLIMETERS			INCHES		
Dim	Min	Nom	Max	Min	Nom	Max
Α	0.90	-	1.10	0.035	-	0.043
A ₁	_	-	0.10	_	_	0.004
A ₂	0.80	-	1.00	0.031	_	0.039
b	0.15	-	0.30	0.006	_	0.012
С	0.10	-	0.25	0.004	-	0.010
D	1.80	2.00	2.20	0.071	0.079	0.087
Е	1.80	2.10	2.40	0.071	0.083	0.094
E ₁	1.15	1.25	1.35	0.045	0.049	0.053
е	0.65BSC				0.026BSC	;
e ₁	1.20	1.30	1.40	0.047	0.051	0.055
L	0.10	0.20	0.30	0.004	0.008	0.012
۵	7°Nom			7°Nom		
ECN: S-03946—Rev. B, 09-Jul-01						

DWG: 5550



RECOMMENDED MINIMUM PADS FOR SC-70: 6-Lead



Recommended Minimum Pads Dimensions in Inches/(mm)

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