



General Description

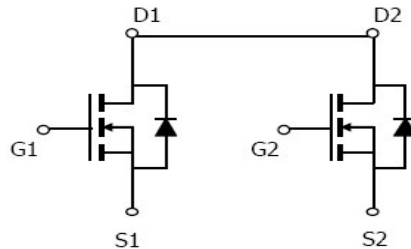
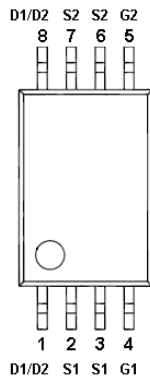
AFN8822, N-Channel enhancement mode MOSFET, uses Advanced Trench Technology to provide excellent $R_{DS(ON)}$, low gate charge.

These devices are particularly suited for low voltage power management, and low in-line power loss are needed in commercial industrial surface mount applications.

Features

- 20V/7.2A, $R_{DS(ON)}=28m\Omega@V_{GS}=4.5V$
- 20V/4.8A, $R_{DS(ON)}=32m\Omega@V_{GS}=2.5V$
- 20V/3.0A, $R_{DS(ON)}=45m\Omega@V_{GS}=1.8V$
- Super high density cell design for extremely low $R_{DS(ON)}$
- TSSOP-8P package design

Pin Description (TSSOP-8P)



Application

- Load Switch
- Portable Equipment
- Battery Powered System

Pin Define

Pin	Symbol	Description
1	D1 / D2	Drain
2	S1	Source
3	S1	Source
4	G1	Gate
5	G2	Gate
6	S2	Source
7	S2	Source
8	D1 / D2	Drain

Ordering Information

Part Ordering No.	Part Marking	Package	Unit	Quantity
AFN8822TS8RG	8822	TSSOP-8P	Tape & Reel	3000 EA

※ A Lot code

※ B Date code

※ AFN8822TS8RG : 13" Tape & Reel ; Pb- Free ; Halogen- Free



Absolute Maximum Ratings

(T_A=25°C Unless otherwise noted)

Parameter	Symbol	Typical	Unit
Drain-Source Voltage	V _{DSS}	20	V
Gate –Source Voltage	V _{GSS}	±12	V
Continuous Drain Current(T _J =150°C)	I _D	T _A =25°C	7.2
		T _A =70°C	4.8
Pulsed Drain Current	I _{DM}	20	A
Continuous Source Current(Diode Conduction)	I _S	1.5	A
Power Dissipation	P _D	T _A =25°C	2.8
		T _A =70°C	1.8
Operating Junction Temperature	T _J	150	°C
Storage Temperature Range	T _{STG}	-55/150	°C
Thermal Resistance-Junction to Ambient	R _{θJA}	62.5	°C/W

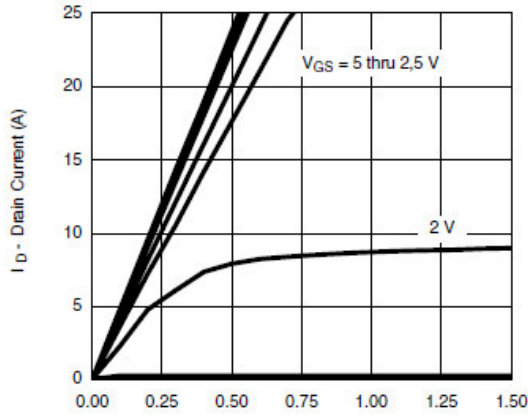
Electrical Characteristics

(T_A=25°C Unless otherwise noted)

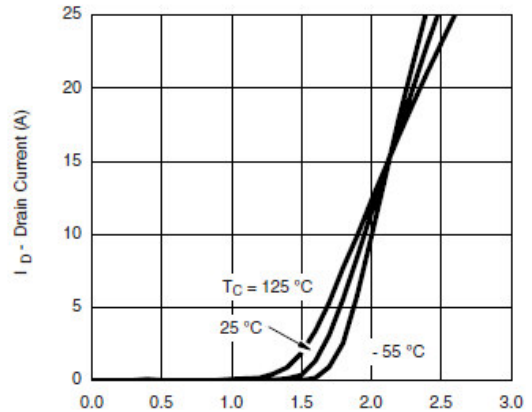
Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =250uA	20			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250uA	0.4		0.8	
Gate Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±12V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =16V, V _{GS} =0V			1	uA
		V _{DS} =16V, V _{GS} =0V T _J =85°C			30	
On-State Drain Current	I _{D(on)}	V _{DS} ≥ 5V, V _{GS} =4.5V	10			A
Drain-Source On-Resistance	R _{DS(on)}	V _{GS} = 4.5V, I _D =7.2A		24	28	mΩ
		V _{GS} = 2.5V, I _D =4.8A		27	32	
		V _{GS} = 1.8V, I _D =3.0A		36	45	
Forward Transconductance	g _{FS}	V _{DS} =5V, I _D =7A		25		S
Diode Forward Voltage	V _{SD}	I _S =1.6A, V _{GS} =0V		0.7	1.2	V
Dynamic						
Total Gate Charge	Q _g	V _{DS} =10V, V _{GS} =4.5V I _D ≧7.0A		650		pC
Gate-Source Charge	Q _{gs}			200		
Gate-Drain Charge	Q _{gd}			180		
Input Capacitance	C _{iss}	V _{DS} =20V, V _{GS} =0V f=1MHz		700		pF
Output Capacitance	C _{oss}			75		
Reverse Transfer Capacitance	C _{rss}			45		
Turn-On Time	t _{d(on)}	V _{DD} =10V, R _L =1.4Ω I _D ≧1.0A, V _{GEN} =4.5V R _G =3Ω		8	12	ns
	t _r			12	20	
Turn-Off Time	t _{d(off)}			32	40	
	t _f			10	15	



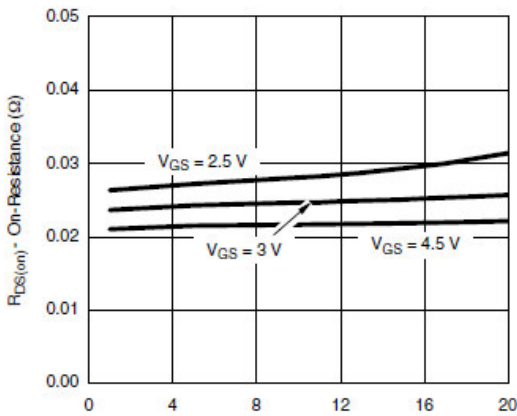
Typical Characteristics



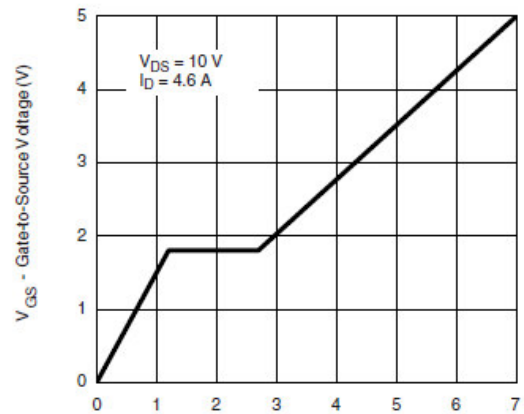
Output Characteristics



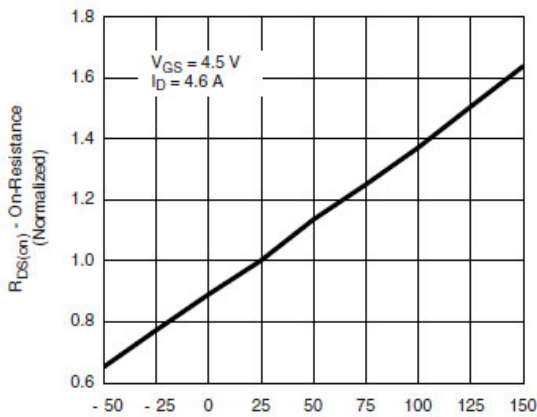
Transfer Characteristics



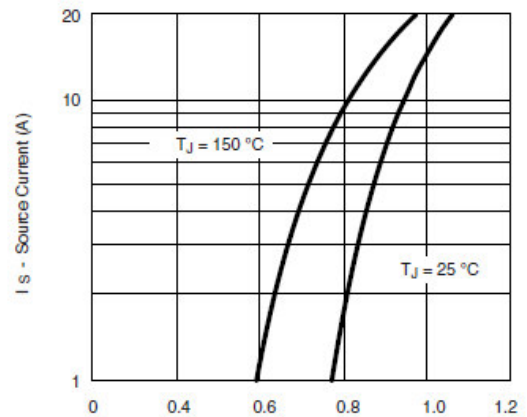
On-Resistance vs. Drain Current



Gate Charge



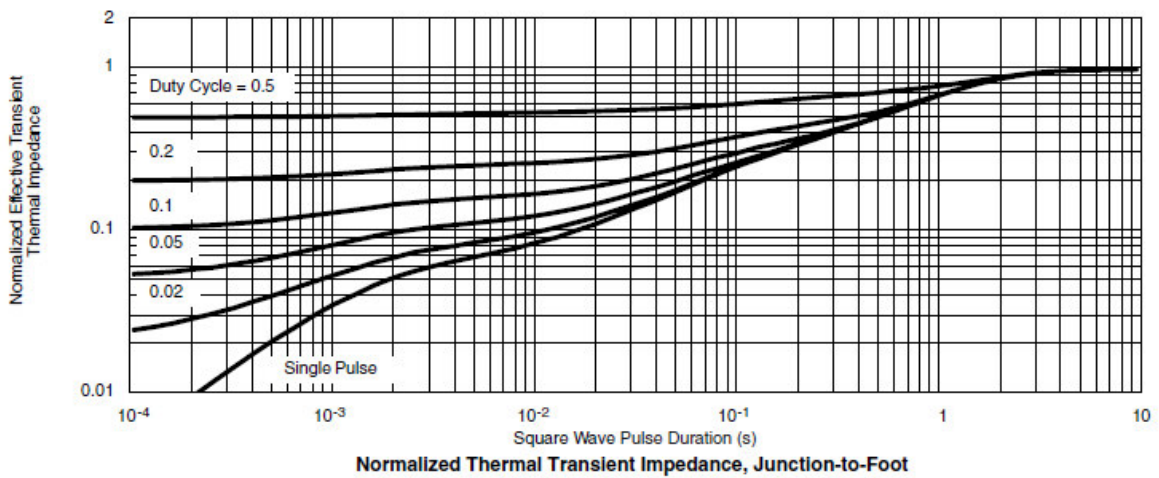
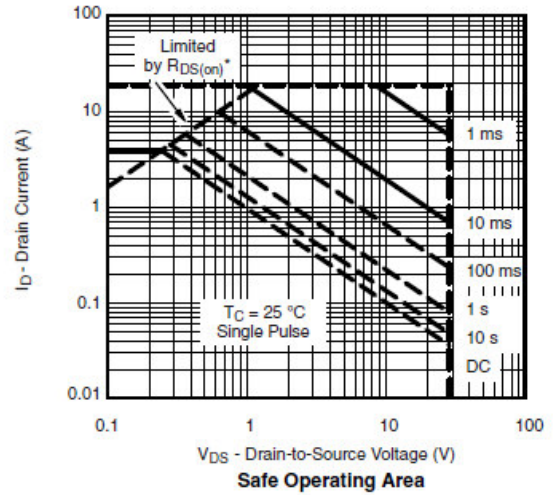
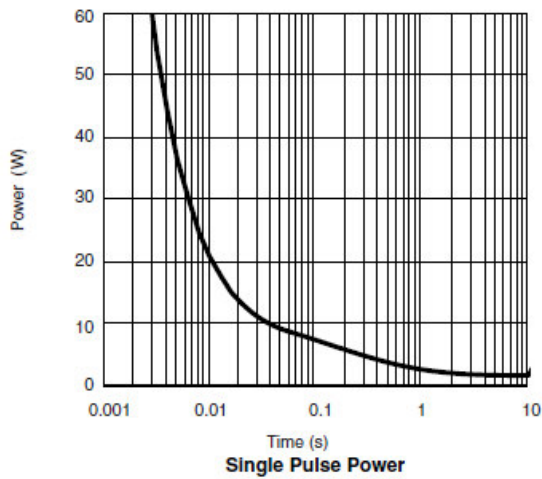
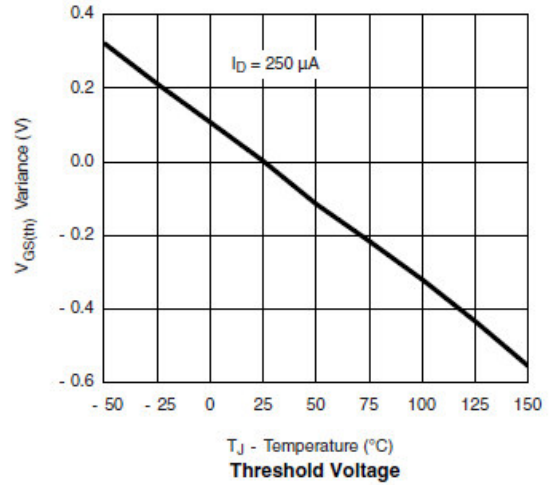
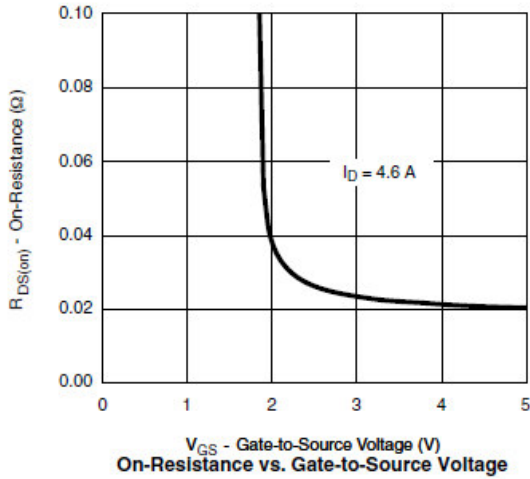
On-Resistance vs. Junction Temperature



Source-Drain Diode Forward Voltage



Typical Characteristics



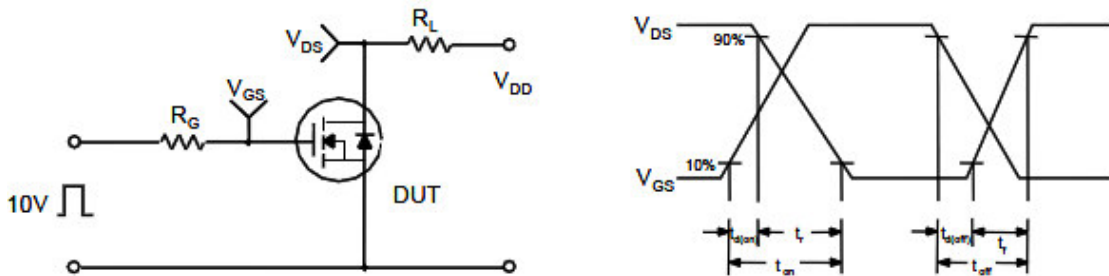


Typical Characteristics

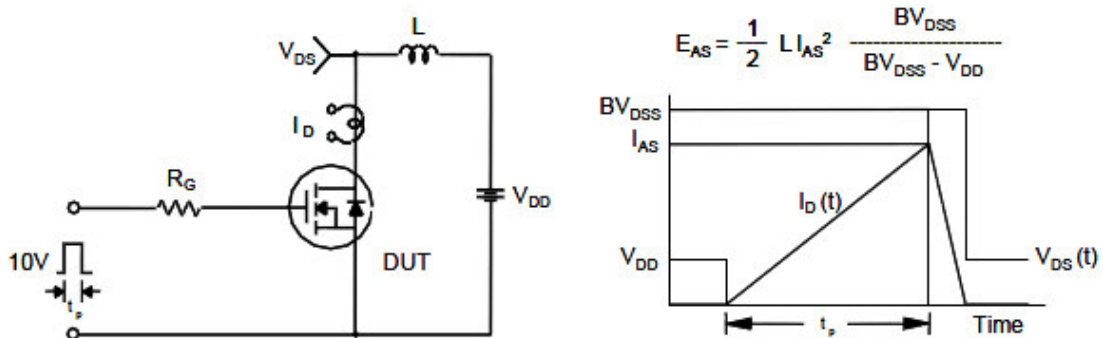
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms

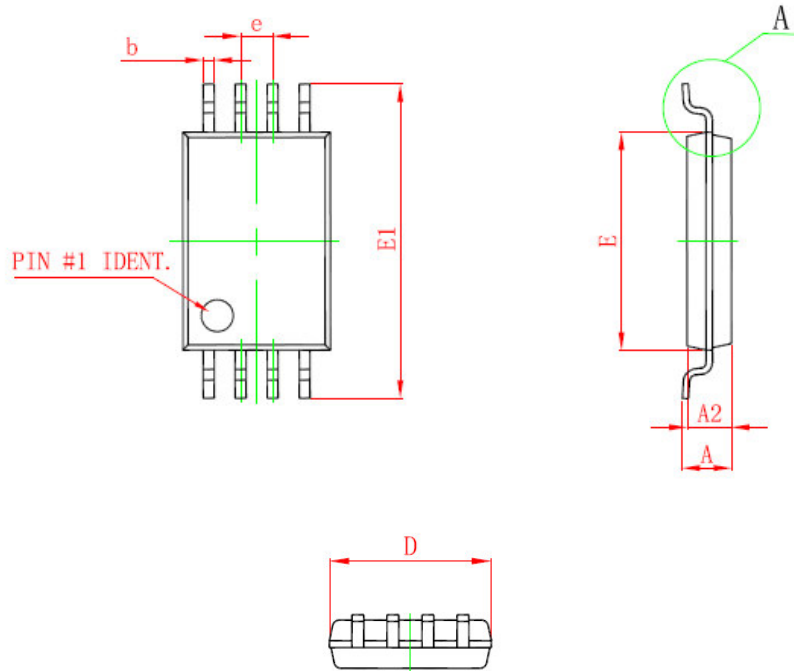


Unclamped Inductive Switching Test Circuit & Waveforms





Package Information (TSSOP-8P)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
D	2.900	3.100	0.114	0.122
E	4.300	4.500	0.169	0.177
b	0.190	0.300	0.007	0.012
c	0.090	0.200	0.004	0.008
E1	6.250	6.550	0.246	0.258
A		1.100		0.043
A2	0.800	1.000	0.031	0.039
A1	0.020	0.150	0.001	0.006
e	0.65 (BSC)		0.026 (BSC)	
L	0.500	0.700	0.020	0.028
H	0.25 (TYP)		0.01 (TYP)	
θ	1°	7°	1°	7°

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